

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	216878	(independent\$2 or separat\$2 or (stand adj1 alone)) near3 (process\$2 or task\$1 or operation\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:00
L2	10913	(execut\$3 or run\$4 or complet\$3) near5 1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:00
L3	106397	(simultaneous\$2 or concurrent\$2 or (in adj1 parrallel)) near4 (process\$3 or microprocess\$3 or CPU\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:04
L4	16274	((field adj programmable adj gate adj array) or FPGA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:06
L5	10913	1 with 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:06
L6	8	5 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:13
L7	0	5 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:07
L10	46	3 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:23
L11	0	2 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:23
L12	8	2 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:24

L13	184	1 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:24
L14	75	1 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:25
L15	4816	(implement\$3 or design\$3 or us\$3 or hav\$3) near3 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:46
L16	726	15 with (process\$2 or coprocess\$2 or (co adj1 process\$2))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:48
L17	0	16 same 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:49
L18	0	16 same 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:49
L19	31	16 same 1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:56
L20	251	15 with ((digital adj signal adj process\$3) or DSP\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:57
L21	0	20 same 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:57
L22	0	20 same 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:57

L23	3	20 same 1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 08:58
L24	28	15 with ((signal adj distribut\$3 adj matri\$3) or (switch\$3 adj matri\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:04
L25	764	(signal adj2 condition\$3 adj2 filter\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:04
L26	0	15 with 25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:05
L27	0	4 with 25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:05
L28	1455	(cross adj1 bar adj switch\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:06
L29	6	15 with 28	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:07
L31	181551	((analog-to-digital) or (A adj1 D)) adj conver\$3) or ADC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:20
L32	99605	((digital-to-analog) or (D adj1 A)) adj conver\$3) or DAC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:20
L33	48	(31 or 32) with 15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:27

L34	510387	(multi adj chip module) or MCM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:30
L35	492	(instrument adj controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:31
L36	60	(smart adj control adj system\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:31
L37	128	(space adj based) near3 (device\$1 or module\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:32
L38	41370	(application adj specific adj integrated adj circuit) or ASIC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:33
L39	172	(tightly near3 coupl\$3) with 34	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:48
L40	1	39 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:34
L41	16	35 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:38
L42	1	35 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:35
L43	0	36 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:38

L44	1	37 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:38
L45	6123	38 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:44
L46	1126	38 same 15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:39
L47	924	38 with 15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:39
L48	26	47 with 34	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:40
L49	0	47 with 39	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:40
L50	0	37 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:46
L51	1	37 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:47
L52	1	39 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:48
L53	8281	34 near3 design	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:50

L54	63	53 same 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:50
L55	35	53 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:58
L56	1658	(discontinuous\$2 or irregular\$2) near4 (power or (power adj operation))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 09:59
L57	780	((zero adj power) or unpower\$2) near2 (state\$1 or mode\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:00
L58	0	56 with 57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:00
L59	0	56 same 57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:01
L60	28645	automatic\$4 near4 (activat\$3 or deactivat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:01
L61	899	(processor\$1 or microprocessor\$1 or CPU\$1) with 60	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:02
L62	92404	(external or outside) near2 signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:03
L63	1	61 with 62	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:04

L64	0	61 same 56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:05
L65	0	61 same 57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:05
L66	0	60 same 56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:05
L67	1	60 same 57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:06
L68	0	37 same 56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:07
L69	0	37 same 57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:07
L70	1	35 same 56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:08
L71	0	35 same 57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 10:08