

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	22588	(independent\$2 or (stand adj alone)) near4 (execut\$3 or run\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:14
S2	4690	S1 with (process\$2 or task\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 13:55
S3	16274	(field adj programmable adj gate adj array) or FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 13:56
S4	10	S2 same S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 13:56
S5	739	instrument\$5 adj controller\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:09
S6	16274	(field adj programmable adj gate adj array) or FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:09
S7	2	S5 with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:12
S8	56135	microcontroller\$1 or (micro adj controller\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:13
S9	1623	S8 with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:13
S10	22588	(independent\$2 or (stand adj alone)) near4 (execut\$3 or run\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:13

S11	4690	S10 with (process\$2 or task\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:13
S12	244720	(processor\$1 or microprocessor\$1 or CPU\$1) near4 (coupl\$3 or connect\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:16
S13	386	S12 with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:16
S14	21	S9 same S13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:19
S15	6358	(implement\$3 or (based adj on)) with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:20
S16	1462	((digital adj signal adj process\$3) or DSP\$1) with S15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:22
S17	0	S16 same S10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:23
S18	169353	((analog adj to adj digital) or (A adj D)) adj convert\$3) or ADC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:27
S19	92482	((digital adj to adj analog) or (D adj A)) adj convert\$3) or DAC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:28
S21	26215	(S18 or S19) near4 (coupl\$3 or connect\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:31

S22	44	S21 with S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:34
S23	689	signal adj condition\$3 adj2 filter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:34
S24	1	S23 same S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:35
S25	2813	(monitor\$3 or control\$4) near5 S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:36
S26	164	S25 same (S18 or S19)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:38
S27	63	S25 with (S18 or S19)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 14:38
S28	22285	motor adj controller\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:15
S29	16274	(field adj programmable adj gate adj array) or FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:15
S30	6358	(implement\$3 or (based adj on)) with S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:15
S31	4	S28 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:18

S32	134091	(simultaneous\$2 or concurrent\$2 or (in adj paralell)) near5 (process\$3 or execut\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:25
S33	147347	(simultaneous\$2 or concurrent\$2 or (in adj paralell)) near5 (process\$3 or execut\$3 or run\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:27
S34	2601175	(independent\$2 or autonomous\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:32
S35	62	S34 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:30
S36	171069	(process\$2 or task\$1) near3 execut\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:34
S37	3958	S34 with S36	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:34
S38	0	S37 with S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:34
S39	2	S37 same S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:46
S40	9119	coprocessor or (co adj processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:47
S41	32	S40 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:51

S42	526	controller with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:53
S43	17705	(multi adj chip adj module\$1) or MCM\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:54
S44	9	S43 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:56
S45	1	S33 with S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/22 15:59