OIPE JC107 JUL 01 2005 PATENT & TRADEMARK OFFICE

7-5-05

IFW 2667

In re application of:	Enric Musoll Mario D. Nemirovsky Thomas Y. Yeh
Serial No.:	09/924755
Filed:	08/07/01
Docket:	MIPS.0176-00-US
For:	METHOD AND APPARATUS FOR PREVENTING UNDESIRABLE PACKET DOWNLOAD WITH PENDING READ/WRITE OPERATIONS IN DATA PACKET PROCESSING

INFORMATION DISCLOSURE STATEMENT SUBMITTED UNDER 37 CFR 1.97(D) AND 1.97 (E)(2)

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the Examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims and return an initialed copy of each form to the undersigned.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 C.F.R. § 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR § 1.56(b) or is available as a reference under 35 U.S.C. § 102 *et seq.* Applicant reserves the right to swear behind or otherwise disprove the allege "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98, and the Manual of Patent Examining Procedures § 609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

A copy of each document is enclosed. Some of the documents may have markings thereon. No significance is intended to be attached to the markings.

Also attached hereto is a check in the amount of **$180.00** for the fee set forth in § 1.17(p).

Respectfully submitted,

James W. Huffman
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Date: 6-17-05

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Date of Deposit 7/1/05. I hereby certify that this paper is being deposited with the U.S. Postal Service Express Mail Post Office to Addressee Service under 37 C.F.R. §1.10 on the date shown above and is addressed to the U.S. Commissioner of Patents and Trademarks, Washington, D.C. 20231.
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known	
Application Number	09/924755
Filing Date	08/07/01
First Named Inventor	Enric Musoll
Art Unit	2667
Examiner Name	KIZOU, HASSAN
Attorney Docket Number	MIPS.0176-00-US

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Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear
		4200927	04-29-1980	Huges et al.	
		5708814	1-13-1998	Short et al.	
		5724565	03-03-1998	Dubey et al.	
		5784649	07-21-1998	Begur et al.	
		6151644	11-21-2000	Chun-Chu Archie Wu	
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		6523109	02-18-2003	Stephan G. Meier	
		6640248	10-28-2003	Jacob W. Jorgensen	
		6625808	09-23-2003	David R. Tarditi	
		6738371	05-18-2004	Lawrence Ayres	
		6738378	05-18-2004	Tuck, III et al.	
		6813268	11-02-2004	Kalkunte et al.	
		6535905	03-18-2003	Kalafatis et al.	
		20010004755	06-21-2001	Levy et al.	
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		20010052053	12-13-2001	Nemirovsky et al.	
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		20020016883	02-07-2002	Musoll et al.	
		20020049964	04-25-2002	Takayama et al.	
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		20040213251	10-28-2004	Tran et al.	
		20040172504	09-02-2004	Balazich et al.	
		20040015598	01-22-2004	Jin-Fun, Lin	
		09/608,750	NA	Nemirovsky et al.	
		09/602,279	NA	Nemirovsky et al.	

Examiner Signature		Date Considered	

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FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No.[1]	Foreign Patent Document: Country Code[3] Number[4] Kind Code[5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T[6]
	AA	WO 03/005645A2	06-25-2002	Sampath et al.		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
	BA	MELVIN ET AL., "Extended Instruction Set for a Packet Processing Applications," July 5, 2001, Disclosure Document #496559, USPTO	
	BB	MUSOLL ET AL., "Hardware Algorithm for Allocating and De-Allocating Consecutive Blocks of Memory," April 3, 2001, Disclosure Document #491557, USPTO	
	BC	MUSOLL ET AL., "Mechanism to Overflow Packets to a Software Controlled Memory When They Do Not Fit into a Hardware Controlled Memeory,", July 3, 2001, Disclosure Document #496391, USPTO	
	BD	MUSOLL ET AL., "Mechanism for Allowing a Limited Packet Head and/or Tail Growth Without Moving the Packet to a Different Memeory Location," April 16, 2001, Disclosure Document #492429, USPTO	
	BE	MUSOLL ET AL., "Mechanism to Activate a Context When No Stream is Running in a Multi-Streaming Processing Core,: April 16, 2001, Disclosure Document #492431, USPTO	
	BF	MUSOLL, ENRIC, "Functional Validation of a Packet Management Unit," May 18, 2001, Disclosure Document #429011, USPTO	
	BG	SAMPATH ET AL., "Mechanism to Un-speculatively Pre-fetch Instructions from the Thread Associated to a Packet," April 2, 2001, Disclosure Document #491527, USPTO	
	BH	YAMAMOTO, WAYNE. An Analysis of Multistreamed, Superscalar Processor Architectures. University of California Santa Barbara Dissertation. December 1995. Santa Barbara, US.	
	BI	YAMAMOTO ET AL. "Increasing Superscalar Performance Through Multistreaming." Parallel Architectures and Compilation Techniques (PACT '95). 1995	

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Attorney Docket Number	MIPS.0176-00-US

Sheet	3	of	4

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	BJ	The PowerPC Architecture: A Specification for a New Family of RISC Processors. 2nd Ed. May 1994. pp. 70-72. Morgan Kaufmann. San Francisco, US.	
	BK	MC68020 32-Bit Microprocessor User's Manual. 3rd Ed.. 1989. pp. 3-125, 3-126, and 3-127. Prentice Hall, NJ, US.	
	BL	POTEL, M. J. "Real-Time Playback in Animation Systems." Proceedings of the 4th Annual Conference on Computer Graphics and Interactive Techniques. 1977. pp. 72-77. San Jose, CA, US.	
	BM	ARM Architecture Reference Manual. 1996. pp. 3-41, 3-42, 3-43, 3-67, and 3-68. Prentice Hall, NJ, US.	
	BN	ESA/390 Principles of Operation. IBM Online Publications Center Reference Number SA22-7201-08. Table of Contents and paras. 7.5.31 and 7.5.70. IBM Corporation. Boulder, CO, US.	
	BO	MC88110 Second Generation RISC Microprocessor User's Manual. 1991. pp. 10-66, 10-67, and 10-71. Motorola, Inc.	
	BP	DIEFENDORFF ET AL. "Organization of the Motorola 88110 Superscalar RISC Microprocessor." IEEE Journal of Microelectronics. April 1992. pp. 40-63. Vol. 12, No. 2. IEEE. New York, NY, US.	
	BQ	KANE, GERRY. PA-RISC 2.0 Architecture. 1996, pp. 7-106 and 7-107. Prentice Hall. NJ, US.	
	BR	DIEFENDORFF ET AL. "AltiVec Extension to PowerPC Accelerates Media Processing." IEEE Journal of Microelectronics. Vol. 20, No. 2 (2000): pp. 85-95	
	BS	GRUNEWALD ET AL. "Towards Extremely Fast Context Switching in a Block Multithreaded Processor." Proceedings of EUROMICRO 22, 1996. pp. 592-599	

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	BT	BRADFORD ET AL. "Efficient Synchronization for Multithreaded Processors." Workshop on Multithreaded Execution, Architecture, and Compilation. Jan-Feb 1998. pp. 1-4	
	BU	PAI ET AL. "An Evaluation of Memory Consistency Models for Shared-Memory Systems with ILP Processors." Proceedings of ASPLOS-VII, Oct. 1996: pp. 12-23, ACM, Inc.	
	BV	YOAZ ET AL. "Speculation Techniques for Improving Load Related Instruction Scheduling." 1999. Pages 42-53, IEEE.	
	BW	KESSLER, R. E. "The Alpha 21264 Microprocessor: Out-of-Order Execution at 600 MHz." August 1998.	
	BX	DONALSON ET AL. "DISC: Dynamic Instruction Stream Computer, An Evaluation of Performance." 26th Hawaii Conference on Systems Sciences. Vol. 1. 1993. Pages 448-456.	
	BY	NEMIROVSKY ET AL. "DISC: Dynamic Instruction Stream Computer." ACM. 1991. Pages 163-171	
	BZ	UNGERER ET AL. A Survey of Processors with Explicit Multithreading. ACM Computing Surveys, Vol., 35, No. 1. March 2003. Pages 29-63.	

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