IN THE UNITED STATES DISTRICT COURT
FOR THE WESTERN DISTRICT OF TEXAS
AUSTIN DIVISION

SLINGSHOT PRINTING LLC,	§	
Plaintiff,	§	
	§	
v.	§	**1-20-CV-00186-ADA**
	§	**1-20-CV-00187-ADA**
HP INC.,	§	
Defendant.	§	
	§	

CLAIM CONSTRUCTION ORDER

The Court provided its preliminary constructions on October 1, 2020. The Court held a claim construction hearing on October 2, 2020, during which the Court heard argument on the claim term: "each of the driver and logic transistors comprise a high density array of MOS transistors" ('012 patent, claim 1). Case No. 1-20-CV-00186-ADA, ECF No. 76.

At the claim construction hearing, the Court requested supplemental briefing on the term "each of the driver and logic transistors comprise a high density array of MOS transistors" ('012 patent, claim 1). *Id.* On October 14, 2020, after conclusion of the supplemental briefing, the Court held another hearing on the term. Case No. 1-20-CV-00187-ADA, ECF No. 80.

After careful consideration of the parties' briefs, oral argument, and the applicable law, the Court enters its final constructions for each terms as shown below.

Claim Terms for Case No. 1:20-cv-00186

Claim Term	Court
"ink tank configuration instructions, the ink tank configuration instructions identifying a total number of ink tanks that are installed in a printing system" "ink tank configuration instructions, the ink tank configuration instructions identifying a total number of ink wells that are installed in a printing system"	Plain and ordinary meaning

’498 patent, claim 1 ’618 patent, claim 1	
“a wall portion” ’088 patent, claims 8, 20	Plain and ordinary meaning
“perimetrical lip” ’088 patent, claims 8, 9, 20, 22	a lip extending around a perimeter of the printhead maintenance cap
“perimetrical valley” ’088 patent, claims 8, 20	a valley extending around a perimeter of the printhead maintenance cap
“the portion of the print zone receiving medium . . . being at least partially exposed to an atmosphere having a temperature” ’162 patent, claim 1	Plain and ordinary meaning
“a temperature of up to about 16º Celsius above the temperature of the atmosphere to which the print zone is at least partially exposed” “a temperature of between about 4º to about 16º Celsius above the temperature of the atmosphere to which the print zone is at least partially connected” ’162 patent, claims 1, 5	Plain and ordinary meaning
“button” ’936 patent, claim 8	Plain and ordinary meaning
“the carrier moves along a carrier frame and during printhead and ink tank replacement or installation is configured to stop at either a first or second of two different positions along the carrier frame” ’712 patent, claim 9	Plain and ordinary meaning

Claim Terms for Case No. 1:20-cv-00187

Claim Term	Court
"at least one logic array" "a first logic array" '629 patent, claims 1, 8	at least one group of logic cells a first group of logic cells
"allows the first ink via and second ink via to be simultaneously controlled by" '629 patent, claims 1, 8	Plain and ordinary meaning
"substantially between the first heater array and the second heater array" "substantially between the first heater array and the second heater array" '629 patent, claims 1, 8	Plain and ordinary meaning
"wherein each of the driver and logic transistors comprise a high density array of MOS transistors" '012 patent, claim 1	The Court finds that there was a drafting error and corrects the term as follows: "wherein the driver and logic transistors comprise a high density array of MOS transistors"
"a columnar array of logic circuits disposed adjacent the columnar array of power transistors and connected through a polysilicon conductor layer to the power transistors" '341 patent, claim 1	Plain and ordinary meaning
"a first metal resistive layer on the substrate" '575 patent, claim 1	Plain and ordinary meaning
"the second metal conductive layer overlying the first portion of the resistive layer is substantially devoid of the passivation layer" '575 patent, claim 1	Plain and ordinary meaning
"the second metal conductive layer overlying the first portion of the resistive layer is substantially devoid of the cavitation layer" '575 patent, claim 1	Plain and ordinary meaning
"a deposited and etched dielectric layer overlying the first portion of the resistive layer, the dielectric layer having a second thickness" '575 patent, claim 1	Plain and ordinary meaning

"heater resistor" '563 patent, claims 1,5,11 '246A patent, claims 1, 4-6, 11	Plain and ordinary meaning
"non-contiguous hybrid arrangement" '629 patent, claims 1, 8	An arrangement in which the first set of logic cells are interleaved or interlaced with the second set of logic cells
"ink via" '629 patent, claims 1-4, 8, 9, 14	A path to fluidly connect the supply of ink to one or more heaters

IT IS SO ORDERED.

SIGNED this 14th day of January, 2021.

ALAN D ALBRIGHT
UNITED STATES DISTRICT JUDGE