

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

SONY GROUP CORPORATION
Petitioner,

v.

VISIONX TECHNOLOGIES, LLC,
Patent Owner.

Case No. IPR2023-00389
Patent No. 8,035,143

**NOTICE OF FILING TRANSCRIPT OF MAY 24, 2023 CONFERENCE
CALL AND *SOTERA* STIPULATION**

LISTING OF EXHIBITS

Exhibit	Description
1001	U.S. Patent No. 8,035,143 (“the ’143 patent”)
1002	Prosecution History of U.S. Patent No. 8,035,143
1003	Declaration of Michael Guidash (“Guidash”)
1004	Curriculum Vitae of Michael Guidash
1005	U.S. Patent Publication No. US 2006/0094151 A1 (“Sumi”)
1006	Certified English Translation of Japanese Publication No. JP 2006-032497 A (“Yanagida-1”)
1007	Certified English Translation of Japanese Publication No. JP 2006-339566 A (“Yanagida-2”)
1008	Certified English Translation of Japanese Patent Publication No. JP 2005-353996 A (“Shiraiwa”)
1009	U.S. Patent No. 7,875,948 (“Hynecek”)
1010	“Sony develops back-illuminated CMOS image sensor, realizing high picture quality, nearly twofold sensitivity(*1) and low noise”, Sony Group Portal - Sony Global Headquarters (June 11, 2008), https://www.sony.com/en/SonyInfo/News/Press/200806/08-069E/
1011	U.S. Patent Publication No. US 2006/0076590 A1 (“Pain-1”)
1012	U.S. Patent Publication No. US 2007/0117254 A1 (“Pain-2”)
1013	Jean-Luc Leray et al., An Overview of Buried Oxides on Silicon: New Processes and Radiation Effects. Journal de Physique III, EDP Sciences, 1996, 6 (12), 1625-1646 (“Leray”)
1014	Michael Quirk and Julian Serda, Semiconductor Manufacturing Technology, 257-333 (2001) (“Quirk”)
1015	Metallization by Chemical Vapor Deposition of W and Cu, 1 in Handbook of 3D integration: Technology and applications of 3D Integrated Circuits (Philip Garrou, Christopher Bower & Peter Ramm, eds., 2008) (“Garrou”)
1016	British Library Public Availability Date Authentication Letter
1017	Garrou Vol. 1 Public Availability Date Authentication
1018	Declaration of Mary Bolin
1019	H. C.M Knoops et al., “Deposition of TiN and TaN by Remote Plasma ALD for Cu and Li Diffusion Barrier Applications” Journal of The Electrochemical Society (Oct. 10, 2008)
1020	Japanese-language Publication of Japanese Publication No. JP 2006-032497 A

1021	Japanese-language Publication of Japanese Publication No. JP 2006-339566 A
1022	Japanese-language Publication of Japanese Publication No. JP 2005-353996 A
1023	U.S. Patent Publication No. US 2005/0179053 A1 (“Ezaki”)
1024	McGraw-Hill, McGraw-Hill Dictionary of Scientific and Technical Terms (6th ed. 2003) (“McGraw-Hill”)
1025	Electrical Engineering Dictionary (Phillip A. Laplante et al. eds., 2000) (“Laplante”)
1026	Robert S. Patti, “Three-Dimensional Integrated Circuits and the Future of the System-on-Chip Designs,” Proceedings of the IEEE, vol. 94, no. 6, 1214-1224, 2006 (“Patti”)
1027	Transcript of May 24, 2023 Conference Call
1028	Letter from M. Rader to A. Alavi Re: <i>Sotera</i> Stipulations (May 16, 2023)

Pursuant to 37 CFR § 42.6(e)(4)(ii), and the Board's authorization during the May 24, 2023 Conference Call, Petitioner Sony Group Corporation herewith submits the following Exhibits:

<u>Exhibit</u>	<u>Description</u>
1027	Transcript of May 24, 2023 Conference Call
1028	Letter from M. Rader to A. Alavi Re: <i>Sotera</i> Stipulations (May 16, 2023)

Respectfully submitted,
Sony Group Corporation

Date: May 25, 2023

/Michael N. Rader/

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CERTIFICATE OF SERVICE UNDER 37 C.F.R. § 42.6 (E)(4)

I certify that on May 25, 2023, I will cause a copy of the foregoing document, including any exhibits filed therewith, to be served via electronic mail, as previously consented to by Patent Owner, upon the following:

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Masood Anjom
Steven Jugle

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