

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

SAMSUNG ELECTRONICS CO., LTD., MICRON TECHNOLOGY, INC.,
MICRON SEMICONDUCTOR PRODUCTS, INC., and
MICRON TECHNOLOGY TEXAS LLC,¹
Petitioners,

v.

NETLIST, INC.,
Patent Owner.

Case No. IPR2023-00454
Patent No. 11,093,417

PATENT OWNER'S UPDATED EXHIBIT LIST

¹ Micron Technology, Inc., Micron Semiconductor Products, Inc., and Micron Technology Texas LLC filed a motion for joinder and a petition in IPR2023-01141 and have been joined as petitioners in this proceeding.

EXHIBIT LIST

Exhibit No.	Document
EX2001	Crisp, R., “Direct Rambus Technology: The New Main Memory Standard,” <i>IEEE Micro</i> , pp. 18-28 (1997)
EX2002	Datasheet for Samsung’s RIMM MR16R1624 (Wolfe Deposition Exhibit 2100)
EX2003	Deposition Transcript of Dr. Andrew Wolfe from IPR2022-00615 (an updated exhibit was uploaded on April 17, 2024)
EX2004	Samsung Datasheet on Rambus Direct RDRAM (Dec. 2001)
EX2005	Deposition Transcript of SungJoo Park in <i>Netlist, Inc. v. Samsung Electronics Co., Ltd.</i> , 2:21-cv-00463-JRG (E.D. Tex. November 23, 2022) (excerpt)
EX2006	Ex. 7 to Park Depo
EX2007	<i>Frequently Asked Questions</i> , What is the difference between a "bank" and a "rank?", Micron, https://web.archive.org/web/20141231144416/https://www.micron.com/support/faqs (last accessed Mar. 7, 2023).
EX2008	Reserved
EX2009	“XDR Architecture “ by Rambus (last modified December 2005), downloaded from https://web.archive.org/web/20110724044154/http://www.rambus.com/assets/documents/products/dl_0161_v0_8.pdf
EX2010	IDT, “DDR4 LRDIMMs Unprecedented Memory Bandwidth on Samsung DDR4 LRDIMM Enabled by IDT’s Register and Data Buffer,” downloaded from https://www.renesas.com/us/en/document/whp/ddr4-lrdimms-both-memory-capacity-and-speed

EX2011	“HyperCloud HCDIMM: Scaling the High Density Memory Cliff,” July 14, 2012, downloaded from https://web.archive.org/web/20120915083048/http://www.netlist.com/media/blog/hypercloud-memory-scaling-the-high-density-memory-cliff/
EX2012	<i>What is a Memory Rank</i> , Crucial: Micron, https://www.crucial.com/support/articles-faq-memory/what-is-a-memory-rank (last accessed Mar. 7, 2023)
EX2013	<i>Lecture 15: DRAM Main Memory Systems</i> , University of Utah, https://my.eng.utah.edu/~cs6810/pres/11-6810-15.pdf
EX2114	“Configuring and using DDR3 memory with HP ProLiant Gen8 Servers: Best Practice Guidelines for ProLiant Servers with Intel Xeon Processors” by HP, downloaded from https://web.archive.org/web/20121023195754/http://h20000.www2.hp.com/bc/docs/support/SupportManual/c03293145/c03293145.pdf
EX2015	Ganesh, B. et al., <i>Fully-Buffered DIMM Memory Architectures: Understanding Mechanisms, Overheads and Scaling</i> , HPCA (2007)
EX2116	Datasheet for Micron DDR2 SDRAM FBDIMM MT36HTF51272FD
EX2017	U.S. Patent No. 7,356,639 (“Perego-639”)
EX2018	Redline Comparison of EX1035 (U.S. Patent No. 7,363,422 to Perego) With Related U.S. Patent No. 7,356,639
EX2019	US District Court – Judicial Caseload Profile (as of December 31, 2022)
EX2020	JESD79-2B
EX2021	Amended Docket Control Order in <i>Netlist, Inc. v. Samsung Electronics Co. Ltd.</i> , 2:22-cv-00293-JRG (E.D. Tex. Feb. 1, 2023)

EX2022	Cover pleading of Samsung's invalidity contentions in <i>Netlist, Inc. v. Samsung Electronics Co. Ltd.</i> , 2:22-cv-00293-JRG (E.D. Tex. April 13, 2023)
EX2023	IPR2023-00203, Paper 8
EX2024	Declaration of Steven Przybylski, Ph.D.
EX2025	Rambus®, Advance Information, 64/72-Mbit (256Kx16/18x16d) Direct RDRAMDirect™ Data Sheet, Modified Mar 12, 1998
EX2026	Direct Rambus™ 1.6GB/sec memory Technology Disclosure, Oct. 15, 1997
EX2027	Direct Rambus™ RIMM™ Module Specification, Version 1.0
EX2028	Samsung Electronics, 256 Mbit XDR DRAM (F-die), 2M x 16(/8/4) bit x 8s Banks, Version 1.0, Jan. 2005
EX2029	Przybylski, Steven A., "NEW DRAM TECHNOLOGIES, A Comprehensive Analysis of the New Architectures," Second Edition, ©1996
EX2030	Claim Construction Memorandum Opinion and Order, Doc. 228, Case 2:22-cv-00293, filed Nov. 21, 2023
EX2031	JEDEC Standard No. 21-C (Rev. 1.0) SPD for DDR2 memory modules, http://web.archive.org/web/20050515195114/http://www.jedec.org:80/download/search/4_01_02_10R13.pdf
EX2032	Brent Keeth, <i>et. al.</i> , DRAM Circuit Design, Fundamental and High-Speed Topics, IEEE Press Series on Microelectronic Systems, ©2008
EX2033	Deposition Transcript of Dr. Andrew Wolfe, Nov. 4, 2023
EX2034	U.S. Patent No. 7,610,447 issued to Pergo <i>et al.</i> , Oct. 27, 2009
EX2035	Texas Instruments, Direct Rambus™ Clock Generator, CDCR83A, Aug. 2005

EX2036	Patent Owner's Demonstratives for Oral Argument on May 3, 2024
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Dated: April 30, 2024

Respectfully submitted,

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CERTIFICATE OF SERVICE

I hereby certify, pursuant to 37 C.F.R. section 42.6, that on April 30, 2024, a complete copy of the **PATENT OWNER'S UPDATED EXHIBIT LIST** was served upon the following, by ELECTRONIC MAIL:

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