

Declaration of Alyssa G. Resnick

I, Alyssa G. Resnick, declare as follows:

1. I am the Associate Dean, Technical Services & Collection Development at the University of Southern California, USC Libraries. Technical Services & Collection Development is a department of the University of Southern California's USC Libraries. In my role of Associate Dean, I oversee Technical Services & Collection Development's operations. I have served as Associate Dean since July 2021 and have been in a similar position at USC Libraries since April 2017. I oversee all aspects of procuring and maintaining collections in all formats. The department's functions include acquisitions, cataloging, physical processing, electronic resources management, interlibrary loan and management of the print collections. From my work as Associate Dean, I am knowledgeable about Technical Services & Collection Development and the University of Southern California library system. I am also knowledgeable about the practices of the University of Southern California library system with respect to receiving and cataloging publications, placing publications in circulation, and making publications available for inspection by the public. USC Libraries Technical Services & Collection Development is located at 3434 South Grand Ave., CAL-215, Los Angeles, CA 90089-2810. I am over the age of majority and make this declaration of my own personal knowledge.

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Stanley Wolf and Richard Tauber

Lattice Press, 2000

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Executed at Los Angeles, CA, on October 19, 2022.

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Alyssa G. Resnick

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Silicon Processing

for the VLSI Era
Volume 1 - Process Technology
Second Edition

S. Wolf and R.N. Tauber

**SILICON PROCESSING
FOR
THE VLSI ERA**

**VOLUME 1:
PROCESS TECHNOLOGY
Second Edition**

**STANLEY WOLF Ph.D.
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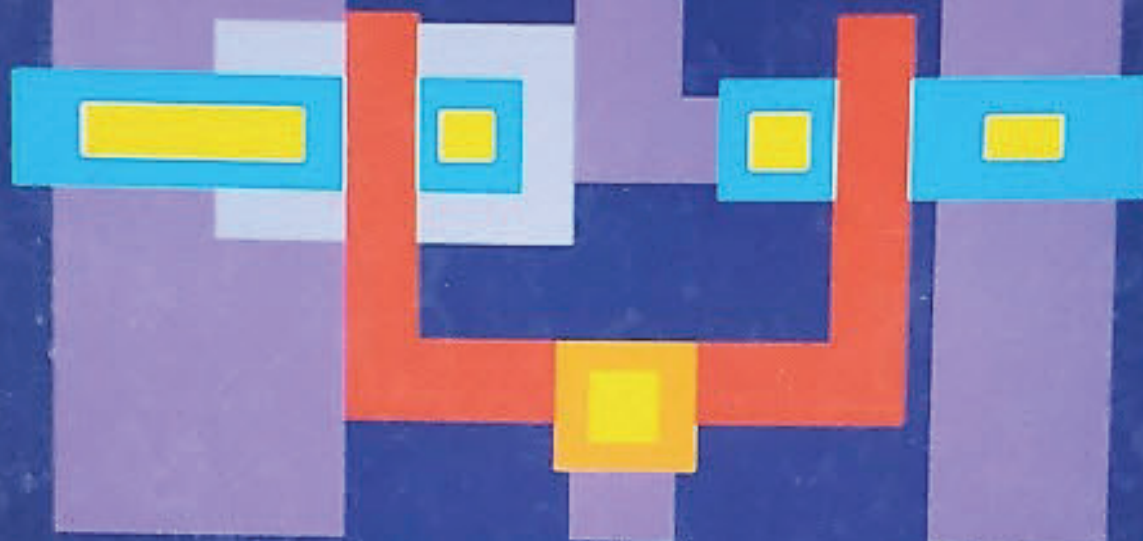
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Silicon Processing

for the VLSI Era

Volume 2 - Process Integration



S. Wolf

SILICON PROCESSING FOR THE VLSI ERA

VOLUME 2: PROCESS INTEGRATION

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Professor, Department of Electrical Engineering
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Long Beach, California

LATTICE PRESS

Sunset Beach, California

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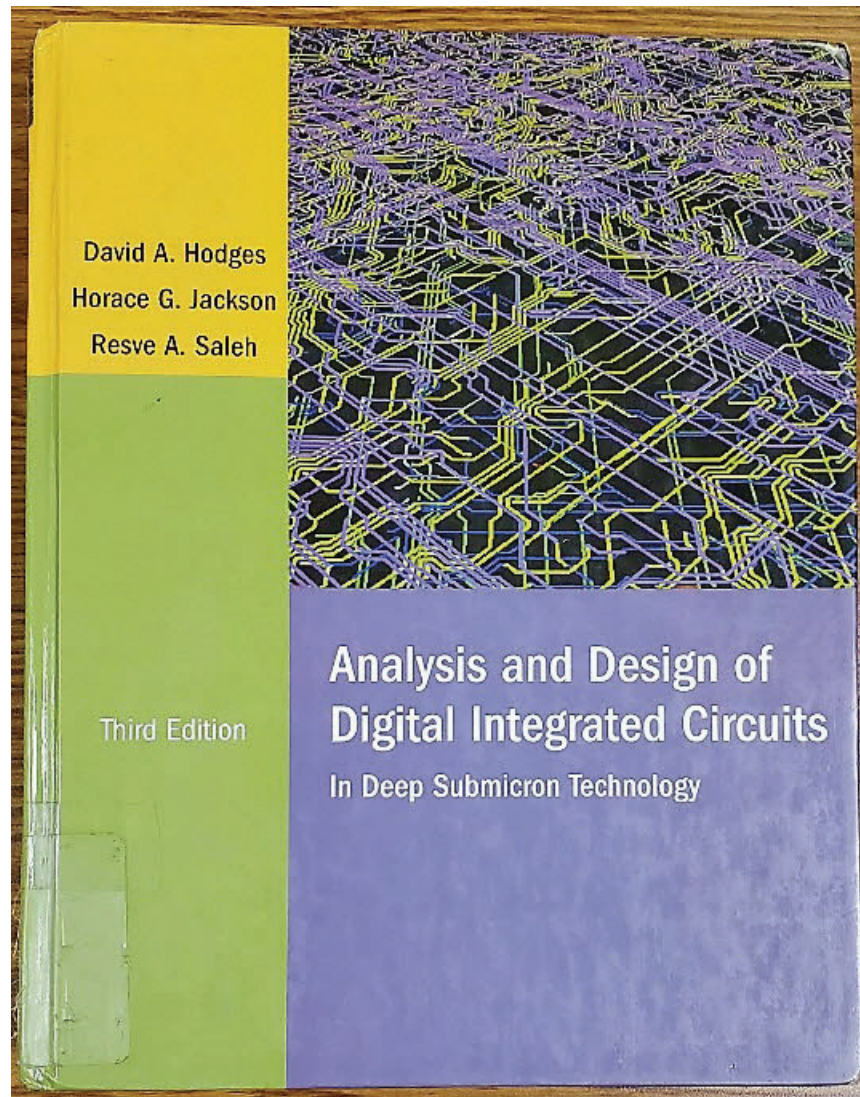
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Executed at Los Angeles, CA, on October 19, 2022.

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Analysis and Design of Digital Integrated Circuits

In Deep Submicron Technology

THIRD EDITION

David A. Hodges

University of California, Berkeley

Horace G. Jackson

University of California, Berkeley

Resve A. Saleh

University of British Columbia



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