

Declaration of Alyssa G. Resnick

I, Alyssa G. Resnick, declare as follows:

1. I am the Associate Dean, Technical Services & Collection Development at the University of Southern California, USC Libraries. Technical Services & Collection Development is a department of the University of Southern California's USC Libraries. In my role of Associate Dean, I oversee Technical Services & Collection Development's operations. I have served as Associate Dean since July 2021 and have been in a similar position at USC Libraries since April 2017. I oversee all aspects of procuring and maintaining collections in all formats. The department's functions include acquisitions, cataloging, physical processing, electronic resources management, interlibrary loan and management of the print collections. From my work as Associate Dean, I am knowledgeable about Technical Services & Collection Development and the University of Southern California library system. I am also knowledgeable about the practices of the University of Southern California library system with respect to receiving and cataloging publications, placing publications in circulation, and making publications available for inspection by the public. USC Libraries Technical Services & Collection Development is located at 3434 South Grand Ave., CAL-215, Los Angeles, CA 90089-2810. I am over the age of majority and make this declaration of my own personal knowledge.

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Stanley Wolf and Richard Tauber

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Executed at Los Angeles, CA, on October 19, 2022.

Alyssa G. Resnick

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Silicon Processing

for the VLSI Era
Volume 1 - Process Technology
Second Edition

S. Wolf and R.N. Tauber

**SILICON PROCESSING
FOR
THE VLSI ERA**

**VOLUME 1:
PROCESS TECHNOLOGY
Second Edition**

STANLEY WOLF Ph.D.

RICHARD N. TAUBER Ph.D.

LATTICE PRESS

Sunset Beach, California

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Published by:

Lattice Press,
Post Office Box 340
Sunset Beach, California 90742, U.S.A.

Cover design by Roy Montibon, Visionary Art Resources, Inc., Santa Ana, CA.

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Library of Congress Cataloging in Publication Data
Wolf, Stanley

Silicon Processing for the VLSI Era
Volume 1 : Process Integration

Includes Index

1. Integrated circuits-Very large scale integration. 2. Silicon. I. Title

86-081923

ISBN 0-961672-4-5

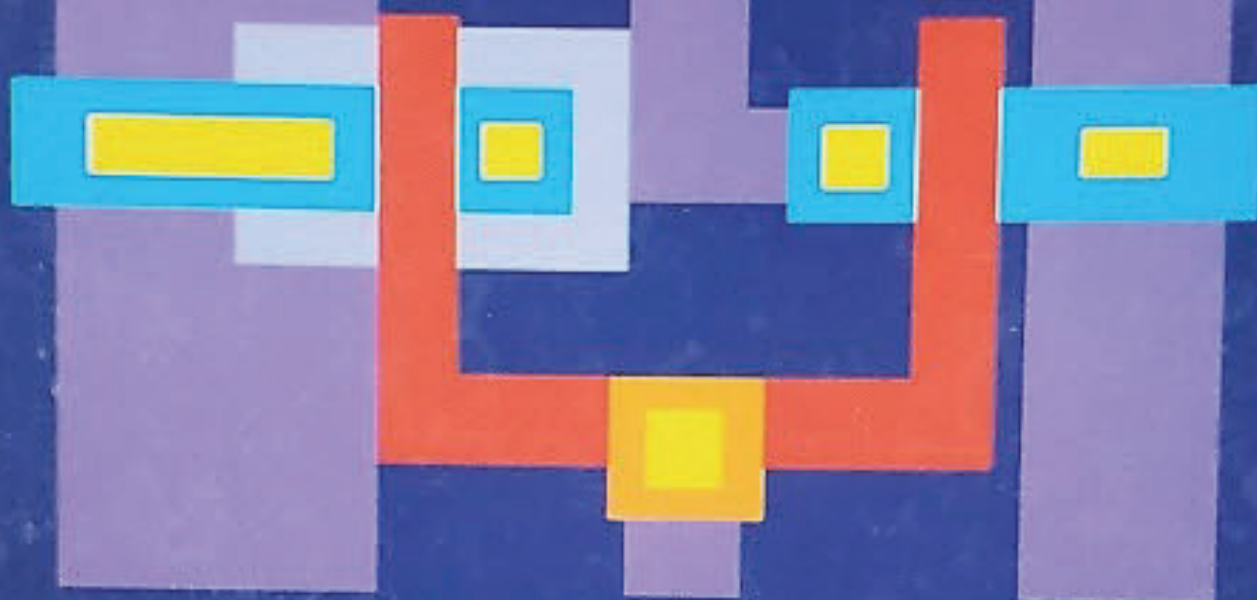
9 8 7 6

PRINTED IN THE UNITED STATES OF AMERICA

Silicon Processing

for the VLSI Era

Volume 2 - Process Integration



S. Wolf

SILICON PROCESSING FOR THE VLSI ERA

VOLUME 2: PROCESS INTEGRATION

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Sunset Beach, California

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Published by:

Lattice Press,
Post Office Box 340
Sunset Beach, California 90742, U.S.A.

Cover design by Roy Montibon, Visionary Art Resources, Inc., Santa Ana, CA.

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Library of Congress Cataloging in Publication Data
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Analysis and design of digital integrated circuits: In deep submicron technology

McGraw-Hill 2004 3rd edition

Hodges, David A. et al.

University of Southern California Libraries Call #: TK7874.65 H64 2004

Date of public availability: on or around 9/10/2003 as indicated by library's received date in the ILS

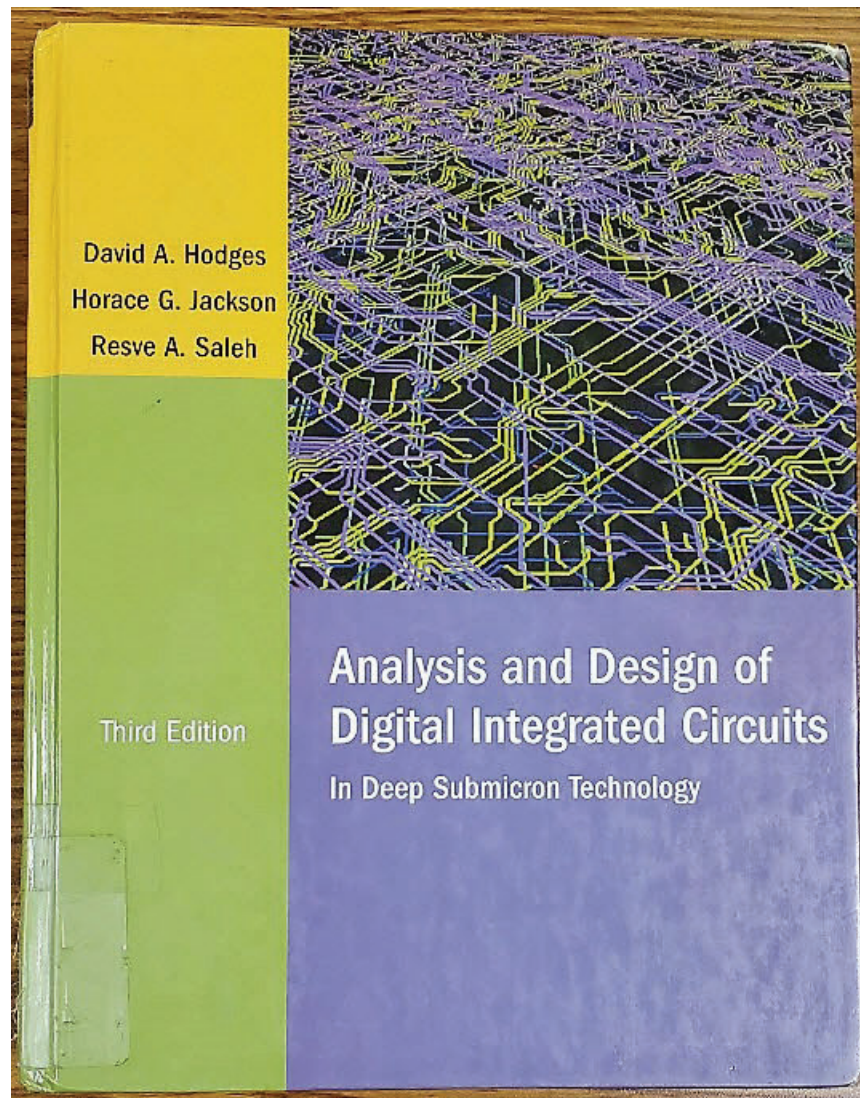
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Executed at Los Angeles, CA, on October 19, 2022.

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Analysis and Design of Digital Integrated Circuits

In Deep Submicron Technology

THIRD EDITION

David A. Hodges

University of California, Berkeley

Horace G. Jackson

University of California, Berkeley

Resve A. Saleh

University of British Columbia



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This book is printed on acid-free paper.

1 2 3 4 5 6 7 8 9 0 DOC/DOC 0 9 8 7 6 5 4 3

ISBN 0-07-228365-3

Publisher: Elizabeth A. James

Senior sponsoring editor: Carlise Paulson

Senior developmental editor: Emily J. Lupash

Marketing manager: Dawn R. Bercier

Project manager: Joyce Watters

Senior production supervisor: Laura Fuller

Lead media project manager: Audrey A. Reiter

Senior media technology producer: Phillip Meek

Senior coordinator of freelance design: Michelle D. Whitaker

Cover/interior designer: Argosy Publishing

Cover image: © X-Architecture Routing Illustration Courtesy of Cadence Design Systems, Inc.

Lead photo research coordinator: Carrie K. Burger

Compositor: Lachina Publishing Services

Typeface: 10.5/12 Times Roman

Printer: R. R. Donnelley Crawfordsville, IN

Library of Congress Cataloging-in-Publication Data

Hodges, David A., 1937-

Analysis and design of digital integrated circuits : in deep submicron technology. David A. Hodges, Horace G.

Jackson, Resve Saleh.—3rd ed

p. cm.—(McGraw-Hill series in electrical engineering)

Includes index.

ISBN 0-07-228365-3

1. Digital integrated circuits—Design and construction. 2. Integrated circuits—Ultra large scale integration.
3. Electric circuit analysis. I. Jackson, Horace G. II. Saleh, Resve. III. Title. IV. Series.

TK7874.65.H64 2004
621.3815—dc21

2003051006
CIP

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