

Exhibit 1036

Claim Term	Patent Owner's Proposed Construction	Petitioner's Proposed Construction
“substrate”	Plain Meaning (“an underlying layer”)	“the initial material within which or on which the semiconductor device is fabricated”
“active region”	Plain Meaning (“a doped silicon region at the surface of a semiconductor device (e.g., the source and drain and silicon between them) where a transistor can be formed”)	“region that forms the current path of a device” or “the source, drain and channel region of a device”
“to aid carrier movement from ... [to/towards] ...”	Plain Meaning	Indefinite Alternative Construction: “to sweep the carriers from ... [to/towards] ...”
“well region”	Plain Meaning (“a doped region that surrounds the active region of a semiconductor device”)	“A well, whether formed by single or multiple implants. Portions of a well are not well regions.”
“active region ... within which transistors can be formed”	Plain Meaning (“a doped silicon region at the surface of a semiconductor device where a transistor can be formed”)	Indefinite