UNITED STATES PATENT AND TRADEMARK OFFICE

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BEFORE THE PATENT TRIAL AND APPEAL BOARD

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XILINX, INC.
Petitioner

v.

POLARIS INNOVATIONS LIMITED
Patent Owner

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Case IPR2023-00513
U.S. Patent 7,532,523

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PETITIONER'S UPDATED EXHIBIT LIST

UPDATED EXHIBIT LIST

Exhibit	Description
1001	U.S. Patent No. 7,532,523 (the "'523 Patent")
1002	Prosecution history of the '523 Patent
1003	Declaration of Dr. R. Jacob Baker
1004	Curriculum Vitae of Dr. R. Jacob Baker
1005	U.S. Patent No. 7,433,992 to Bains ("Bains")
1006	JEDEC STANDARD, Double Data Rate (DDR) 2 SDRAM Specification, JESD79-2A ("JESD79-2A")
1007	U.S. Patent No. 7,092,299 to Kwak ("Kwak")
1008	Samuel D., *DDR-II: How it Works*, X86-SECRET (Sept. 8, 2004), http://www.x86-secret.com/articles/ram/ddr2_667/ddr2667-6en.htm.
1009	U.S. Patent No. 6,754,132 ("Kyung")
1010	Declaration of Julie Carlson regarding JESD79-2A
1011	First Amended Complaint, Polaris Innovations Ltd. v. Xilinx, Inc., 1:22-cv-00174-RGA (D. Del. April 28, 2022), ECF No. 14
1012	Scheduling Order, Polaris Innovations Ltd. v. Xilinx, Inc., 1:22-cv-00174-RGA (D. Del. May 31, 2022), ECF No. 20
1013	United States District Courts — Federal Court Management Statistics, National Judicial Caseload Profile (June 30, 2022), available at https://www.uscourts.gov/sites/default/files/fcms_na_distprofile0630.2022_0.pdf
1014	Motion Success for Stay Pending IPR before Judge Richard G. Andrews in the District of Delaware (Docket Navigator data from 2020 to 1/20/2023)
1015	JEDEC STANDARD, Configurations for Solid State Memories, JEDEC Standard No. 21-C, Release 14
1016	Patent License, Tolling Stand-Still and Settlement Agreement

Dated: February 8, 2024

Respectfully submitted,

/Brian W. Oaks/

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CERTIFICATE OF SERVICE

Pursuant to 37 C.F.R. § 42.6, the undersigned certifies that on February 8, 2024, a copy of the PETITIONER'S UPDATED EXHIBIT LIST and EXHIBIT 1016 was electronically served on the following counsel of record for Patent Owner:

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