

Claim Term	Patent Owner's Proposed Construction	Petitioner's Proposed Construction
"surface layer"	Plain meaning ("a layer at the surface") ¹	Indefinite ²
"substrate"	Plain Meaning ("an underlying layer") ³	"the initial material within which or on which the semiconductor device is fabricated" ⁴
"active region"	Plain Meaning ("a doped silicon region at the surface of a semiconductor device (e.g., the source and drain and silicon between them) where a transistor can be formed") ⁵	"region that forms the current path of a device" ⁶ or "the source, drain and channel region of a device" ⁷
"unidirectional"	Plain meaning ⁸	"unidirectional over the [drift layer/well region]" ⁹
"to aid the movement of minority carriers from ... to ..."	Plain Meaning ¹⁰	Indefinite Alternative Construction: "to sweep the minority carriers from ... to ..." ¹¹
"well region"	Plain Meaning ("a doped region that surrounds the active region of a semiconductor device") ¹²	"A well, whether formed by single or multiple implants. Portions of a well are not well regions." ¹³

¹ Ex.1031, 7.

² *Id.*

³ *Id.*

⁴ *Id.*

⁵ Ex.1030, 29.

⁶ Ex.1031, 7.

⁷ *Id.*

⁸ *Id.*, 7-8.

⁹ *Id.*

¹⁰ *Id.* 8.

¹¹ *Id.*.

¹² Ex.1030, 39.

¹³ Ex.1031., 8