

I. DISPUTED CONSTRUCTIONS

Term No.	Term	Plaintiff's Proposed Construction	Defendant's Proposed Construction	Court's Final Construction
1	“look up table of M inputs and N outputs” (claims 1, 7, and 13)	Plain and ordinary meaning	“component of a programmable logic circuit device that is composed of memories and is capable of implementing an arbitrary logic circuit of M inputs on at least one of N outputs”	Plain and ordinary meaning
2	“LUT unit” (all asserted claims)	Plain and ordinary meaning	Indefinite Otherwise, if not indefinite, each “LUT unit” is a “component of a programmable logic circuit device that is composed of memories and is capable of implementing an arbitrary logic circuit of its inputs with at least one output”	Plain and ordinary meaning