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JUL 26, 2006

Synopsys Donates Library of Advanced SystemVerilog Assertion Checkers to Accellera Standards Organization

Checkers Proven to Accelerate Customer Adoption of SystemVerilog Assertions to Benefit Entire Industry

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that it has donated a library of advanced SystemVerilog assertion checkers defined in the...



JUL 25, 2006

Synopsys' PrimeYield Delivers Fast Turn-Around Time on Intel® Dual-Core Xeon® 5160 Processor

Scalable DFM Checks Accelerate Time-to-Yield for Designers

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that integrated circuit (IC) designers can achieve fast runtimes when using Synopsys' new PrimeYield...



JUL 24, 2006

Synopsys PrimeYield LCC Links to IC Compiler for Automated Correction of Lithography Problems

Toshiba to Deploy Integrated, Production-Proven DFM Technology into Standard Layout Flow

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that PrimeYield – the company's new, comprehensive tool suite for design-yield analysis – enables...



JUL 24, 2006

Synopsys Extends PrimeTime and Star-Rcxt With Statistical Capabilities To Address Variation-Aware Design Challenges

Built on Gold Standards, PrimeTime VX and Star-RCXT VX Tools Deliver Foundation for Comprehensive Variation-aware Design Solution for Sub-65nm Design

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced the latest design-centric offerings in its comprehensive design-for-manufacturing (DFM) solution...

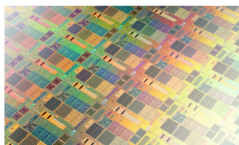


JUL 24, 2006

Synopsys Extends DFM Leadership With Launch of PrimeYield Tool Suite for Yield Analysis

Integration of Production-Proven Design and Manufacturing Technology Accelerates Time to Yield

In a move that empowers integrated circuit (IC) designers by giving them more control over the manufacturability of their leading-edge designs, Synopsys, Inc. (NASDAQ: SNPS), a world leader in...

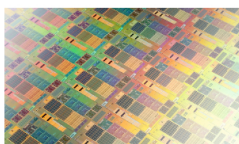


JUL 20, 2006

Synopsys DFT MAX Cuts Test Costs on Nanometer Designs

More Than 50 Tapeouts Prove DFT MAX Reduces Test Time and Cost

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that its DFT MAX scan compression automation solution has been instrumental in reducing test costs...

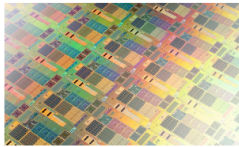


JUL 19, 2006

Synopsys Galaxy Platform Reduces Power Consumption of Industry-Leading Multi- Voltage Designs

Multiple Multi-Voltage Tapeouts Completed With Galaxy Advanced Low-Power Solution

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that ARM, DSP Group Inc., Freescale, Infineon Technologies AG,



JUL 19, 2006

Synopsys Delivers First 65-nm Reference Flow for IBM, Samsung and Chartered

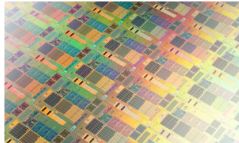
Common Platform Technology Reference Flow Adds Critical Area Design-for-Manufacturing Capability in IC Compiler
Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced availability of its extended RTL-to-GDSII low-power reference design flow for the latest...



JUL 18, 2006

TSMC Reference Flow 7.0 Incorporates Synopsys' IC Compiler

Reference Flow 7.0 Incorporates Latest Technologies for Low Power and Yield Optimization to Address 65-nanometer Design Challenges
Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, and Taiwan Semiconductor Manufacturing Company (NYSE: TSM) – the world largest semiconductor foundry, today...



JUL 18, 2006

Industry Momentum Builds for the ARM-Synopsys VMM for SystemVerilog

Users Embrace the VMM Methodology for SystemVerilog and Synopsys' VCS® Native Testbench
Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that industry adoption and support of the Verification Methodology Manual (VMM) for SystemVerilog,...

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Corporate Headquarters

690 East Middlefield Road
Mountain View, CA 94043

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650-584-5000
800-541-7737

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