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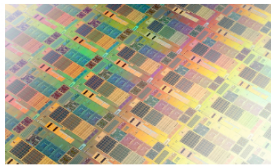
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MAY 29, 2007

Synopsys and ARM Optimize Reference Methodology for Aggressive Power Management

Enhanced Flow Uses Synopsys' IC Compiler and DC Topographical Technology to Manage Leakage for 65-Nanometer and Below Processes

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software and ARM (LSE: ARM); (NASDAQ: ARMHY), today announced an enhanced implementation Reference Methodology (iRM) for the...



MAY 29, 2007

New Low Power Methodology Manual Demystifies Advanced Power Management

Springer Book Explains How to Ease Adoption of Aggressive Power Management Techniques
Springer Science+Business Media, LLC today announced the publication of the "Low Power Methodology Manual for System-on-Chip Design" (LPMM), a comprehensive and practical guide to managing...



MAY 25, 2007

Synopsys Senior Vice President to Speak at Cowen and Company Technology Conference June 1, 2007

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that John Chilton, senior vice president of...



MAY 25, 2007

AlI Corporation Accelerates Tapeout of Set-Top Box Chip Using Synopsys IC Compiler

IC Compiler Exceeds Target Performance While Lowering Project Cost

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that AlI Corporation, a leading supplier of...



MAY 24, 2007

Synopsys Achieves Two IP Firsts: 65-nm PCIe and 90-nm USB Compliance Utilizing Common Platform Technologies

Single GDSII, Multi-Foundry Connectivity IP Enables Designs Using Common Platform Processes

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced compliance of its PCI Express® and...

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