

Change 96339 on 2003/04/17 by paulv@paulv_r400_linux_marlboro

Fix for the RBT write fifo.

Change 96295 on 2003/04/17 by johnchen@johnchen_r400_linux_marlboro_rbr

major changes: using 3 128x96 rams instead of 4 96x96 rams

Change 96248 on 2003/04/17 by paulv@paulv_r400_linux_marlboro

Timing fix.

Change 95833 on 2003/04/16 by paulv@paulv_r400_linux_marlboro

Major HiZ rework (equations fixed, some misunderstandings corrected, etc.). Tests passing again.

Change 95534 on 2003/04/14 by paulv@paulv_r400_linux_marlboro

The include I removed, while its okay for simulation, is not okay for synthesis.

Change 95533 on 2003/04/14 by paulv@paulv_r400_linux_marlboro

Removed an include and added another.

Change 95396 on 2003/04/14 by wlawless@wlawless_r400_linux_marlboro

fragment mslock probe stuff

Change 95254 on 2003/04/11 by paulv@paulv_r400_linux_marlboro

Added include of rb_reg.v

Change 95029 on 2003/04/10 by paulv@paulv_r400_linux_marlboro

Minor code simplification.

Change 94962 on 2003/04/10 by johnchen@johnchen_r400_linux_marlboro_rbr

correct memory operations for standard hier_stencil

Change 94953 on 2003/04/10 by wlawless@wlawless_r400_linux_marlboro

fixed a probe bug found in r400rb_rb/standard_z by Paul

Change 94947 on 2003/04/10 by paulv@paulv_r400_linux_marlboro

Fixed the event_cache_flush, context_done and flush_and_invalidate signals to account

for surface_enabled.

Change 94916 on 2003/04/10 by paulv@paulv_r400_linux_marlboro

Two major fixes. The first is in the RBM for external queue logic. The second is the consolidation of registers in tile and small part of depth.

Change 94820 on 2003/04/09 by johnchen@johnchen_r400_linux_marlboro_rbrc

fixes for random test hangs

Change 94780 on 2003/04/09 by hmonsef@hmonsef

No need

Change 94776 on 2003/04/09 by paulv@paulv_r400_linux_marlboro

Removed a file and added a file to the checklist.

Change 94775 on 2003/04/09 by paulv@paulv_r400_linux_marlboro

LEDA fix.

Change 94767 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94766 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94765 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94764 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94762 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94761 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94760 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94759 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94758 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94757 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94756 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94755 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94754 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94753 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94752 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94751 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94749 on 2003/04/09 by hmonsef@hmonsef

Worst case RTL file

Change 94748 on 2003/04/09 by hmonsef@hmonsef

Worst case rtl file

Change 94747 on 2003/04/09 by hmonsef@hmonsef

Replaced by 16x34

Change 94744 on 2003/04/09 by hmonsef@hmonsef

Replaced with 8x111

Change 94740 on 2003/04/09 by hmonsef@hmonsef

Replaced by 16x228

Change 94727 on 2003/04/09 by jayw@jayw_r400_linux_marlboro

Fix for 1555 format with alpha channel masked out.

Change 94706 on 2003/04/09 by wlawless@wlawless_r400_linux_marlboro

Changed the fragment probe to work with a probe mask instead of a coarse mask for each fragment....

Change 94617 on 2003/04/08 by johnchen@johnchen_r400_linux_marlboro_rbrc

update tile_id and qc_index for cacheline hits because these values can change in tile block

Change 94198 on 2003/04/07 by jayw@jayw_r400_linux_marlboro

Pass sign through for neg zero, max and min functions. hang fix.

Change 94118 on 2003/04/06 by johnchen@johnchen_r400_linux_marlboro_rbrc

fix a cache write conflict

Change 94063 on 2003/04/04 by jayw@jayw_r400_linux_marlboro

missing comma from Mark's checkin.

Change 94062 on 2003/04/04 by msprague@msprague_r400_synth

Update to reflect timing changes with comments retained

Change 94022 on 2003/04/04 by hmonsef@hmonsef

Wrong file name

Change 94021 on 2003/04/04 by hmonsef@hmonsef

Replaces 8x128

Change 94001 on 2003/04/04 by johnchen@johnchen_r400_linux_marlboro_rbrc

bug fix for figuring out when to expand and some stencil support

Change 93999 on 2003/04/04 by wlawless@wlawless_r400_linux_marlboro

fixed the previous fix... oops

Change 93998 on 2003/04/04 by hmonsef@hmonsef

Replace 8x128 with 8x136

Change 93997 on 2003/04/04 by hmonsef@hmonsef

Replaced 8x128 with 8x136

Change 93995 on 2003/04/04 by hmonsef@hmonsef

Replaced 8x128 with 8x136

Change 93994 on 2003/04/04 by hmonsef@hmonsef

Replaces 8x128

Change 93988 on 2003/04/04 by wlawless@wlawless_r400_linux_marlboro

changed a muc cnt

Change 93985 on 2003/04/04 by hmonsef@hmonsef

Replaced 8x128 with 8x136

Change 93984 on 2003/04/04 by hmonsef@hmonsef

Replaced 8x128 with 8x136

Change 93972 on 2003/04/04 by msprague@msprague_r400_synth

Incorporated timing and area changes into pre-vpp version, so comments are kept

Change 93957 on 2003/04/04 by hmonsef@hmonsef_r400_linux_marlboro_rbrc

Reduced the number of comparators by rewriting the code

Change 93934 on 2003/04/04 by wlawless@wlawless_r400_linux_marlboro

Addressing bug when flushing fragment bits

Change 93914 on 2003/04/04 by paulv@paulv_r400_linux_marlboro

Timing fix.

Change 93845 on 2003/04/04 by jayw@jayw_r400_linux_marlboro

adding new file

Change 93801 on 2003/04/03 by jayw@jayw_r400_linux_marlboro

Mark Sprague's timing, power and area reduction updates.

Change 93627 on 2003/04/03 by jayw@jayw_r400_linux_marlboro_rbrc

resolve pixelpair signal for hang fix.

Change 93626 on 2003/04/03 by jayw@jayw_r400_linux_marlboro_rbrc

Fixes for hangs.

Change 93591 on 2003/04/03 by paulv@paulv_r400_linux_marlboro

Fixed logic for the cacheline dirty vector.

Change 93450 on 2003/04/02 by wlawless@wlawless_r400_linux_marlboro

it was using the allocated addr for the second tile in a hit line

Change 93427 on 2003/04/02 by wlawless@wlawless_r400_linux_marlboro

Update multisample for 2 and 4 sample

Change 93402 on 2003/04/02 by jayw@jayw_r400_linux_marlboro

Fixes for hang and clamping

Change 93196 on 2003/04/01 by paulv@paulv_r400_linux_marlboro

For non-surface-enabled tiles, hiz_quad_state_mask should be set to zeroes.

Change 93181 on 2003/04/01 by johnchen@johnchen_r400_linux_marlboro_rbrc

add smask_enable and zmask_enable to request register

Change 93069 on 2003/04/01 by paulv@paulv_r400_linux_marlboro

Changed definition of upper quad_state_mask bit (per quad). Also added no_detail_mask qualifier to output fifo to RBD.

Change 92977 on 2003/03/31 by paulv@paulv_r400_linux_marlboro

HiZ fixes.

Change 92965 on 2003/03/31 by jayw@jayw_r400_linux_marlboro

Added missing include.

Change 92885 on 2003/03/31 by paulv@paulv_r400_linux_marlboro

Fixed some other_tiledata logic.

Change 92721 on 2003/03/28 by johnchen@johnchen_r400_linux_marlboro_rbrc

quaddata even flush happen independent of z_enable or stencile_enable

Change 92693 on 2003/03/28 by paulv@paulv_r400_linux_marlboro

Fixed bug where quad cache was being read even though mask was 0.

Change 92674 on 2003/03/28 by hmonsef@hmonsef

Replaces 16x32 with 16x34

Change 92671 on 2003/03/28 by hmonsef@hmonsef

Replaces 16x32 with 16x34

Change 92667 on 2003/03/28 by hmonsef@hmonsef

Replaced 16x32 with 16x34

Change 92666 on 2003/03/28 by hmonsef@hmonsef

replaced 16x32 with 16x34

Change 92665 on 2003/03/28 by hmonsef@hmonsef

replaced 16x32 with 16x34

Change 92664 on 2003/03/28 by hmonsef@hmonsef

Replaces 16x32

Change 92649 on 2003/03/28 by paulv@paulv_r400_linux_marlboro

Fix to flush logic (again).

Change 92617 on 2003/03/28 by johnchen@johnchen_r400_linux_marlboro_rbrc

update szmask request interface

Change 92611 on 2003/03/28 by hmonsef@hmonsef

Replaced 16x224 with 16x228

Change 92609 on 2003/03/28 by hmonsef@hmonsef

Replaced 16x224 with 16x228

Change 92608 on 2003/03/28 by hmonsef@hmonsef

Replaced 16x224 with 16x228

Change 92607 on 2003/03/28 by hmonsef@hmonsef

Replaced 16x224 with 16x228

Change 92606 on 2003/03/28 by hmonsef@hmonsef

Replaced 16x224 with 16x228

Change 92605 on 2003/03/28 by hmonsef@hmonsef

Replaced 16x224 with 16x228

Change 92590 on 2003/03/28 by hmonsef@hmonsef

Replaces 16x224.

Change 92582 on 2003/03/28 by paulv@paulv_r400_linux_marlboro

Bunch of fixes, including fixes to the quad cache cacheline allocation and the flush logic between the quad and tile cache.

Change 92496 on 2003/03/27 by johnchen@johnchen_r400_linux_marlboro_rbrc

remove surface_enable quaddata even flush

Change 92466 on 2003/03/27 by jayw@jayw_r400_linux_marlboro

more signal name renaming...

Change 92435 on 2003/03/27 by johnchen@johnchen_r400_linux_marlboro_rbrc

add surface_enable to quaddata even flush

Change 92424 on 2003/03/27 by jayw@jayw_r400_linux_marlboro

removed last pixel of quad, never used, regenerated.

Change 92423 on 2003/03/27 by jayw@jayw_r400_linux_marlboro

renamed many sigs. fixed a hold_non_fog pulling in wrong cycle data.

Change 92415 on 2003/03/27 by johnchen@johnchen_r400_linux_marlboro_rbrc

add flush bits to the quaddata interface

Change 92314 on 2003/03/27 by jayw@jayw_r400_linux_marlboro

Fix for color mask and plane mask.

Change 92288 on 2003/03/26 by jayw@jayw_r400_linux_marlboro

fix for color mask and color swap set.

Change 92221 on 2003/03/26 by jayw@jayw_r400_linux_marlboro

LEDA fixes.

Change 92210 on 2003/03/26 by paulv@paulv_r400_linux_marlboro

Several fixes in the HiZ tile and quad checkers and the quad cache (cachelines weren't being flushed because inflight counts weren't going to zero). Also removed RBD_RBT_quaddata_z_enable (it wasn't needed).

Change 92158 on 2003/03/26 by wlawless@wlawless_r400_linux_marlboro

Added the 2 & 4 sample logic

Change 92080 on 2003/03/25 by johnchen@johnchen_r400_linux_marlboro_rbrc

add the dk bit to the pipeline

Change 91920 on 2003/03/24 by paulv@paulv_r400_linux_marlboro

leda fixes

Change 91890 on 2003/03/24 by johnchen@johnchen_r400_linux_marlboro_rbrc

get the right tile_id when expand

Change 91876 on 2003/03/24 by paulv@paulv_r400_linux_marlboro

LEDA fixes.

Change 91872 on 2003/03/24 by hmonsef@hmonsef

replaced 8x110 with 8x111

Change 91869 on 2003/03/24 by hmonsef@hmonsef

replaced 8x110 with 8x111

Change 91868 on 2003/03/24 by hmonsef@hmonsef

Replaced 8x110 with 8x111

Change 91867 on 2003/03/24 by hmonsef@hmonsef

Repalced 8x110 with 8x111

Change 91866 on 2003/03/24 by hmonsef@hmonsef

Repalces 8x110

Change 91865 on 2003/03/24 by hmonsef@hmonsef

Replaced 8x110 with 8x111

Change 91864 on 2003/03/24 by hmonsef@hmonsef

Replaced 8x110 with 8x111

Change 91845 on 2003/03/24 by wlawless@wlawless_r400_linux_marlboro

Added a 64 bit signal of line_in_read_cam to block it from being allocated

Change 91820 on 2003/03/24 by paulv@paulv_r400_linux_marlboro

Added RBT_RBC_c_frag_color_sel.

Change 91818 on 2003/03/24 by paulv@paulv_r400_linux_marlboro

Multiple fixes and some additions.

Change 91808 on 2003/03/24 by wlawless@wlawless_r400_linux_marlboro

new stuff for ms_lock color cache

Change 91746 on 2003/03/24 by jayw@jayw_r400_linux_marlboro

More LEDA 'fixes'.

Change 91737 on 2003/03/23 by jayw@jayw_r400_linux_marlboro

Many LEDA fixes and added one missing include to rb_rbd_cache_access.v

Change 91706 on 2003/03/23 by jayw@jayw_r400_linux_marlboro

LEDA fixes and important subnorm result fix in ab_format.v

Change 91426 on 2003/03/20 by johnchen@johnchen_r400_linux_marlboro_rbrc

freeing cachelines independent of tag...temporary fix....might not work when multiple
cachelines need freeing

Change 91383 on 2003/03/20 by johnchen@johnchen_r400_linux_marlboro_rbrc

probe cache hit compare fix

Change 91352 on 2003/03/20 by hmonsef@hmonsef

Replaced 16x223 with 16x224

Change 91339 on 2003/03/20 by hmonsef@hmonsef

Replaced 16x223 with 16x224

Change 91338 on 2003/03/20 by hmonsef@hmonsef

Replaced 16x223 with 16x224

Change 91337 on 2003/03/20 by hmonsef@hmonsef

Replaced 16x223 with 16x224

Change 91336 on 2003/03/20 by paulv@paulv_r400_linux_marlboro

A previous fix had tiles coming out every cycle, which shouldn't happen. This has been
fixed.

Change 91335 on 2003/03/20 by hmonsef@hmonsef

Replaces 16x223

Change 91331 on 2003/03/20 by hmonsef@hmonsef

Replaced 16x223 by 16x224

Change 91330 on 2003/03/20 by hmonsef@hmonsef

replaced 16x223 by 16x224

Change 91303 on 2003/03/20 by jayw@jayw_r400_linux_marlboro_rbrc

non-blend not set at allocation

Change 91278 on 2003/03/20 by paulv@paulv_r400_linux_marlboro

Fixed bypass fifo from overflowing and slightly modified the stall signal for the head.

Change 91208 on 2003/03/20 by paulv@paulv_r400_linux_marlboro

A few other minor fixes.

Change 91199 on 2003/03/20 by paulv@paulv_r400_linux_marlboro

Code fixes and optimizations.

Change 90935 on 2003/03/19 by paulv@paulv_r400_linux_marlboro

Intermediate check-in. Main fix is fixing surface_enabled in the HiZ logic to only care about z-related surfaces.

Change 90929 on 2003/03/19 by wlawless@wlawless_r400_linux_marlboro

found a bit width typo in the tile inflight decrement logic...

Change 90897 on 2003/03/19 by hmonsef@hmonsef

Added scan ports as requested by Jim Bosco

Change 90803 on 2003/03/18 by paulv@paulv_r400_linux_marlboro

Fixed the quad cache to prevent HiZ from starving RBD requests (although some better method may be needed) and fixed the quad_cache_cline signal for HiZ writes to quad cache.

Change 90747 on 2003/03/18 by paulv@paulv_r400_linux_marlboro

Tile optimizations and a few bug fixes.

Change 90271 on 2003/03/14 by jayw@jayw_r400_linux_marlboro

Fix for 128 bit linear pixels.

Change 90242 on 2003/03/14 by wlawless@wlawless_r400_linux_marlboro

Removed the LSB from the cam lookup the blend bypass for the num_quads_in_pipe only

Change 90199 on 2003/03/14 by johnchen@johnchen_r400_linux_marlboro_rbrc

compare the complete centerz value..not just the mantisa

Change 90106 on 2003/03/13 by johnchen@johnchen_r400_linux_marlboro_rbrc

turn off request registers for plane4

Change 90101 on 2003/03/13 by paulv@paulv_r400_linux_marlboro

Timing fix.

Change 90086 on 2003/03/13 by johnchen@johnchen_r400_linux_marlboro_rbrc

memory request for plane4

Change 90053 on 2003/03/13 by johnchen@johnchen_r400_linux_marlboro_rbrc

use stencil clear when surface enable is on

Change 90026 on 2003/03/13 by paulv@paulv_r400_linux_marlboro

Timing and area optimizations.

Change 89972 on 2003/03/13 by paulv@paulv_r400_linux_marlboro

Fixed timing loop and the stall_head_addr_calc_b signal for the quad_cache_tc_rtr signal.

Change 89835 on 2003/03/12 by jayw@jayw_r400_linux_marlboro

Clean LEDA errors.

Change 89808 on 2003/03/12 by wlawless@wlawless_r400_linux_marlboro

separated the probe and flush completely... Made the first flush addr free flowing and locked when flush is busy..

Change 89804 on 2003/03/12 by jayw@jayw_r400_linux_marlboro

up to date..

Change 89766 on 2003/03/12 by paulv@paulv_r400_linux_marlboro

Performance optimization for tile cache.

Change 89762 on 2003/03/12 by johnchen@johnchen_r400_linux_marlboro_rbrc

cache write problem when planes are almost filled up

Change 89654 on 2003/03/11 by jayw@jayw_r400_linux_marlboro

Fix for sample mask zero killed quads and cache inflight coherency.

Change 89498 on 2003/03/11 by paulv@paulv_r400_linux_marlboro

Fixed state machine and read_quad_cache signal to take into account the empty propagation delay for the hiz data fifo.

Change 89419 on 2003/03/10 by jayw@jayw_r400_linux_marlboro

Fixes for very 16 bit srepeated and urepeated precision. hang fix, bad state for tiling, unknowns for rc cache signal

Change 89412 on 2003/03/10 by wlawless@wlawless_r400_linux_marlboro

changes how the ms_lock is sent on probe

Change 89374 on 2003/03/10 by johnchen@johnchen_r400_linux_marlboro

plane_in_cache fix for multi cache access quads

Change 89136 on 2003/03/07 by johnchen@johnchen_r400_linux_marlboro

probe hit does not depend on if the cacheline is free or not. Even if it is free, it'll still be a hit

Change 89074 on 2003/03/07 by johnchen@johnchen_r400_linux_marlboro

fix typo

Change 89049 on 2003/03/07 by johnchen@johnchen_r400_linux_marlboro

handful of compression fixes

Change 89041 on 2003/03/07 by paulv@paulv_r400_linux_marlboro

Slight code simplification to the previous timing fix.

Change 89031 on 2003/03/07 by paulv@paulv_r400_linux_marlboro

Minor timing fixes.

Change 88996 on 2003/03/07 by wlawless@wlawless_r400_linux_marlboro

just some small changes for multisample

Change 88918 on 2003/03/06 by jayw@jayw_r400_linux_marlboro

Fix for rop_2d_using_shadow

Change 88804 on 2003/03/06 by jayw@jayw_r400_linux_marlboro

Added comment.

Change 88801 on 2003/03/06 by jayw@jayw_r400_linux_marlboro

Fix for r400rb_color_channels and r400rb_tb

Change 88773 on 2003/03/06 by jayw@jayw_r400_linux_marlboro

Fix for r400rb_tb, breaks r400rb_mask_color_channels alpha.

Change 88750 on 2003/03/06 by wlawless@wlawless_r400_linux_marlboro

mssa fixes

Change 88710 on 2003/03/05 by paulv@paulv_r400_linux_marlboro

Timing and a logic fix.

Change 88686 on 2003/03/05 by johnchen@johnchen_r400_linux_marlboro

hold off accesses to the cache when expand is not done

Change 88659 on 2003/03/05 by jayw@jayw_r400_linux_marlboro

Bug fix for missing define.

Change 88504 on 2003/03/05 by wlawless@wlawless_r400_linux_marlboro

some decrement inflight for fragment tiles stuff for the sample mask zero case

Change 88448 on 2003/03/04 by jayw@jayw_r400_linux_marlboro

Fix for non_blend not being set during allocation.

Change 88447 on 2003/03/04 by jayw@jayw_r400_linux_marlboro

Fix for DST.

Change 88414 on 2003/03/04 by johnchen@johnchen_r400_linux_marlboro

bug fixes for expand

Change 88399 on 2003/03/04 by paulv@paulv_r400_linux_marlboro

A few more possible timing fixes.

Change 88171 on 2003/03/03 by wlawless@wlawless_r400_linux_marlboro

fixed fragment fifo from being written when full/////

Change 88152 on 2003/03/03 by wlawless@wlawless_r400_linux_marlboro

fixed a non_blend after blend from setting the non_blend bit

Change 88074 on 2003/03/03 by wlawless@wlawless_r400_linux_marlboro

fixed the 128b thing correctly

Change 88011 on 2003/03/03 by jayw@jayw_r400_linux_marlboro_rbrc

More random fixes for DST reads.

Change 87990 on 2003/03/03 by wlawless@wlawless_r400_linux_marlboro

128b pixels needed to check both sides of cache to stop in blend mode

Change 87947 on 2003/03/02 by jayw@jayw_r400_linux_marlboro_rbrc

Merged my dst required state changes with Paul's.

Change 87946 on 2003/03/02 by jayw@jayw_r400_linux_marlboro_rbrc

blend dst required logic needed updating to handle blend on/off and more!

Change 87871 on 2003/03/01 by johnchen@johnchen_r400_linux_marlboro

z expand for planes overflow

Change 87839 on 2003/02/28 by paulv@paulv_r400_linux_marlboro

When color clamping was being done, if dst was needed (e.g., clrcmp_fcn_sel = dst), there was not logic to enable dst blending (and, hence, dst was always 0). This has been corrected.

Change 87817 on 2003/02/28 by jayw@jayw_r400_linux_marlboro_rbrc

Fixes for random rb tests.

Change 87745 on 2003/02/28 by ygiong@ygiong_r400_pv2_marlboro

fixed: for modelsim

Change 87653 on 2003/02/28 by jayw@jayw_r400_linux_marlboro_rbrc

Fixes for coherency discovered in random tests.

Change 87652 on 2003/02/28 by paulv@paulv_r400_linux_marlboro

Changed rb_rbt_tc_depthclear_calc to a mc file (from a verilog file).

Change 87644 on 2003/02/28 by wlawless@wlawless_r400_linux_marlboro

fixed a frag flush addr problem, also, blend_cam_rdy only during the pixel 01 cycle

Change 87472 on 2003/02/27 by paulv@paulv_r400_linux_marlboro

Fixed typo.

Change 87393 on 2003/02/27 by hmonsef@hmonsef

Compiled with rev 3220

Change 87385 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87383 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87382 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87380 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87377 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87375 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87374 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87371 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87368 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87364 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87363 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87361 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87360 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87359 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87358 on 2003/02/27 by hmonsef@hmonsef

compiled with rev 3220

Change 87357 on 2003/02/27 by hmonsef@hmonsef

Compiled with rev 3220

Change 87356 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87355 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87354 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87352 on 2003/02/27 by hmonsef@hmonsef

Compiled with REV 3220

Change 87350 on 2003/02/27 by hmonsef@hmonsef

Compiled with Rev 3220

Change 87346 on 2003/02/27 by hmonsef@hmonsef

Compile with Rev 3220

Change 87335 on 2003/02/27 by paulv@paulv_r400_linux_marlboro

Timing fixes.

Change 87314 on 2003/02/27 by jayw@jayw_r400_linux_marlboro_rbrc

Fix for fog alpha blending.

Change 87267 on 2003/02/27 by paulv@paulv_r400_linux_marlboro

Timing fixes.

Change 87211 on 2003/02/26 by johnchen@johnchen_r400_linux_marlboro

change how z expansion is done (instead of going to tile_q0, it goes directly to access_fifo)

Change 87043 on 2003/02/26 by wlawless@wlawless_r400_linux_marlboro

degate msaa_enable with ~event

Change 86971 on 2003/02/26 by jayw@jayw_r400_linux_marlboro

Fragment address gen, first new try. not complete.

Change 86968 on 2003/02/26 by wlawless@wlawless_r400_linux_marlboro
memory export again...

Change 86739 on 2003/02/25 by johnchen@johnchen_r400_linux_marlboro
resubmit LEDA fixes after Bill overwrite them

Change 86733 on 2003/02/25 by wlawless@wlawless_r400_linux_marlboro
Some more changes for mexport, had to bring over 32 bit addr from
depth to use the lsb's ass the quad_offset...

Change 86729 on 2003/02/25 by johnchen@johnchen_r400_linux_marlboro
LEDA fixes

Change 86678 on 2003/02/25 by paulv@paulv_r400_linux_marlboro
Fixed typo.

Change 86591 on 2003/02/24 by johnchen@johnchen_r400_linux_marlboro
refixed some z expand problems

Change 86473 on 2003/02/24 by jayw@jayw_r400_linux_marlboro_rbrc
Some fixes for linear dithering and fog pass detection.

Change 86332 on 2003/02/24 by johnchen@johnchen_r400_linux_marlboro
fix some typos and mexport stuff

Change 86259 on 2003/02/23 by johnchen@johnchen_r400_linux_marlboro
smooth out some plane8 flow

Change 86130 on 2003/02/21 by paulv@paulv_r400_linux_marlboro
Timing/area optimizations.

Change 86037 on 2003/02/21 by hmonsef@hmonsef
Replaced with 8x128

Change 86033 on 2003/02/21 by hmonsef@hmonsef

Replaced 8x120 with 8x128

Change 86032 on 2003/02/21 by hmonsef@hmonsef

Replaces 8x120

Change 86030 on 2003/02/21 by hmonsef@hmonsef

Replace 8x120 by 8x128

Change 86029 on 2003/02/21 by hmonsef@hmonsef

Replaced 8x120 by 8x128

Change 86028 on 2003/02/21 by hmonsef@hmonsef

Replaced 8x120 by 8x128

Change 86027 on 2003/02/21 by hmonsef@hmonsef

Replaced 8x120 by 8x128

Change 86011 on 2003/02/21 by hmonsef@hmonsef

First Rev of test file

Change 85972 on 2003/02/21 by johnchen@johnchen_r400_linux_marlboro

correct plane8 flushing

Change 85958 on 2003/02/21 by wlawless@wlawless_r400_linux_marlboro

fix a double flush on a frag cache line, and something in c_cache?

Change 85708 on 2003/02/20 by paulv@paulv_r400_linux_marlboro

Removed state_soft_reset from tile block, fixed flush signals in tile cache and removed parameter sizes in rb_rbt_hiz_quad_stencil_test.v.

Change 85682 on 2003/02/20 by johnchen@johnchen_r400_linux_marlboro

more mexport fixes

Change 85638 on 2003/02/20 by paulv@paulv_r400_linux_marlboro

Combined writeab_address and writecd_address signals into one signal.

Change 85547 on 2003/02/20 by paulv@paulv_r400_linux_marlboro

Removed parameter sizes (e.g. parameter [1:0] x = 2'b01) and some timing/area fixes.

Change 85521 on 2003/02/20 by wlawless@wlawless_r400_linux_marlboro

memory export type to default state

Change 85458 on 2003/02/20 by johnchen@johnchen_r400_linux_marlboro

fix compression mismatches

Change 85375 on 2003/02/19 by wlawless@wlawless_r400_linux_marlboro

changing version back to previous one

Change 85364 on 2003/02/19 by johnchen@johnchen_r400_linux_marlboro

change op for mexport

Change 85333 on 2003/02/19 by wlawless@wlawless_r400_linux_marlboro

conected AB_RB_type

Change 85332 on 2003/02/19 by paulv@paulv_r400_linux_marlboro

Minor Lint/comment fixes.

Change 85309 on 2003/02/19 by johnchen@johnchen_r400_linux_marlboro

added type on rbd_rbc_quad interface

Change 85281 on 2003/02/19 by wlawless@wlawless_r400_linux_marlboro

more mexport stuff, trpe bit to control state outputs

Change 85276 on 2003/02/19 by dclifton@dclifton_crayola_linux_orl

fixed problem with the assignment of blend_fifo_in for COLOR_16_16 and
COLOR_16_16_FLOAT modes

Change 84927 on 2003/02/18 by wlawless@wlawless_r400_linux_marlboro

added addr search for allocating mexport lines

Change 84923 on 2003/02/18 by paulv@paulv_r400_linux_marlboro

Removed MC_RB_read_source (not used) and made a timing fix to the RBM.

Change 84874 on 2003/02/18 by johnchen@johnchen_r400_linux_marlboro

leda fixes

Change 84695 on 2003/02/16 by paulv@paulv_r400_linux_marlboro

Fixed order of memory patchin/patchout wiring.

Change 84652 on 2003/02/15 by johnchen@johnchen_r400_linux_marlboro_rbrc

timing fixes

Change 84639 on 2003/02/15 by johnchen@johnchen_r400_linux_marlboro_rbrc

reduced sx index to 7 bits

Change 84578 on 2003/02/14 by johnchen@johnchen_r400_linux_marlboro_rbrc

timing fixes

Change 84570 on 2003/02/14 by paulv@paulv_r400_linux_marlboro

Added 2D shadow register support for color_base and pitch.

Change 84482 on 2003/02/14 by johnchen@johnchen_r400_linux_marlboro_rbrc

8 planes bug fix

Change 84294 on 2003/02/13 by johnchen@johnchen_r400_linux_marlboro_rbrc

memory export for depth

Change 84262 on 2003/02/13 by paulv@paulv_r400_linux_marlboro

Added surface synchronization support.

Change 84217 on 2003/02/13 by hmonsef@hmonsef

Recompiled the file the size of the previous file did not look right

Change 84213 on 2003/02/13 by hmonsef@hmonsef

Replaced by 8x120

Change 84211 on 2003/02/13 by hmonsef@hmonsef

Added 8x120 RAM

Change 84210 on 2003/02/13 by hmonsef@hmonsef

Added 8x120 RAM

Change 84209 on 2003/02/13 by hmonsef@hmonsef

Added 8x120 RAM

Change 84208 on 2003/02/13 by hmonsef@hmonsef

Removed the output pipeline registers

Change 84205 on 2003/02/13 by hmonsef@hmonsef

Added 8x120 RAM

Change 84203 on 2003/02/13 by hmonsef@hmonsef

Removed the output registers

Change 84200 on 2003/02/13 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 2-13-02

Change 84199 on 2003/02/13 by hmonsef@hmonsef

Added 8x120 RAM. Removed 8x112 RAM

Change 84189 on 2003/02/13 by wlawless@wlawless_r400_linux_marlboro

mexport stuff, and send the state bit to RMB,,, added a state bit per cache line to the fragment cache

Change 84096 on 2003/02/13 by johnchen@johnchen_r400_linux_marlboro_rbrc

initial memory export

Change 83973 on 2003/02/12 by paulv@paulv_r400_linux_marlboro

Added rbm clean logic.

Change 83785 on 2003/02/12 by wlawless@wlawless_r400_linux_marlboro

mexport to AB is now only 5 bits

Change 83769 on 2003/02/12 by wlawless@wlawless_r400_linux_marlboro

all mexport stuff, endian now 3 bits, and goes through blend bypass

Change 83663 on 2003/02/11 by hmonsef@hmonsef

Includes the latest RAMs as of 2-11-03

Change 83656 on 2003/02/11 by hmonsef@hmonsef

Previous version of the rb_rf_fusebox was incorrect

Change 83651 on 2003/02/11 by hmonsef@hmonsef

Previous version was incorrect

Change 83649 on 2003/02/11 by hmonsef@hmonsef

Previous revision might not have been correct

Change 83615 on 2003/02/11 by jayw@jayw_r400_linux_marlboro

more rb<->sx index size fixes, fog when alpha only.

Change 83557 on 2003/02/11 by wlawless@wlawless_r400_linux_marlboro

added all the busses for memory export, and fixed that pesky inout thing

Change 83435 on 2003/02/10 by johnchen@johnchen_r400_linux_marlboro_rbrc

fix a free_d bug

Change 83383 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 2-10-03

Change 83371 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 10-2-0

Change 83369 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 10-2-03

Change 83367 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 10-2-03

Change 83366 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 10-2-03

Change 83365 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 10-2-03

Change 83363 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 10-2-03

Change 83362 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 10-2-03

Change 83361 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 10-2-03

Change 83360 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 10-2-03

Change 83359 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 10-2-03

Change 83358 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 10-2-03

Change 83357 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 10-2-03

Change 83355 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest RAM compiler as of 10-2-03

Change 83354 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest compiler as of 2-10-03

Change 83353 on 2003/02/10 by paulv@paulv_r400_linux_marlboro

Fixed a bug in the flush logic.

Change 83352 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest Rev of the compiler as of 2-10-03

Change 83351 on 2003/02/10 by hmonsef@hmonsef

Compiled with the latest Rev of the compiler as of 10-2-03

Change 83339 on 2003/02/10 by hmonsef@hmonsef

Recompiling RAMs with the latest REV of the compiler as of 2-10-03

Change 83326 on 2003/02/10 by hmonsef@hmonsef

Module is no longer needed

Change 83325 on 2003/02/10 by hmonsef@hmonsef

The memory module is no longer needed

Change 83324 on 2003/02/10 by hmonsef@hmonsef

The memory module is no longer needed

Change 83255 on 2003/02/10 by johnchen@johnchen_r400_linux_marlboro_rbrc

some fixes for plane4 and 32bits depth

Change 83245 on 2003/02/10 by wlawless@wlawless_r400_linux_marlboro

made it faster....

Change 83222 on 2003/02/10 by paulv@paulv_r400_linux_marlboro

Timing fix.

Change 83206 on 2003/02/10 by wlawless@wlawless_r400_linux_marlboro

Bug in the toggle for block flush on a blend to a non blend line....

Change 83010 on 2003/02/07 by paulv@paulv_r400_linux_marlboro

Removed commented-out code no longer needed.

Change 82930 on 2003/02/07 by jayw@jayw_r400_linux_marlboro

RB_SX#_index is changed from 8 bits to 7 bits. MSB was not being used.

Change 82903 on 2003/02/07 by hmonsef@hmonsef

No longer needed

Change 82901 on 2003/02/07 by hmonsef@hmonsef

No longer needed

Change 82900 on 2003/02/07 by hmonsef@hmonsef

No longer needed

Change 82899 on 2003/02/07 by hmonsef@hmonsef

No longer needed

Change 82898 on 2003/02/07 by hmonsef@hmonsef

No longer needed

Change 82897 on 2003/02/07 by hmonsef@hmonsef

No longer needed

Change 82895 on 2003/02/07 by hmonsef@hmonsef

No longer needed

Change 82893 on 2003/02/07 by hmonsef@hmonsef

It has been replaced

Change 82891 on 2003/02/07 by hmonsef@hmonsef

It has been replaced

Change 82890 on 2003/02/07 by hmonsef@hmonsef

<It has been replaced>

Change 82887 on 2003/02/07 by hmonsef@hmonsef

<It has been replaced>

Change 82869 on 2003/02/07 by paulv@paulv_r400_linux_marlboro

Fixed problem with doing a non-surface read after a surface read (must have broke when I added a pipe stage to meet timing).

Change 82866 on 2003/02/07 by hmonsef@hmonsef

<Removing old RAM>

Change 82865 on 2003/02/07 by hmonsef@hmonsef

<submitting memory updates>

Change 82862 on 2003/02/07 by hmonsef@hmonsef

changing star compiler rev

Change 82824 on 2003/02/07 by wlawless@wlawless_r400_linux_marlboro

timing fix on the force flush off when a non blend line,

Change 82782 on 2003/02/07 by wlawless@wlawless_r400_linux_marlboro

Replaced the flush count with a circuit to search for more then four remaining
Also, I removed first_line_q... this made the path longer..

Change 82725 on 2003/02/06 by paulv@paulv_r400_linux_marlboro

Reduced precision of depthrange calculation blocks. Instead of using the full center
and slope values (from zplane), only need S.3.15 (14 + 1 bit of round). Also fixes for
bad timing path introduced recently in hiz_quad_checker

Change 82505 on 2003/02/06 by johnchen@johnchen_r400_linux_marlboro_rbrc

2D register typo

Change 82485 on 2003/02/06 by johnchen@johnchen_r400_linux_marlboro_rbrc

end of depth expand checkin

Change 82452 on 2003/02/06 by paulv@paulv_r400_linux_marlboro

Fixed signal to star compiler (rb_rfsm_s_stp).

Change 82447 on 2003/02/06 by paulv@paulv_r400_linux_marlboro

Fixed various signal (valids, ptrs, etc.).

Change 82445 on 2003/02/06 by paulv@paulv_r400_linux_marlboro

Removed fuse_box.

Change 82415 on 2003/02/06 by jayw@jayw_r400_linux_marlboro

run alpha for 1 and 2 component colors as 4th color. AB fix for alpha with swapping and gamma on.

Change 82283 on 2003/02/05 by wlawless@wlawless_r400_linux_marlboro

timing

Change 82203 on 2003/02/05 by paulv@paulv_r400_linux_marlboro

Fixed depth_probe_killed equation.

Change 82199 on 2003/02/05 by wlawless@wlawless_r400_linux_marlboro

Added a new do_flush using a count and kick off flush when it hits 60
Added a fix to block frag_tile_id when not in msaa...

Change 82091 on 2003/02/05 by paulv@paulv_r400_linux_marlboro

Added depthclear calculation logic to tile cache.

Change 82089 on 2003/02/05 by paulv@paulv_r400_linux_marlboro

Fixed a typo.

Change 81950 on 2003/02/04 by paulv@paulv_r400_linux_marlboro

Fixed probe read pointer in rb_rbt_tc_fifo and trimmed register decode modules for surface_pitch, surface_slice and tiledata_base (less bits were needed than decoded).

Change 81854 on 2003/02/04 by johnchen@johnchen_r400_linux_marlboro_rbrc

fix a cacheline read problem when fetching pmask

Change 81819 on 2003/02/04 by wlawless@wlawless_r400_linux_marlboro

Changed if_notzero_q to a 64 vector, this was 128 for earlier reasons,
this helps gates and timing, and will be used for EXPORT!

Change 81815 on 2003/02/04 by paulv@paulv_r400_linux_marlboro

Timing fix.

Change 81814 on 2003/02/04 by paulv@paulv_r400_linux_marlboro

Fixed depth_probe_killed equation.

Change 81670 on 2003/02/03 by paulv@paulv_r400_linux_marlboro

Renamed instantiations of address calculators.

Change 81666 on 2003/02/03 by paulv@paulv_r400_linux_marlboro

A few small timing fixes, code reduction and synopsys lint cleanup.

Change 81648 on 2003/02/03 by johnchen@johnchen_r400_linux_marlboro_rbrc

resize access_fifo from 112 to 120

Change 81623 on 2003/02/03 by paulv@paulv_r400_linux_marlboro

Added hardware resolve logic for the AB.

Change 81393 on 2003/02/03 by wlawless@wlawless_r400_linux_marlboro

added full dirty logic, and it appears to be the first lines flushed
Aslo, some more timing fixed in frag_op

Change 81326 on 2003/02/02 by johnchen@johnchen_r400_linux_marlboro_rbrc

modify instantiation name

Change 81325 on 2003/02/02 by johnchen@johnchen_r400_linux_marlboro_rbrc

split rb_rbd_cache to two

Change 81229 on 2003/01/31 by johnchen@johnchen_r400_linux_marlboro_rbrc

last part of z expand logic.....need more debug

Change 81192 on 2003/01/31 by paulv@paulv_r400_linux_marlboro

Another fix for the zmask probe interface.

Change 81169 on 2003/01/31 by paulv@paulv_r400_linux_marlboro

Timing fix.

Change 81139 on 2003/01/31 by paulv@paulv_r400_linux_marlboro

Fixed state machine for doing cmask and zmask reads.

Change 81125 on 2003/01/31 by paulv@paulv_r400_linux_marlboro

Fixed bit mask of cmask,zmask,smask and depthrange values to cache.

Change 81121 on 2003/01/31 by ygiang@ygiang_r400_pv2_marlboro

added: more rb perfcounters

Change 81112 on 2003/01/31 by jayw@jayw_r400_linux_marlboro

Fix bit width bug

Change 81079 on 2003/01/31 by paulv@paulv_r400_linux_marlboro

Removed event_cache_flush, context_done and flush_and_invalidate quantifiers from read_probe_zmask_req and cmask_read_tile_cache signals.

Change 81057 on 2003/01/31 by jayw@jayw_r400_linux_marlboro

2d shadow register for pitch.

Change 81044 on 2003/01/31 by wlawless@wlawless_r400_linux_marlboro

Timing fix, flopped the pixel#_covered bits

Change 81029 on 2003/01/31 by wlawless@wlawless_r400_linux_marlboro

flopped pitch_q for timing, also don't need all the statel's

Change 80901 on 2003/01/30 by paulv@paulv_r400_linux_marlboro

Fixed alpha channel when fog was enabled (should of just passes src_alpha through blender). Wired performance counter signals from AB to RB.

Change 80855 on 2003/01/30 by wlawless@wlawless_r400_linux_marlboro

Removed a mux on out data, now all outputs are flopped, yeeehaaa

Change 80788 on 2003/01/30 by wlawless@wlawless_r400_linux_marlboro

A mega bunch of timing fixes...

Change 80632 on 2003/01/29 by paulv@paulv_r400_linux_marlboro

Added some quad cache performance signals (read and write) and fixed a bug with the zeroing out of inflight counts for writeback to tile cache.

Change 80591 on 2003/01/29 by johnchen@johnchen_r400_linux_marlboro_rbrc

middle part of fzx

Change 80557 on 2003/01/29 by wlawless@wlawless_r400_linux_marlboro

more timing stuff

Change 80338 on 2003/01/29 by paulv@paulv_r400_linux_marlboro_rbrc

Killed quad cache allocations when tile cache kills a tile.

Change 80251 on 2003/01/28 by paulv@paulv_r400_linux_marlboro_rbrc

Forgot to add.

Change 80239 on 2003/01/28 by paulv@paulv_r400_linux_marlboro_rbrc

HiZ logic fixes.

Change 80127 on 2003/01/28 by paulv@paulv_r400_linux_marlboro_rbrc

Resized memory from 8x107 to 8x110.

Change 80113 on 2003/01/28 by johnchen@johnchen_r400_linux_marlboro_rbrc

quaddata_rtr stall fix.

First part of fzx

Change 80048 on 2003/01/28 by wlawless@wlawless_r400_linux_marlboro

a rewrite for timing only

Change 79901 on 2003/01/28 by jayw@jayw_r400_linux_marlboro

Added missing signals.

Change 79897 on 2003/01/28 by jayw@jayw_r400_linux_marlboro

LEDA only fixes. Missing dependencies.

Change 79780 on 2003/01/27 by paulv@paulv_r400_linux_marlboro_rbrc

Fixes for the masks (coarse and hierarchical) for HiZ/HiS.

Change 79760 on 2003/01/27 by paulv@paulv_r400_linux_marlboro_rbrc

LEDA fixes.

Change 79655 on 2003/01/27 by wlawless@wlawless_r400_linux_marlboro

timing fixes

Change 79626 on 2003/01/27 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed timing loop.

Change 79565 on 2003/01/26 by johnchen@johnchen_r400_linux_marlboro_rbrc

timing fixes: add pipe in rb_rbd_depth_compare.v, add pin_pong in
rb_rbd_slope_to_pixel.v
interface addition: add mask to quaddata interface

Change 79420 on 2003/01/24 by paulv@paulv_r400_linux_marlboro_rbrc

Added RBD_RBT_quaddata_mask to quaddata interface in the quad cache and reversed order
of tiles/quads in tile cache/quad cache from 0-1-2-3 to 3-2-1-0.

Change 79388 on 2003/01/24 by johnchen@johnchen_r400_linux_marlboro_rbrc

correct data order

Change 79381 on 2003/01/24 by jayw@jayw_r400_linux_marlboro

2D Shadow register support for pitch.

Change 79331 on 2003/01/24 by johnchen@johnchen_r400_linux_marlboro_rbrc

connect probe_tile_cover bit on top level

Change 79317 on 2003/01/24 by paulv@paulv_r400_linux_marlboro_rbrc

Mainly timing fixes.

Change 79301 on 2003/01/24 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed write_dmask_fifo signal to check all conditions of fast_color_expand.

Change 79287 on 2003/01/24 by jayw@jayw_r400_linux_marlboro

More 2D shadow registers fixes.

Change 79265 on 2003/01/24 by wlawless@wlawless_r400_linux_marlboro

Timing... timing timingtimingtimingtimingtimingtimingtimingtingyy.....

Change 79233 on 2003/01/24 by jayw@jayw_r400_linux_marlboro

Added rb_color_info input to tile logic.

Change 79220 on 2003/01/24 by jayw@jayw_r400_linux_marlboro

More 2D register updates.

Change 79186 on 2003/01/24 by paulv@paulv_r400_linux_marlboro_rbrc

New signal (RBT_RBD_probe_tile_covered) from tile block to depth block.

Change 79183 on 2003/01/24 by paulv@paulv_r400_linux_marlboro_rbrc

Resized 12x44 memory to a 12x45.

Change 79154 on 2003/01/24 by paulv@paulv_r400_linux_marlboro_rbrc

Renamed unwrapper instantiation name to urb_rbt_mc_address_calc_wrapper (the filename).

Change 79106 on 2003/01/23 by johnchen@johnchen_r400_linux_marlboro_rbrc

timing fix for simpler cacheline search algorithm

Change 79105 on 2003/01/23 by jayw@jayw_r400_linux_marlboro

2D shadow register support almost complete.

Change 79087 on 2003/01/23 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed depth_probe_killed equation (whole thing needed to depend on a completely covered tile).

Change 79080 on 2003/01/23 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed a typo.

Change 79074 on 2003/01/23 by jayw@jayw_r400_linux_marlboro

Add missing include file.

Change 79065 on 2003/01/23 by paulv@paulv_r400_linux_marlboro_rbrc

Timing fixes and logic fixes (more fast_logic fixes, dirty vector was going to 'x')

Change 79010 on 2003/01/23 by jayw@jayw_r400_linux_marlboro

Almost done with shadow 2D registers.

Change 78878 on 2003/01/23 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed writing of the dmask_fifo to account for fast_color_expand and cmask_enable.

Change 78877 on 2003/01/23 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed determination of fifo_rptr_probe_*.

Change 78794 on 2003/01/23 by wlawless@wlawless_r400_linux_marlboro

All timing fixes...

Change 78784 on 2003/01/23 by paulv@paulv_r400_linux_marlboro_rbrc

Timing fixes.

Change 78667 on 2003/01/22 by johnchen@johnchen_r400_linux_marlboro

timing fixes

Change 78561 on 2003/01/22 by paulv@paulv_r400_linux_marlboro_rbrc

Timing fixes.

Change 78555 on 2003/01/22 by johnchen@johnchen_r400_linux_marlboro

more plane4 request corrections

Change 78544 on 2003/01/22 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed fifo_rptr_probe_* when write and read occurred at same time (and fifo empty).

Change 78420 on 2003/01/22 by wlawless@wlawless_r400_linux_marlboro

needed a reset on load)inflight_q

Change 78313 on 2003/01/21 by paulv@paulv_r400_linux_marlboro_rbrc

Leda and timing fixes. Also flopped performance counter signals.

Change 78309 on 2003/01/21 by johnchen@johnchen_r400_linux_marlboro

correct some plane4 request logic

Change 78295 on 2003/01/21 by wlawless@wlawless_r400_linux_marlboro

blocked inc_inflight, not needed in ms

Change 78265 on 2003/01/21 by wlawless@wlawless_r400_linux_marlboro

an all new tile_inflight for all the multi-sample stuff

Change 78175 on 2003/01/21 by paulv@paulv_r400_linux_marlboro_rbrc

Removed logic to handle the update of zmask and smask when fast_depth/stencil_clear or fast_depth/stencil_expand were enabled. From my understanding, the depth block handles this.

Change 78123 on 2003/01/21 by wlawless@wlawless_r400_linux_marlboro

added rb_st_info into rb_sx_int

Change 78122 on 2003/01/21 by wlawless@wlawless_r400_linux_marlboro

Bug john chen found,

Change 78077 on 2003/01/20 by paulv@paulv_r400_linux_marlboro_rbrc

Fixes for fast_*_clear and fast_*_expand logic.

Change 77942 on 2003/01/20 by johnchen@johnchen_r400_linux_marlboro

area changes: change rb_rbd_quad_info_sx_fifos to rams

Change 77764 on 2003/01/19 by johnchen@johnchen_r400_linux_marlboro

fix a typo in the cmask write logic

Change 77732 on 2003/01/18 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed tiling4 bit. It was pulling off the wrong bit of st4_0.

Change 77725 on 2003/01/18 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed hier bus when mask entered quad checker as zero and when mask wasn't zero, but tile completely covered.

Change 77646 on 2003/01/17 by paulv@paulv_r400_linux_marlboro_rbrc

For some reason, MH_RB_queuecount was being used in the determination of d_queuecount_agp, instead of the ati_flopped version (q_mh_rb_queuecount). Fixed.

Change 77644 on 2003/01/17 by paulv@paulv_r400_linux_marlboro_rbrc

Timing fix for the queuecount adders/subtractor. Changed the subtractor to a 2's complement add and removed the paranthesis around the add portion so all adders (including the new one for the subtractor) are (hopefully) done together.

Change 77620 on 2003/01/17 by johnchen@johnchen_r400_linux_marlboro

LEDA fixes

Change 77515 on 2003/01/17 by johnchen@johnchen_r400_linux_marlboro

LEDA corrections

Change 77392 on 2003/01/17 by johnchen@johnchen_r400_linux_marlboro

area saving: changing access_fifo to RAM

Change 77374 on 2003/01/17 by wlawless@wlawless_r400_linux_marlboro

quick check in

Change 77296 on 2003/01/16 by jayw@jayw_r400_linux_marlboro

Routing the color_info registers around the RB. for bug 1090

Change 77212 on 2003/01/16 by paulv@paulv_r400_linux_marlboro_rbrc

When tiles killed in tile cache (by covered and fast clear bits), needed to also decrement inflight count for the tile.

Change 77184 on 2003/01/16 by johnchen@johnchen_r400_linux_marlboro

more timing fixes for the flushing logic

Change 77175 on 2003/01/16 by wlawless@wlawless_r400_linux_marlboro

Yet more timing fixes, these were in read arb for frag cache, will cause and extra clk in the read path

Change 77137 on 2003/01/16 by paulv@paulv_r400_linux_marlboro_rbrc

Memory macro config files I either just created or forgot to previously add.

Change 77129 on 2003/01/16 by paulv@paulv_r400_linux_marlboro_rbrc

Replaced ati_fifo with memory macro.

Change 77089 on 2003/01/16 by jayw@jayw_r400_linux_marlboro

Partial fix for bug1090 # RC&RB do not process special 2D

Change 77031 on 2003/01/15 by johnchen@johnchen_r400_linux_marlboro

register performance counter bus

Change 76997 on 2003/01/15 by johnchen@johnchen_r400_linux_marlboro

timing fixes to search algorithm

Change 76953 on 2003/01/15 by wlawless@wlawless_r400_linux_marlboro

multisample stuff:

routed the 3 bit frag_tile_id all over the place... regress5 was mostly passed, some
take
long

Change 76810 on 2003/01/15 by fhsien@fhsien_r400_linux_marlboro

Change MES_ALL parameter to OFF

Change 76787 on 2003/01/15 by johnchen@johnchen_r400_linux_marlboro

some fixes for plane3

Change 76686 on 2003/01/14 by paulv@paulv_r400_linux_marlboro_rbrc

Updated file with all the latest verilog modules.

Change 76685 on 2003/01/14 by paulv@paulv_r400_linux_marlboro_rbrc

More timing fixes.

Change 76623 on 2003/01/14 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed mask when surface disabled.

Change 76554 on 2003/01/14 by johnchen@johnchen_r400_linux_marlboro

correct Paul typo and fix some zmask write back stuff

Change 76521 on 2003/01/14 by paulv@paulv_r400_linux_marlboro_rbrc

Fixes for expand and clear logic.

Change 76518 on 2003/01/14 by wlawless@wlawless_r400_linux_marlboro

timing fot mask_allocate_hit

Change 76506 on 2003/01/14 by wlawless@wlawless_r400_linux_marlboro

timing fix on tag register reads, build mux into the cam_probe_hit

Change 76448 on 2003/01/14 by wlawless@wlawless_r400_linux_marlboro

fixed bus width

Change 76399 on 2003/01/14 by johnchen@johnchen_r400_linux_marlboro

for cache write, change the byte enables to half-byte enables

Change 76309 on 2003/01/13 by johnchen@johnchen_r400_linux_marlboro

write to type_tag when get the latest zmask and also correct cache_write_ready for memory return for PLANE2

Change 76237 on 2003/01/13 by jayw@jayw_r400_linux_marlboro

Remove timing loop hold_non_fog

Change 76184 on 2003/01/13 by wlawless@wlawless_r400_linux_marlboro

fragment probe new logic

Change 76145 on 2003/01/13 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed some signal names (specifically send to snd) in the tile block to make things uniform.

Change 76127 on 2003/01/13 by wlawless@wlawless_r400_linux_marlboro

added a mask one bit to remove a hit address for the first_line search,
A hit inc the inflight from zero and loaded that address into the
first_line_q, the next allocate took a non-zero line...

Change 76099 on 2003/01/13 by johnchen@johnchen_r400_linux_marlboro

LEDA fixes

Change 76095 on 2003/01/13 by johnchen@johnchen_r400_linux_marlboro

prevent latch in rb_rbm.v and some area improvement for depth

Change 76065 on 2003/01/12 by johnchen@johnchen_r400_linux_marlboro

prevent latch in cacheline_pixel_output_ready

Change 76038 on 2003/01/11 by johnchen@johnchen_r400_linux_marlboro

valid_fifo_out should be high when cacheline isn't fetched from memory, but started
it's life as back ground value

Change 75913 on 2003/01/10 by johnchen@johnchen_r400_linux_marlboro

impliment probe_zmask interface for tile block

Change 75878 on 2003/01/10 by jayw@jayw_r400_linux_marlboro

Fixes for color expand being broken.

Change 75845 on 2003/01/10 by ygiang@ygiang_r400_pv_marlboro

added: depth flushes and fills

Change 75712 on 2003/01/10 by jayw@jayw_r400_linux_marlboro

Fix coherence detection for when expand operations are going down the pipe.

Change 75696 on 2003/01/10 by johnchen@johnchen_r400_linux_marlboro

the new zmask interface (implemented on the depth side)

Change 75673 on 2003/01/09 by johnchen@johnchen_r400_linux_marlboro

more perf signals

Change 75672 on 2003/01/09 by johnchen@johnchen_r400_linux_marlboro

more perf signals

Change 75608 on 2003/01/09 by ygiang@ygiang_r400_pv_marlboro

added: 1st rev of perfcounters

Change 75585 on 2003/01/09 by wlawless@wlawless_r400_linux_marlboro

Timing fix in flush addr

Change 75562 on 2003/01/09 by johnchen@johnchen_r400_linux_marlboro

add tile_untouched signal to determind if it is the first time this tile is been touched

Change 75444 on 2003/01/09 by wlawless@wlawless_r400_linux_marlboro

made a force_flush_q for a odd case

Change 75411 on 2003/01/08 by johnchen@johnchen_r400_linux_marlboro

correct the second half of pmask write for memory returns

Change 75346 on 2003/01/08 by ygiang@ygiang_r400_pv_marlboro

backup:compiling rb perf code

Change 75337 on 2003/01/08 by wlawless@wlawless_r400_linux_marlboro

added a mask for first line _q

Change 75296 on 2003/01/08 by jayw@jayw_r400_linux_marlboro

Fix bugzilla 1046. hang of RB

Change 75179 on 2003/01/08 by ygiang@ygiang_r400_pv_marlboro

added:rb perfcounter files

Change 75150 on 2003/01/08 by wlawless@wlawless_r400_linux_marlboro

Created a pixel_res0_fifo for use with c128, timing loop

Change 75035 on 2003/01/07 by wlawless@wlawless_r400_linux_marlboro

just removed a signal that is not needed

Change 74917 on 2003/01/07 by johnchen@johnchen_r400_linux_marlboro

correct access_state_counter_q for some of the plane formats

Change 74882 on 2003/01/07 by wlawless@wlawless_r400_linux_marlboro

I changed all flopped to have _q, this will help timing tracking....

Change 74793 on 2003/01/06 by johnchen@johnchen_r400_linux_marlboro

timing fixes had major typos

Change 74736 on 2003/01/06 by wlawless@wlawless_r400_linux_marlboro

fixed a lonf timing path through first_line

Change 74713 on 2003/01/06 by johnchen@johnchen_r400_linux_marlboro

add RBD performance bus

Change 74681 on 2003/01/06 by johnchen@johnchen_r400_linux_marlboro

put flops on the RBD_RBM_zmask interface. Fixed free_d signal, make sure the cachelines are not dirty before setting free.

Change 74637 on 2003/01/06 by jayw@jayw_r400_linux_marlboro

updated file names

Change 74624 on 2003/01/06 by jayw@jayw_r400_linux_marlboro

Attempted fix for cam.

Change 74609 on 2003/01/06 by jayw@jayw_r400_linux_marlboro

set valid for every valid data, not just once per quad.

Change 74466 on 2003/01/03 by paulv@paulv_r400_linux_marlboro_rbrc

Forgot to change the reset values for the signals of the logic I fixed relating to the rbd write fifo size.

Change 74464 on 2003/01/03 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed signals related to the size of the rbd write fifo (which was changed from 5 to 7).

Change 74436 on 2003/01/03 by paulv@paulv_r400_linux_marlboro_rbrc

Timing fix.

Change 74404 on 2003/01/03 by johnchen@johnchen_r400_linux_marlboro

Flopped signals from rbd to rbm and Paul increased size of rbd write fifo from 5 to 7 to keep the bandwidth.

Change 74382 on 2003/01/03 by wlawless@wlawless_r400_linux_marlboro

got 8 fragment test case to flush all 8 fragments, this has the update of the fragment cache disabled for now..... probing problems!!!

Change 74254 on 2003/01/02 by paulv@paulv_r400_linux_marlboro_rbrc

Timing fixes.

Change 74204 on 2003/01/02 by paulv@paulv_r400_linux_marlboro_rbrc

Added a few performance counter signals from tile_cache in tile up to the top-level rb.

Change 73964 on 2002/12/31 by paulv@paulv_r400_linux_marlboro_rbrc

Small typo. Fixed.

Change 73963 on 2002/12/31 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed all logic dependent of the read and write cam fifo sizes. Also increases rbt write fifo to 7.

Change 73866 on 2002/12/31 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed byte enable to mc for tile write requests.

Change 73562 on 2002/12/27 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed address calculation when doing write requests.

Change 73522 on 2002/12/27 by paulv@paulv_r400_linux_marlboro_rbrc

Had to fix the size of two fifo memories due to addition of a signal.

Change 73421 on 2002/12/26 by johnchen@johnchen_r400_linux_marlboro_rbrc

tile_id send through the tile side of the depth logic (as well as probe side)

Change 73267 on 2002/12/23 by jayw@jayw_r400_linux_marlboro

A minor bug fix for r400rb_tb, a couple LEDA fixes, removed unused internal ports for src_data.

Change 73239 on 2002/12/23 by paulv@paulv_r400_linux_marlboro_rbrc

The cam addr generation ahead of time did not work for a few tests. When I backed out the changes, the tests worked. Some of the mux simplification is still good.

Change 73181 on 2002/12/23 by paulv@paulv_r400_linux_marlboro_rbrc

Timing fixes.

Change 73176 on 2002/12/23 by jayw@jayw_r400_linux_marlboro

Timing fix for AB, moved some deformat muxing to RB.

Change 73144 on 2002/12/23 by wlawless@wlawless_r400_linux_marlboro

changed some fragment probe stuff.. When the op happens before the probe screws around with the inflight counts...

Change 73135 on 2002/12/23 by prunstad@prunstad_r400rb_linux_marlboro

Added some test related I/O for Star memories.

Change 72895 on 2002/12/20 by paulv@paulv_r400_linux_marlboro_rbrc

Hopefully the last fix for the probe logic.

Change 72806 on 2002/12/20 by jayw@jayw_r400_linux_marlboro

Added color busy term to context busies.

Change 72791 on 2002/12/20 by paulv@paulv_r400_linux_marlboro_rbrc

Fixes for probe logic.

Change 72764 on 2002/12/20 by paulv@paulv_r400_linux_marlboro_rbrc

Killed detail kill logic for now (needs more thought) and fixed another issue with the quad cache ram fifos.

Change 72669 on 2002/12/19 by paulv@paulv_r400_linux_marlboro_rbrc

Fixes for when tile buffer is active (timing got out of sync due to changes in fifos).

Change 72544 on 2002/12/19 by paulv@paulv_r400_linux_marlboro_rbrc

Resized the dmask fifo from 117 bits wide to 121.

Change 72515 on 2002/12/19 by jayw@jayw_r400_linux_marlboro

Fix endian8in64

Change 72313 on 2002/12/18 by jayw@jayw_r400_linux_marlboro

Fix some port issues in RC and added new endian codes.

Change 72281 on 2002/12/18 by paulv@paulv_r400_linux_marlboro_rbrc

Old fuse_box file no longer needed.

Change 72275 on 2002/12/18 by paulv@paulv_r400_linux_marlboro_rbrc

Added a new (and should be last) STAR memory to tile block.

Change 72230 on 2002/12/18 by johnchen@johnchen_r400_linux_marlboro_rbrc

timing fixes for depth

Change 72095 on 2002/12/18 by prunstad@prunstad_r400rb_linux_marlboro

A few changes related to new star rf memories.

Change 71973 on 2002/12/17 by paulv@paulv_r400_linux_marlboro_rbrc

New STAR memories added to RB tile block.

Change 71876 on 2002/12/17 by jayw@jayw_r400_linux_marlboro

Fix tri52x52 part 2.

Change 71851 on 2002/12/17 by paulv@paulv_r400_linux_marlboro_rbrc

Timing fixes.

Change 71848 on 2002/12/17 by jayw@jayw_r400_linux_marlboro

Fixes tri52x52 on GC.

Change 71746 on 2002/12/17 by jayw@jayw_r400_linux_marlboro

Top level fixes for the clean and start sync signals and terminating RBBM_be.

Change 71744 on 2002/12/17 by wlawless@wlawless_r400_linux_marlboro

fixed a small bug in fragment probe, cmask was updated in the middle of probe

Change 71703 on 2002/12/17 by jayw@jayw_r400_linux_marlboro

no change.

Change 71702 on 2002/12/17 by jayw@jayw_r400_linux_marlboro

Added memory export FIFO. Should clean up unconnected nets for Top level.

Change 71697 on 2002/12/17 by paulv@paulv_r400_linux_marlboro_rbrc

Timing fixes.

Change 71695 on 2002/12/17 by paulv@paulv_r400_linux_marlboro_rbrc

Changes sclk to sclk_rb.

Change 71598 on 2002/12/16 by johnchen@johnchen_r400_linux_marlboro_rbrc

add CAM for cache coherence on a pixel basis

Change 71374 on 2002/12/16 by wlawless@wlawless_r400_linux_marlboro

added in the 9th quad

Change 71320 on 2002/12/15 by johnchen@johnchen_r400_linux_marlboro_rbrc

fixes for the correct depth 42bits to 16/24bits conversion algorithm

Change 71044 on 2002/12/13 by wlawless@wlawless_r400_linux_marlboro

fixed previous fixes, check in too fast

Change 71027 on 2002/12/13 by wlawless@wlawless_r400_linux_marlboro

sensitivity list

Change 71025 on 2002/12/13 by wlawless@wlawless_r400_linux_marlboro

fixed and inflight decrement problem, found in a depth test

Change 70927 on 2002/12/13 by jayw@jayw_r400_linux_marlboro

AB split changes.

Change 70925 on 2002/12/13 by jayw@jayw_r400_linux_marlboro

Missing always dependency.

Change 70833 on 2002/12/12 by wlawless@wlawless_r400_linux_marlboro

refixed count, again

Change 70831 on 2002/12/12 by paulv@paulv_r400_linux_marlboro_rbrc

Timing fixes for the AB.

Change 70674 on 2002/12/12 by wlawless@wlawless_r400_linux_marlboro

Adjusted which pipe stages are used in the quad in pipe count

Change 70459 on 2002/12/11 by paulv@paulv_r400_linux_marlboro_rbrc

New memories in tile logic.

Change 70414 on 2002/12/11 by johnchen@johnchen_r400_linux_marlboro_rbrc

free the cache line after flushing only if inflight_count is zero

Change 70389 on 2002/12/11 by jayw@jayw_r400_linux_marlboro

changed SX fifo depth to 7

Change 70374 on 2002/12/11 by jayw@jayw_r400_linux_marlboro

added memory export control signals to ab block.

Change 70351 on 2002/12/11 by jayw@jayw_r400_linux_marlboro

resolve for change 70439

Change 70349 on 2002/12/11 by jayw@jayw_r400_linux_marlboro

moved sx color fifos to AB block. RB_SX_color_data. RBRC and GC changed.

Change 70321 on 2002/12/11 by johnchen@johnchen_r400_linux_marlboro_rbrc

connect quad_state_mask signal

Change 70297 on 2002/12/11 by paulv@paulv_r400_linux_marlboro_rbrc

Removed read data ports from rbm to rbc for fragment interface (fragment uses the color interface for its read data).

Change 70156 on 2002/12/10 by paulv@paulv_r400_linux_marlboro_rbrc

Resized a memory in the tile fifo subblock of rbt and added two new memories for large fifos.

Change 69984 on 2002/12/10 by jayw@jayw_r400_linux_marlboro

Added new quad bus endian signal from SX and connected to reg quad number and swap.

Change 69969 on 2002/12/10 by paulv@paulv_r400_linux_marlboro_rbrc

Initialized fifo_count on reset (for some reason, it was removed from reset initialization).

Change 69927 on 2002/12/10 by johnchen@johnchen_r400_linux_marlboro_rbrc

fix multi-sampling case with z and stencil off

Change 69921 on 2002/12/10 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed applicable leda warnings and errors.

Change 69759 on 2002/12/09 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed register reads for incomplete registers (i.e., only needed fields are decoded).

Change 69732 on 2002/12/09 by paulv@paulv_r400_linux_marlboro_rbrc

Alpha blender does not exist under the RB anymore. It is its own top-level block (AB).

Change 69729 on 2002/12/09 by paulv@paulv_r400_linux_marlboro_rbrc

Removed unnecessary flops.

Change 69725 on 2002/12/09 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed surface and nonsurface requests when they occurred back-to-back.

Change 69690 on 2002/12/09 by jayw@jayw_r400_linux_marlboro

Fix stupid blunder by Jay. --jayw

Change 69620 on 2002/12/09 by paulv@paulv_r400_linux_marlboro_rbrc

All alpha blender code is now its own separate block.

Change 69509 on 2002/12/08 by jayw@jayw_r400_linux_marlboro

Primlib_1st_tri runs with new blender.

Change 69433 on 2002/12/07 by paulv@paulv_r400_linux_marlboro_rbrc

Unconnected signals, typos, etc. that were causing bad data to enter color cache.

Change 69397 on 2002/12/06 by jayw@jayw_r400_linux_marlboro

Got fog 2-pass tests working.

Change 69386 on 2002/12/06 by paulv@paulv_r400_linux_marlboro_rbrc

Fixes for the removal of the alpha blender (AB) from the RB.

Change 69306 on 2002/12/06 by wlawless@wlawless_r400_linux_marlboro

up to 9 quads in the pipe

Change 69298 on 2002/12/06 by wlawless@wlawless_r400_linux_marlboro

num of quad in the pipe can be 8 so needed 4 bits

Change 69288 on 2002/12/06 by paulv@paulv_r400_linux_marlboro_rbrc

More removal of the blender functionality from the rb.

Change 69284 on 2002/12/06 by wlawless@wlawless_r400_linux_marlboro

changed y to the blend cam to be 3 bits instead of 4

Change 69257 on 2002/12/06 by wlawless@wlawless_r400_linux_marlboro

cache line early

Change 69245 on 2002/12/06 by wlawless@wlawless_r400_linux_marlboro

project remove blender

Change 69076 on 2002/12/06 by wlawless@wlawless_r400_linux_marlboro

brought up color_flush_event for paul to flush tile cache

Change 69032 on 2002/12/05 by jayw@jayw_r400_linux_marlboro

Back date to rev 12. This causes total failure of RB at present.

Change 69027 on 2002/12/05 by johnchen@johnchen_r400_linux_marlboro

24bits stencil disabled flush correction. Stop writting stencil under this situation.
Put in the correct detail mask info when z and stencil are turned on and write back to
memory under the right situations

Change 68932 on 2002/12/05 by jayw@jayw_r400_linux_marlboro

loop count fix on second pass.

Change 68896 on 2002/12/05 by paulv@paulv_r400_linux_marlboro_rbrc

Slight logic modification for 2-pass fog and multisample (which also needs to do
feedback in the blender).

Change 68847 on 2002/12/05 by wlawless@wlawless_r400_linux_marlboro

Fragment Flush now works.... yeeehhhhaaaaa!

Change 68782 on 2002/12/05 by jayw@jayw_r400_linux_marlboro

clear loop_count and do_fog from reset onward.

Change 68622 on 2002/12/04 by johnchen@johnchen_r400_linux_marlboro

make sure stencil write is dependent on the original detail_mask and coorrect the
stencil data for flush in the case of z24 expanded

Change 68565 on 2002/12/04 by wlawless@wlawless_r400_linux_marlboro

added fragment cache flush

Change 68559 on 2002/12/04 by jayw@jayw_r400_linux_marlboro

New fog control signals to blender. RBM busy fix, RC cache_flush fix. RTR deassert on reset.

Change 68469 on 2002/12/04 by paulv@paulv_r400_linux_marlboro_rbrc

Removed an unused internal signal.

Change 68396 on 2002/12/04 by jayw@jayw_r400_linux_marlboro

blending with less than 4 channels didn't work.

Change 68262 on 2002/12/03 by paulv@paulv_r400_linux_marlboro_rbrc

Moved alpha sat calc submodule to deformat block (done in parallel with degamma).

Change 68205 on 2002/12/03 by johnchen@johnchen_r400_linux_marlboro

LEDA fixes and stencil zpass fixes

Change 68195 on 2002/12/03 by jayw@jayw_r400_linux_marlboro

Fixed RB_RC_cache_flush and improved clock names in rbm.

Added busy to rbm. Plus some Bill fixes.

Change 68188 on 2002/12/03 by wlawless@wlawless_r400_linux_marlboro

color flush stuff

Change 68109 on 2002/12/03 by johnchen@johnchen_r400_linux_marlboro

have the same timing as before putting in multi-sampling

Change 68047 on 2002/12/03 by wlawless@wlawless_r400_linux_marlboro

the if_nonzero bit was being reset with a probe to the same addr

Change 67982 on 2002/12/02 by jayw@jayw_r400_linux_marlboro

leda added missing files, fixed 3D/2D addressing bug in RC.

Change 67938 on 2002/12/02 by johnchen@johnchen_r400_linux_marlboro_rbrc

fix an underflow problem in the rb_rbd_mc_slope_to_pixel.mc. Also added support for multi-sampling of 1 to 4

Change 67868 on 2002/12/02 by jayw@jayw_r400_linux_marlboro

Fix for wrong quad counting in rb_ati_fifo_cam

Change 67800 on 2002/12/02 by wlawless@wlawless_r400_linux_marlboro

Stop the read op if what's in the pipe will make the inflight go to zero
an event cycle will turn this off

Change 67409 on 2002/11/27 by paulv@paulv_r400_linux_marlboro_rbrc

Hopefully one last 2-pass fog blending fix (at least tonight).

Change 67408 on 2002/11/27 by paulv@paulv_r400_linux_marlboro_rbrc

2-pass fog blending fixes and synthesis timing fixes.

Change 67353 on 2002/11/27 by jayw@jayw_r400_linux_marlboro

Real timing fix for 1st stage (deformat) of blender.

Change 67270 on 2002/11/27 by jayw@jayw_r400_linux_marlboro

Moved color format and number state lookup one pipestage earlier.

Change 67224 on 2002/11/27 by jayw@jayw_r400_linux_marlboro

new clock names and improved busy, clean and power mangement.

Change 67130 on 2002/11/26 by paulv@paulv_r400_linux_marlboro_rbrc

LEDA fixes for blender.

Change 67099 on 2002/11/26 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed a problem with the fog counters.

Change 67050 on 2002/11/26 by paulv@paulv_r400_linux_marlboro_rbrc

Fixes for 2-pass fog.

Change 67049 on 2002/11/26 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed 1-bit alpha clamping.

Change 66774 on 2002/11/25 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed an obscure unsigned number bug with 5- and 6-bit formats. Also more area reductions (streamlined uinteger and sinteger formatting).

Change 66773 on 2002/11/25 by paulv@paulv_r400_linux_marlboro_rbrc

Put in the correct bvrl-mc associations.

Change 66679 on 2002/11/25 by wlawless@wlawless_r400_linux_marlboro

Maded the color expand fifo a cam version to stop on blend and a hit in there

Change 66676 on 2002/11/25 by paulv@paulv_r400_linux_marlboro_rbrc

Some number clamping fixes.

Change 66380 on 2002/11/22 by paulv@paulv_r400_linux_marlboro_rbrc

Added sclk_reg for register state decode and changed all regular clocks to sclk_rb. Also made fix for srepeated clamping and blend functions (srcblend, dstblend, comb function).

Change 66364 on 2002/11/22 by jayw@jayw_r400_linux_marlboro

added correct clean signal, update to more correct clocks.

Change 66331 on 2002/11/22 by prunstad@prunstad_r400rb_linux_marlboro

Added delay chain (ati_delay_chain module).

Change 66259 on 2002/11/22 by wlawless@wlawless_r400_linux_marlboro

cmask update to the tile logic is in

Change 65970 on 2002/11/21 by wlawless@wlawless_r400_linux_marlboro

fixed bug816# milestone_tri_rbrc tri32_pix4 f
there was a bug in the inc adjust case statement

Change 65831 on 2002/11/21 by wlawless@wlawless_r400_linux_marlboro

Missing stuff from sensitivity list

Change 65763 on 2002/11/20 by prunstad@prunstad_r400rb_linux_marlboro

Pass through mode connection for STAR memory fixed.

Change 65709 on 2002/11/20 by johnchen@johnchen_r400_linux_marlboro

2.2 clocks per quad...added a 4 deep fifo in depth_compare

Change 65674 on 2002/11/20 by wlawless@wlawless_r400_linux_marlboro

not used

Change 65673 on 2002/11/20 by wlawless@wlawless_r400_linux_marlboro

not used

Change 65609 on 2002/11/20 by paulv@paulv_r400_linux_marlboro_rbrc

Changed precision to 12 bits from 16 bits (mantissa).

Change 65591 on 2002/11/20 by wlawless@wlawless_r400_linux_marlboro

A new cam search instead of a priority encoder

Change 65566 on 2002/11/20 by prunstad@prunstad_r400rb_linux_marlboro

Changed width of scan in and scan out ports.

Change 65534 on 2002/11/20 by prunstad@prunstad_r400rb_linux_marlboro

Added STAR memory processor and fuse box. Added test related I/O.

Change 65524 on 2002/11/20 by wlawless@wlawless_r400_linux_marlboro

removed a CAM yeehaaaa!! but Jay's funky (-) bug is still there for now

Change 65521 on 2002/11/20 by wlawless@wlawless_r400_linux_marlboro

removed a CAM

Change 65453 on 2002/11/20 by johnchen@johnchen_r400_linux_marlboro

fixes for when both depth and stencil are enabled

Change 65379 on 2002/11/19 by paulv@paulv_r400_linux_marlboro_rbrc

Replaces module compiler version.

Change 65378 on 2002/11/19 by paulv@paulv_r400_linux_marlboro_rbrc

Major precision change. Actual blender is only 12-bits precise (was 16-bit), degamma

now 1 cycle and in rb_rbc_pixel_deformat.v, and pipeline shrunk from 15 cycles to 13.

Change 65376 on 2002/11/19 by paulv@paulv_r400_linux_marlboro_rbrc

Removed rb_rbc_degama and added rb_rbc_convert_ieee_to_rb.

Change 65360 on 2002/11/19 by jayw@jayw_r400_linux_marlboro

fixed tiled128_state, tiled128_sel not bitwidth declared.

Change 65340 on 2002/11/19 by jayw@jayw_r400_linux_marlboro

Back to rev 75, to avoid problems with inflights.

Change 65338 on 2002/11/19 by paulv@paulv_r400_linux_marlboro_rbrc

Module compiler module for degama no longer used (keeping the mc code around, though).

Change 65151 on 2002/11/19 by wlawless@wlawless_r400_linux_marlboro

split it into quarters for timing

Change 65148 on 2002/11/19 by wlawless@wlawless_r400_linux_marlboro

fixed with sizes

Change 65144 on 2002/11/19 by paulv@paulv_r400_linux_marlboro_rbrc

Forgot to save last changes (doh!).

Change 65142 on 2002/11/19 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed comments.

Change 65080 on 2002/11/18 by paulv@paulv_r400_linux_marlboro_rbrc

Precision changes to core of blender. Reduced precision from 16 bits to 12 bits. Also includes some other (minor) fixes.

Change 65000 on 2002/11/18 by wlawless@wlawless_r400_linux_marlboro

changed the blend dec of inflight to the other phase

Change 64979 on 2002/11/18 by johnchen@johnchen_r400_linux_marlboro_rbrc

fix a stencil memory mask error..so that the mask would be dependent on detail_mask.
Added support for when both Z and stencil are both enabled

Change 64942 on 2002/11/18 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed tile_fifo_hiz_rtr signal.

Change 64927 on 2002/11/18 by wlawless@wlawless_r400_linux_marlboro

fixed a weird bug for Jay, color mask test

Change 64876 on 2002/11/18 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed large fifo (32x166) being written when full.

Change 64825 on 2002/11/18 by wlawless@wlawless_r400_linux_marlboro

fixed the freeze on overflow

Change 64651 on 2002/11/16 by jayw@jayw_r400_linux_marlboro

ati_fifo_top must be replaced with invocation of star memories.
some leda fixes for depth files. non-functional changes for RC

Change 64609 on 2002/11/15 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed probe logic when surfaces are disabled.

Change 64541 on 2002/11/15 by jayw@jayw_r400_linux_marlboro

blender fix for r400rb_color_mask_channels, dst_minus_src
better busy logic for rc
fix for broken linear pixels.

Change 64493 on 2002/11/15 by johnchen@johnchen_r400_linux_marlboro_rbrc

working stencil pipeline that works with stencil enabled and z disabled. Some small
pixel corruption caused by events

Change 64480 on 2002/11/15 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed surface_enabled signal.

Change 64422 on 2002/11/15 by wlawless@wlawless_r400_linux_marlboro

added the surface_slice gfx reg to the state

Change 64409 on 2002/11/15 by paulv@paulv_r400_linux_marlboro_rbrc

Restructured negate_* muxes. Fixed comb_max/min logic (src_on & dst_on should have
been on).

Change 64318 on 2002/11/15 by jayw@jayw_r400_linux_marlboro

Fix for endian and 128 bit linear tiles.

Change 64301 on 2002/11/15 by wlawless@wlawless_r400_linux_marlboro

fixed timing path

Change 64247 on 2002/11/14 by johnchen@johnchen_r400_linux_marlboro_rbrc

some stencil fixes...for Steve Allen to look at a false pass problem

Change 64150 on 2002/11/14 by paulv@paulv_r400_linux_marlboro_rbrc

LEDA fixes.

Change 64088 on 2002/11/14 by jayw@jayw_r400_linux_marlboro

fix for r400rb_alpha_destination,rbrc,gl_one hangs test
hooked up context_done, untested.

Change 63764 on 2002/11/13 by paulv@paulv_r400_linux_marlboro_rbrc

Added another test case.

Change 63723 on 2002/11/13 by jayw@jayw_r400_linux_marlboro

more busy code and resets are now registered.

Change 63720 on 2002/11/13 by paulv@paulv_r400_linux_marlboro_rbrc

Few signals in verilog model had name changes. Also added any new signal added to verilog.

Change 63688 on 2002/11/13 by wlawless@wlawless_r400_linux_marlboro

fix leda loop

Change 63638 on 2002/11/13 by wlawless@wlawless_r400_linux_marlboro

assign

Change 63636 on 2002/11/13 by wlawless@wlawless_r400_linux_marlboro

frag_write_addr is 5 bits... ooops

Change 63632 on 2002/11/13 by wlawless@wlawless_r400_linux_marlboro

new signal, forget the flush to keep from being stuck in flush

Change 63613 on 2002/11/13 by paulv@paulv_r400_linux_marlboro_rbrc

Fixes to fog counter (alpha blend path is only 4 stages now).

Change 63605 on 2002/11/13 by wlawless@wlawless_r400_linux_marlboro

write back of frag bit during multi-sample

Change 63574 on 2002/11/12 by johnchen@johnchen_r400_linux_marlboro_rbrc

back to about 3 clocks per quad out of depth

Change 63521 on 2002/11/12 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed sinteger number clamping.

Change 63518 on 2002/11/12 by johnchen@johnchen_r400_linux_marlboro_rbrc

fix leda errors

Change 63445 on 2002/11/12 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed some of the clamping logic, specifically with the unsigned integer number case.

Change 63439 on 2002/11/12 by johnchen@johnchen_r400_linux_marlboro_rbrc

fix plane2 format tally

Change 63293 on 2002/11/11 by paulv@paulv_r400_linux_marlboro_rbrc

initial version

Change 63259 on 2002/11/11 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed result when negative value was input (should have been clamped to zero).

Change 63253 on 2002/11/11 by johnchen@johnchen_r400_linux_marlboro

removing 2/3 of the inflight counters because only need one counter for three
cachelines

Change 63172 on 2002/11/11 by paulv@paulv_r400_linux_marlboro_rbrc

Updated 32x166 tile fifo with memory macro.

Change 63157 on 2002/11/11 by johnchen@johnchen_r400_linux_marlboro

removing probe fifo

Change 63121 on 2002/11/11 by paulv@paulv_r400_linux_marlboro_rbrc

Initial version.

Change 63120 on 2002/11/11 by paulv@paulv_r400_linux_marlboro_rbrc

Initial version.

Change 63095 on 2002/11/11 by paulv@paulv_r400_linux_marlboro_rbrc

Area reduction by using only 1 2-input adder (per color channel).

Change 63068 on 2002/11/11 by prunstad@prunstad_r400rb_linux_marlboro

Added ctmc files for Virage STAR memories.

Change 63045 on 2002/11/10 by johnchen@johnchen_r400_linux_marlboro

removing memory receive fifos

Change 63005 on 2002/11/09 by paulv@paulv_r400_linux_marlboro_rbrc

Huge area reduction. Moved adders out of muxing statements and just use 3 at end of each color format mux.

Change 62812 on 2002/11/08 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed rbd_rbt probe interface so that tiles that are color_expanded are also sent.

Change 62792 on 2002/11/08 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed tile_cache_snd when surface_enabled (and a few other outputs) and fixed hiz_snd signal when valid event occurs.

Change 62494 on 2002/11/07 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed clamping of number_gamma values.

Change 62439 on 2002/11/07 by paulv@paulv_r400_linux_marlboro_rbrc

write_page_change was going to 'x' for a few cycles. This was due to a case statement not having all the correct cases.

Change 62306 on 2002/11/07 by wlawless@wlawless_r400_linux_marlboro

2 addresses

Change 62305 on 2002/11/07 by wlawless@wlawless_r400_linux_marlboro

just typical debug

Change 62304 on 2002/11/07 by wlawless@wlawless_r400_linux_marlboro

another cam for dest address

Change 62292 on 2002/11/07 by wlawless@wlawless_r400_linux_marlboro

2 address for multi-sample

Change 62291 on 2002/11/07 by wlawless@wlawless_r400_linux_marlboro

added 2 address's over to c_cache

Change 62246 on 2002/11/07 by johnchen@johnchen_r400_linux_marlboro

able to send events after all the tiles are done

Change 62205 on 2002/11/06 by jayw@jayw_r400_linux_marlboro

Fix for mis-managment of byte enables.

Change 62147 on 2002/11/06 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed shifts for mantissa denormalization (due to largest exp) and subnorm mantissa shift. This would have led to the "buddha" bug from the Chaplin days. In fact, Brian's alpha blender tb shows this problem.

Change 62127 on 2002/11/06 by johnchen@johnchen_r400_linux_marlboro

fix a cache line congestio problem

Change 61926 on 2002/11/06 by paulv@paulv_r400_linux_marlboro_rbrc

Simplification of result_exp check (9 bits instead of 10).

Change 61923 on 2002/11/06 by paulv@paulv_r400_linux_marlboro_rbrc

Clamp output to exp = 1f, mant = ffff when result_exp > 1f.

Change 61884 on 2002/11/05 by paulv@paulv_r400_linux_marlboro_rbrc

Added signal from tile cache to quad cache denoting when a surface is enabled and fixed

the rtr going from the qc to the tc so that inflight tiledata does not get lost.

Change 61883 on 2002/11/05 by paulv@paulv_r400_linux_marlboro_rbrc

Added blend_enable signals for rop3 code and unaligned byte masks. In other words, for certain rop codes and for unaligned byte masks, dst data need to be read from memory in order to get the correct result. The blend_enable signal tells the color cache/color probe block when to get and use dest data.

Change 61800 on 2002/11/05 by johnchen@johnchen_r400_linux_marlboro_rbrc

24bits depth default format change to expanded

Change 61697 on 2002/11/05 by jayw@jayw_r400_linux_marlboro

Increase probe fifo size so that milestone_tri, tril_pix32, gc passes.
The *real* fix must be to remove dependency upon SX data arriving.

Change 61432 on 2002/11/04 by paulv@paulv_r400_linux_marlboro_rbrc

Added alpha_blend_enable and color_blend_enable bypass for 16 bit fp color formats (really only necessary for the -0 case, but necessary nonetheless).

Change 61246 on 2002/11/02 by jayw@jayw_r400_linux_marlboro

fix for multibuffer writes.

Change 61241 on 2002/11/02 by jayw@jayw_r400_linux_marlboro

Many fixes for 128 bit pixels.

Fix for fix for depth testing. (linear works again)

Change 61186 on 2002/11/01 by jayw@jayw_r400_linux_marlboro

tiled 128 bit pixels work.

Change 61084 on 2002/11/01 by wlawless@wlawless_r400_linux_marlboro

connected fragment addr to cam read bus
bug573 is NOT a bug

Change 61050 on 2002/11/01 by jayw@jayw_r400_linux_marlboro

so bill can check in changes.

Change 60977 on 2002/11/01 by jayw@jayw_r400_linux_marlboro

STAR Rams added

Change 60967 on 2002/11/01 by johnchen@johnchen_r400_linux_marlboro_rbrc
a few fixes for 24bit separate format

Change 60962 on 2002/11/01 by prunstad@prunstad_r400rb_linux_marlboro
Top level module for connecting STAR memory test related signals.

Change 60960 on 2002/11/01 by prunstad@prunstad_r400rb_linux_marlboro
Virage STAR memory models.

Change 60922 on 2002/11/01 by jayw@jayw_r400_linux_marlboro
pre STAR rams, 128 bit almost working (probe inc fix pending)

Change 60693 on 2002/10/31 by johnchen@johnchen_r400_linux_marlboro
cacheline_free_q fix: free up more cache lines then just the lines that are flushed

Change 60562 on 2002/10/30 by paulv@paulv_r400_linux_marlboro_rbrc
Fixed fog factor for fog blending.

Change 60434 on 2002/10/30 by paulv@paulv_r400_linux_marlboro_rbrc
Added COLOR_8_8 to NUMBER_UINTEGER and NUMBER_SINTEGER cases in number clamping mux.

Change 60395 on 2002/10/30 by jayw@jayw_r400_linux_marlboro
Fix for bugzilla 595.
Detail killed last tile hangs RB.

Change 59999 on 2002/10/29 by jayw@jayw_r400_linux_marlboro
fix for depth testing marking quads killed.

Change 59895 on 2002/10/29 by johnchen@johnchen_r400_linux_marlboro
depth plane3 compression format

Change 59572 on 2002/10/27 by jayw@jayw_r400_linux_marlboro
full multi-buffer support and a bug fix for multi-buffer and linear??

Change 59531 on 2002/10/26 by jayw@jayw_r400_linux_marlboro

Add N-to-N multibuffer render support.

Change 59358 on 2002/10/25 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed bit mask logic.

Change 59295 on 2002/10/25 by wlawless@wlawless_r400_linux_marlboro

updates for fragment probe

Change 59241 on 2002/10/24 by johnchen@johnchen_r400_linux_marlboro_rbrc

add zmask update support

Change 59182 on 2002/10/24 by paulv@paulv_r400_linux_marlboro_rbrc

Added tile_id to RBT_RBD_probe interface and removed it from RBT_RBD interface.

Change 58668 on 2002/10/22 by jayw@jayw_r400_linux_marlboro

Fixed error blend_bypass to dest_data

Change 58663 on 2002/10/22 by jayw@jayw_r400_linux_marlboro

missing sensitivities

Change 58661 on 2002/10/22 by jayw@jayw_r400_linux_marlboro

fixed missing sensitivities

Change 58658 on 2002/10/22 by jayw@jayw_r400_linux_marlboro

fixed again.

Change 58657 on 2002/10/22 by johnchen@johnchen_r400_linux_marlboro_rbrc

more plane stuff...stall the access fifo correctly and loading pmask correctly

Change 58653 on 2002/10/22 by paulv@paulv_r400_linux_marlboro_rbrc

Fix for negative src and dst values when combination function is max or min.

Change 58650 on 2002/10/22 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed bypass logic for 32-bit/channel pixels (COLOR_32_FLOAT, COLOR_32_32_FLOAT, COLOR_32_32_32_FLOAT).

Change 58649 on 2002/10/22 by jayw@jayw_r400_linux_marlboro

port error num_multiwrite

Change 58610 on 2002/10/22 by paulv@paulv_r400_linux_marlboro_rbrc

Correctly connect c_blend_dest_data_valid signal from rb_c_cache to rb_rbc_blender.

Change 58593 on 2002/10/22 by jayw@jayw_r400_linux_marlboro

synth edits for msprague.

sx quad buss, added state code

Change 58586 on 2002/10/22 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed output of color_expand bit when a surface is enabled.

Change 58564 on 2002/10/22 by jayw@jayw_r400_linux_marlboro

fix for tiling0 on non buffer 0 states.

Change 58495 on 2002/10/22 by paulv@paulv_r400_linux_marlboro_rbrc

Fix for when srcblend == ALPHA_SATURATE.

Change 58421 on 2002/10/21 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed selector of bypass data. Was using wrong version of blend_bypass signal.

Change 58409 on 2002/10/21 by jayw@jayw_r400_linux_marlboro_rbrc

"Purposely editing directly out of parts_lib_candidate"

revert back to version 15

Change 58375 on 2002/10/21 by paulv@paulv_r400_linux_marlboro_rbrc

Instead of creating another bit (nondirty), used blend_requires_dst in rb_c_cache.v, sent to blender as dest_data_valid and masked out pixel_mask if ((d_format_data == bypass_data) && !blend_bypass && dest_data_valid).

Change 58351 on 2002/10/21 by paulv@paulv_r400_linux_marlboro_rbrc

Added format_data_nondirty output. This signal denotes that the output pixel data did not change from the input dest data.

Change 58341 on 2002/10/21 by jayw@jayw_r400_linux_marlboro_rbrc

fixes for multi-buffer

Change 58288 on 2002/10/21 by paulv@paulv_r400_linux_marlboro_rbrc

Removed unneeded input (not used here).

Change 58285 on 2002/10/21 by jayw@jayw_r400_linux_marlboro_rbrc

Fix for 8 bit linear formats

Change 58218 on 2002/10/20 by jayw@jayw_r400_linux_marlboro_rbrc

multi-buffer support, untested
appears to not kill regular sims.

Change 58203 on 2002/10/20 by jayw@jayw_r400_linux_marlboro_rbrc

blank line removed

Change 58191 on 2002/10/20 by jayw@jayw_r400_linux_marlboro

fix setting dirty after I added optimization

Change 58182 on 2002/10/20 by jayw@jayw_r400_linux_marlboro_rbrc

linear now does 8 8 8 8 pixels

Change 58177 on 2002/10/19 by johnchen@johnchen_r400_linux_marlboro

more plane stuff and memory receive queue refix

Change 58171 on 2002/10/19 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed src_on and dst_on for when the respective blend function is alpha_saturate.

Change 58101 on 2002/10/18 by jayw@jayw_r400_linux_marlboro

removed read byte enables, never used.

Change 58098 on 2002/10/18 by paulv@paulv_r400_linux_marlboro_rbrc

Fixes for alpha saturate and corresponding logic.

Change 57812 on 2002/10/17 by jayw@jayw_r400_linux_marlboro

LEDA fix and some code fixes for linear

Change 57664 on 2002/10/17 by paulv@paulv_r400_linux_marlboro_rbrc

Removed NaN and infinity support.

Change 57649 on 2002/10/17 by jayw@jayw_r400_linux_marlboro

Tile surface disable implemented and
some linear surface debug started.

Change 57642 on 2002/10/17 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed reading of the bypass fifof when surface disabled.

Change 57528 on 2002/10/16 by paulv@paulv_r400_linux_marlboro_rbrc

Tile send signal for surface disabled case needed to take into account if the tile fifo
was being read.

Change 57521 on 2002/10/16 by jayw@jayw_r400_linux_marlboro

added ql4_pixel_valid to reset.

Change 57504 on 2002/10/16 by paulv@paulv_r400_linux_marlboro_rbrc

Some fixes related to the new surface_enabled code.

Change 57487 on 2002/10/16 by paulv@paulv_r400_linux_marlboro_rbrc

If a no surface is enabled (cmask, smask, etc.), tile cache now passes through valid
tiles.

Change 57486 on 2002/10/16 by paulv@paulv_r400_linux_marlboro_rbrc

Removed some commented out logic that is no longer necessary.

Change 57242 on 2002/10/15 by jayw@jayw_r400_linux_marlboro

new state machine for linear addressing

Change 57228 on 2002/10/15 by jayw@jayw_r400_linux_marlboro

Linear code added and simmed

Change 57194 on 2002/10/15 by jayw@jayw_r400_linux_marlboro

more linear additions and leda fixes.

Change 57162 on 2002/10/15 by johnchen@johnchen_r400_linux_marlboro_rbrc

correct cache_write_address for planel

Change 57138 on 2002/10/15 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed hang condition when surface_enabled is true and event occurs.

Change 57096 on 2002/10/15 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed blender result when combination function either COMB_MAX_DST_SRC or COMB_MIN_DST_SRC.

Change 57067 on 2002/10/15 by jayw@jayw_r400_linux_marlboro

More LEDA changes

Change 57046 on 2002/10/15 by johnchen@johnchen_r400_linux_marlboro_rbrc

more LEDA errors

Change 57040 on 2002/10/14 by jayw@jayw_r400_linux_marlboro

Fixed missing LEDA sensitivity list warnings.

Change 56989 on 2002/10/14 by johnchen@johnchen_r400_linux_marlboro_rbrc

LEDA errors

Change 56965 on 2002/10/14 by johnchen@johnchen_r400_linux_marlboro_rbrc

fix race condition

Change 56961 on 2002/10/14 by jayw@jayw_r400_linux_marlboro

aggressive LEDA 'fixes'

Change 56959 on 2002/10/14 by johnchen@johnchen_r400_linux_marlboro_rbrc

planel format

Change 56956 on 2002/10/14 by jayw@jayw_r400_linux_marlboro

includes USE_BEHAVE_MEM

Change 56952 on 2002/10/14 by jayw@jayw_r400_linux_marlboro

fixed file list to just those used.

Change 56939 on 2002/10/14 by paulv@paulv_r400_linux_marlboro_rbrc

Added tiledone and all other related signals from RBD to RBT.

Change 56933 on 2002/10/14 by jayw@jayw_r400_linux_marlboro

LEDA fixes

Change 56911 on 2002/10/14 by jayw@jayw_r400_linux_marlboro

more LEDA fixes.

Change 56897 on 2002/10/14 by paulv@paulv_r400_linux_marlboro_rbrc

Some lines went way beyond the max column width (I think its 132). Wrapped the violating lines to meet this criteria.

Change 56895 on 2002/10/14 by paulv@paulv_r400_linux_marlboro_rbrc

Added some new files.

Change 56874 on 2002/10/14 by jayw@jayw_r400_linux_marlboro

removed rb_ati_fifo_cam.v

Change 56873 on 2002/10/14 by jayw@jayw_r400_linux_marlboro

made signal the right size.

Change 56871 on 2002/10/14 by jayw@jayw_r400_linux_marlboro

fixed missing signal.

Change 56860 on 2002/10/14 by jayw@jayw_r400_linux_marlboro

some leda fixes.

Change 56705 on 2002/10/11 by jayw@jayw_r400_linux_marlboro

reconciled bill's and my changes. for 2d linear addressing and different numbers of pipes.

Change 56704 on 2002/10/11 by jayw@jayw_r400_linux_marlboro

Many changes for 2D Linear.

Removed the split signal rb <-> rc, not needed.

Change 56673 on 2002/10/11 by paulv@paulv_r400_linux_marlboro_rbrc

Made separate CAM fifos due to varying FIFO_DEPTH sizes (fifos are no longer parameterized, with the exception of skid and fifo_depth).

Change 56672 on 2002/10/11 by paulv@paulv_r400_linux_marlboro_rbrc

Couldn't use when FIFO_DEPTH < 16 because of synthesis problems with CAM logic (couldn't make CAM logic parameterized). There are now separate versions due to this problem.

Change 56663 on 2002/10/11 by jayw@jayw_r400_linux_marlboro

Added necessary reset for frag_rts.

Change 56401 on 2002/10/10 by wlawless@wlawless_r400_linux_marlboro

fragment stuff

Change 56366 on 2002/10/10 by paulv@paulv_r400_linux_marlboro_rbrc

Needed to check the color format when determining the final_alpha/blue/green/red values if the color_number was srepeat.

Change 56359 on 2002/10/10 by wlawless@wlawless_r400_linux_marlboro

fixed timing loops

Change 56343 on 2002/10/10 by jayw@jayw_r400_linux_marlboro

endian logic for color cache.

Change 56306 on 2002/10/10 by johnchen@johnchen_r400_linux_marlboro

fix blocking statement

Change 56187 on 2002/10/09 by jayw@jayw_r400_linux_marlboro

added rb_id and two new signals from SX. set to 0 in chip.tree for now.
added some state logic in probe for more linear work.
potential fix for 8_B color and 8_8 color formats.

Change 56148 on 2002/10/09 by wlawless@wlawless_r400_linux_marlboro

fragment edits, state machine to probe F0, cmask buss edits snd to rts

Change 56080 on 2002/10/09 by jayw@jayw_r400_linux_marlboro_rbrc

New register name fixes. Plane Mask and ARRAY_3D_COLOR

Change 56073 on 2002/10/09 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed valid_request signal generation to MC.

Change 56058 on 2002/10/09 by paulv@paulv_r400_linux_marlboro_rbrc

Added coherency for tile requests and cleaned up read and write state machines.

Change 56055 on 2002/10/09 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed cmask expand value from 2 to 1 (when cmask_enabled and a background value (0) is detected upon output of a tile from the tile cache).

Change 55983 on 2002/10/08 by johnchen@johnchen_r400_linux_marlboro

more plane formats and debugged some cacheline allocation issues

Change 55918 on 2002/10/08 by paulv@paulv_r400_linux_marlboro_rbrc

Changed cmask read interface. The snd is now and rts since the color only sends a new read request when the previous one has been satisfied. Therefore, no rtr is necessary and there is no need to hold requests in the tile cache.

Change 55867 on 2002/10/08 by wlawless@wlawless_r400_linux_marlboro

cmask read and write busses

Change 55851 on 2002/10/08 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed cmask write for tile cache (if detected as background) only when cmask_enable is set.

Change 55833 on 2002/10/08 by paulv@paulv_r400_linux_marlboro_rbrc

Some minor changes for fragment signals to RBC from RBT.

Change 55811 on 2002/10/08 by wlawless@wlawless_r400_linux_marlboro

fixed a bug....

Change 55778 on 2002/10/08 by paulv@paulv_r400_linux_marlboro_rbrc

When cmask is detected to be zero upon output to rbc (via hiz and rbd), now write it correctly (?), depending on whether msaa_enable is true or not.

Change 55739 on 2002/10/07 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed a bug with GL_ONE_MINUS_DST_ALPHA.

Change 55698 on 2002/10/07 by paulv@paulv_r400_linux_marlboro_rbrc

Few fixes, including the addition of fast_stencil_expand (not in rb_reg.v yet, but have a temporary define) and the addition of the flush and invalidate defines from vgt_reg.v.

Change 55697 on 2002/10/07 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed the masking of correct data into the ram.

Change 55647 on 2002/10/07 by paulv@paulv_r400_linux_marlboro_rbrc

Logic fixes to correct coherency between reads and writes.

Change 55378 on 2002/10/04 by paulv@paulv_r400_linux_marlboro_rbrc

Added a flop stage to get close to meeting timing and to remove 2 timing loops.

Change 55370 on 2002/10/04 by wlawless@wlawless_r400_linux_marlboro

some fragment updates

Change 55359 on 2002/10/04 by wlawless@wlawless_r400_linux_marlboro

nothing but remove comments

Change 55357 on 2002/10/04 by wlawless@wlawless_r400_linux_marlboro

flush bug, removed do_autoflush from loading the flush addr reg

Change 55354 on 2002/10/04 by jayw@jayw_r400_linux_marlboro

added some code for pipe disable.

Change 55251 on 2002/10/04 by wlawless@wlawless_r400_linux_marlboro

fixed loops

Change 55216 on 2002/10/03 by johnchen@johnchen_r400_linux_marlboro

add 24 float format

Change 55192 on 2002/10/03 by jayw@jayw_r400_linux_marlboro

added 2d linear, untested.

runs tri32_pix4 and r400rb_color_combination.

Change 55122 on 2002/10/03 by paulv@paulv_r400_linux_marlboro_rbrc

Changed reset of rtrs out to blocks outside the tile cache to 0 from 1.

Change 55064 on 2002/10/03 by wlawless@wlawless_r400_linux_marlboro

fixed a bunch of LEDA stuff...

Change 55058 on 2002/10/03 by jayw@jayw_r400_linux_marlboro

no functional change formatting only.

Change 55057 on 2002/10/03 by jayw@jayw_r400_linux_marlboro

no functional change, formatting only.

Change 55046 on 2002/10/03 by paulv@paulv_r400_linux_marlboro_rbrc

Added tiledata rtr between quad cache and tile cache and added zmask interface between RBT and RBD.

Change 55045 on 2002/10/03 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed alpha blender for combination functions COMB_MAX_DST_SRC and COMB_MIN_DST_SRC.

Change 54933 on 2002/10/02 by johnchen@johnchen_r400_linux_marlboro

initial zplane formats check in

Change 54927 on 2002/10/02 by paulv@paulv_r400_linux_marlboro_rbrc

Added smask to rbt_rbd probe interface.

Change 54917 on 2002/10/02 by paulv@paulv_r400_linux_marlboro_rbrc

Added logic to make the 10_11_11 and 11_11_10 color formats blendable (as per the register spec and an inquiry with Larry).

Change 54865 on 2002/10/02 by paulv@paulv_r400_linux_marlboro_rbrc

Removed a few unconnected and unused signals.

Change 54816 on 2002/10/02 by paulv@paulv_r400_linux_marlboro_rbrc

Turned on fragment fifo (was not being written to because the msaa_enable bit in its corresponding register was not being initialized for all tests).

Change 54801 on 2002/10/02 by jayw@jayw_r400_linux_marlboro

first draft of color mask and plane mask, ready for debug.

runs r400_basic_color_combination comb_dst_plus_src

Change 54799 on 2002/10/02 by wlawless@wlawless_r400_linux_marlboro

Added some Multi-sample cache line lock bits..

Change 54795 on 2002/10/02 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed alignment of color fmts that are missing at least 1 component (e.g., 8_8, 8_A, 16_16, etc.).

Change 54775 on 2002/10/02 by jayw@jayw_r400_linux_marlboro

Format change only

Change 54744 on 2002/10/02 by wlawless@wlawless_r400_linux_marlboro

fixed the double flush

Change 54713 on 2002/10/01 by jayw@jayw_r400_linux_marlboro

Fixed byte enables.

Change 54686 on 2002/10/01 by jayw@jayw_r400_linux_marlboro

removed some duplicate lines from previous resolve

Change 54673 on 2002/10/01 by jayw@jayw_r400_linux_marlboro

fixed bad resolve

Change 54646 on 2002/10/01 by jayw@jayw_r400_linux_marlboro

color cache now uses 128 bit byte enables.
lots of code for masking color writes, not hooked up fully
some fixes for color formats

Change 54628 on 2002/10/01 by wlawless@wlawless_r400_linux_marlboro

Added 2 tag bits for each cache line that is valid for a non_blend operation
This will force a mc request if a blend hits the line...

Change 54623 on 2002/10/01 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed alpha for 1-bit and 2-bit cases.

Change 54590 on 2002/10/01 by paulv@paulv_r400_linux_marlboro_rbrc

Fixed pix_res1 case statement to include 2_10_10_10 color format.