

Change 96254 on 2003/04/17 by bhankins@bhankins_crayola_linux_orl

fix syntax error affecting synthesis

Change 96193 on 2003/04/17 by bhankins@bhankins_crayola_linux_orl

edits to support the replacement of sx_rb_detailed_quad_data_fifo

Change 96189 on 2003/04/17 by viviana@viviana_crayola_unix_orl

A new memory was added to the sx and all the simulation verilog models were checked into the test directory.

Change 96187 on 2003/04/17 by bhankins@bhankins_crayola_linux_orl

initial checkin to replace register based fifo with ram based; clone of ati_fifo_top.v

Change 96186 on 2003/04/17 by viviana@viviana_crayola_unix_orl

A new memory was added to the sx (128x104).

Change 95498 on 2003/04/14 by scamlin@scamlin_crayola_unix_orl

typo

Change 95432 on 2003/04/14 by mmantor@mmantor_crayola_linux_orl

<removed duplicate declaration>

Change 94920 on 2003/04/10 by ygjiang@ygjiang_r400_pv2_marlboro

changed: naming of sx perf counters

Change 94917 on 2003/04/10 by bhankins@bhankins_crayola_win_orl

move sample time negedge clk

Change 94901 on 2003/04/10 by bhankins@bhankins_crayola_win_orl

initial add

Change 94867 on 2003/04/10 by bhankins@bhankins_crayola_win_orl

update to enable injector to generate back to back exports for more robust testing. currently disabled.

Change 94830 on 2003/04/09 by mmantor@mmantor_crayola_linux_orl

<SQ/SX/SP out of order thread completion and remove redundant storage in sp for sq/sx communications some sq cfs bug fixed and texture kill mask generation and other misc things>

Change 94648 on 2003/04/09 by bhankins@bhankins_crayola_win_orl

remove unused tracker

Change 94480 on 2003/04/08 by bhankins@bhankins_crayola_win_orl

update to current test hierarchy

Change 94467 on 2003/04/08 by bhankins@bhankins_crayola_win_orl

reduce random wait time

Change 94436 on 2003/04/08 by bhankins@bhankins_crayola_win_orl

parameterize random wait times

Change 94399 on 2003/04/08 by bhankins@bhankins_crayola_win_orl

1. fix bug in tbmod_fake_rb to properly read request size value from dumps.2. fix test bench to properly detect a good end of test condition.

Change 94394 on 2003/04/08 by bhankins@bhankins_crayola_win_orl

remove unused signal

Change 94239 on 2003/04/07 by bhankins@bhankins_crayola_win_orl

add some randomization to parameter cache deallocs

Change 94222 on 2003/04/07 by bhankins@bhankins_crayola_win_orl

fix to allow the issue of parameter cache deallocates regardless of the number of lines available.

Change 94187 on 2003/04/07 by bhankins@bhankins_crayola_win_orl

1. accomodate the pc_channel_mask column that was added to sp_sx.dmp2. remove references to sq_sx_pc.dmp (former home of pc_channel_mask)

Change 94178 on 2003/04/07 by bhankins@bhankins_crayola_win_orl

misc changes

Change 93951 on 2003/04/04 by bhankins@bhankins_crayola_win_orl

fix to properly get to end of file

Change 93335 on 2003/04/02 by bhankins@bhankins_crayola_win_orl

misc fixes

Change 92622 on 2003/03/28 by bhankins@bhankins_crayola_win_orl

misc fixes and cleanup

Change 92615 on 2003/03/28 by bhankins@bhankins_crayola_win_orl

initial add. very simple. probably will need work.

Change 92610 on 2003/03/28 by grayc@grayc_crayola_linux_orl

new tracker

Change 92554 on 2003/03/28 by bhankins@bhankins_crayola_win_orl

misc fixes and updates. passes mini-regress tests.

Change 92303 on 2003/03/27 by mmantor@mmantor_crayola_linux_orl

<export_id expanded from one to four bit, end_of_clause added to CFS-TIF interface and last_in_clause flag passed down with instruction to eventually trigger a free_done. thread_id outputs added to the SQ_SP and SQ_TP interfaces for testbench, and added sq_sx control signals for exp_table read >

Change 92192 on 2003/03/26 by bhankins@bhankins_crayola_win_orl

obsolete

Change 92191 on 2003/03/26 by bhankins@bhankins_crayola_win_orl

ongoing work. some perf and sp tests pass.paramater cache reads pass, but tbmod_fake_int.v may need some work.

Change 91453 on 2003/03/21 by bhankins@bhankins_crayola_win_orl

snoop into the sx's to get an rtr to throttle requests from the fake pa so as not to blow out the request fifo in the sx.

Change 91267 on 2003/03/20 by bhankins@bhankins_crayola_win_orl

get in sync with gui.

Change 91266 on 2003/03/20 by bhankins@bhankins_crayola_linux_orl

ok for now, but still needs work in properly detecting which pixels to track.

Change 91238 on 2003/03/20 by bhankins@bhankins_crayola_linux_orl

seems to be working.

Change 91234 on 2003/03/20 by bhankins@bhankins_crayola_linux_orl

seems to be working.

Change 91233 on 2003/03/20 by bhankins@bhankins_crayola_linux_orl

update. showing signs of life.

Change 91232 on 2003/03/20 by bhankins@bhankins_crayola_linux_orl

do paramater cache reads

Change 91230 on 2003/03/20 by bhankins@bhankins_crayola_linux_orl

starting to show signs of working

Change 91010 on 2003/03/19 by bhankins@fl_bhankins_r400_win

work in progress. dont use.

Change 90997 on 2003/03/19 by bhankins@bhankins_crayola_linux_orl

temp version; work in progress. do not use.

Change 90875 on 2003/03/19 by bhankins@fl_bhankins_r400_win

mods

Change 90865 on 2003/03/19 by bhankins@fl_bhankins_r400_win

updates

Change 90594 on 2003/03/17 by grayc@grayc_crayola_linux_orl

changed number of pipe stages for valid to 8

Change 90460 on 2003/03/17 by bhankins@fl_bhankins_r400_win

fixed to ignore don't care transfers

Change 90253 on 2003/03/14 by bhankins@fl_bhankins_r400_win

revert back to earlier version until new version is tested

Change 90250 on 2003/03/14 by bhankins@fl_bhankins_r400_win

obsolete

Change 90249 on 2003/03/14 by bhankins@fl_bhankins_r400_win

initial checkin

Change 90247 on 2003/03/14 by bhankins@fl_bhankins_r400_win

mods

Change 89936 on 2003/03/13 by bhankins@fl_bhankins_r400_win

add comment

Change 89935 on 2003/03/13 by bhankins@fl_bhankins_r400_win

simplify.

Change 89863 on 2003/03/12 by mmantor@mmantor_crayola_linux_orl

<Fix bug in the sx export buffers that prevents data corruption. Single color exports should now work also added a test to the sq_regress file to check export buffer back pressure>

Change 89448 on 2003/03/10 by mmantor@mmantor_crayola_linux_orl

<1. Added timestamp to dum_mem read and write from same location error message. 2. Moved flat/gouroud shading and provoking vertex to sq-pc from the sx and worked on ptr instead of data 3.Added control for the texture cylindrical wrapsubcycling. 4. Add rt parameter cache ptr selection in sq 5.Clamped and wrapped pc_ptrs in sq 6. Added support for points and lines in the parameter cache ptr determination. 7. prep seperate write address for export to memory 8. tmp fix for deallocation of export memory deallocation. 9. remove some old comment out code and redundant logic >

Change 89352 on 2003/03/10 by bhankins@fl_bhankins_r400_win

edits

Change 89344 on 2003/03/10 by bhankins@fl_bhankins_r400_win

changes

Change 89341 on 2003/03/10 by bhankins@bhankins_crayola_linux_orl
changes

Change 89340 on 2003/03/10 by bhankins@bhankins_crayola_linux_orl
changes

Change 88999 on 2003/03/07 by rramsey@rramsey_crayola_linux_orl
fix dum_mem instance to be 128 deep

Change 88989 on 2003/03/07 by bhankins@bhankins_crayola_linux_orl
update

Change 88762 on 2003/03/06 by bhankins@bhankins_crayola_linux_orl
updates

Change 88761 on 2003/03/06 by bhankins@bhankins_crayola_linux_orl
updates

Change 88760 on 2003/03/06 by bhankins@bhankins_crayola_linux_orl
updates

Change 88759 on 2003/03/06 by bhankins@bhankins_crayola_linux_orl
update to support 'size' field of sx_rb_quad.dmp

Change 88758 on 2003/03/06 by bhankins@bhankins_crayola_linux_orl
moved to ./tb_sx

Change 88756 on 2003/03/06 by bhankins@fl_bhankins_r400_win
moving to ./tb_sx

Change 88755 on 2003/03/06 by bhankins@bhankins_crayola_linux_orl
moved from ../

Change 88754 on 2003/03/06 by bhankins@bhankins_crayola_linux_orl
moved from ../

Change 88753 on 2003/03/06 by bhankins@bhankins_crayola_linux_orl

moved from ../

Change 88752 on 2003/03/06 by bhankins@bhankins_crayola_linux_orl

initial checkin

Change 88745 on 2003/03/06 by bhankins@bhankins_crayola_linux_orl

added support for reading the 'size' field. This is not a signal, and is not checked.

Change 88005 on 2003/03/03 by bhankins@fl_bhankins_r400_win

checkin ongoing work

Change 87617 on 2003/02/28 by viviana@viviana_crayola_linux_orl

Sx/Rb interface tracker for debugging.

Change 87599 on 2003/02/28 by bhankins@fl_bhankins_r400_win

work in progress

Change 87440 on 2003/02/27 by mmang@mmang_crayola_linux_orl

Predicated parameter cache writes.

Change 87391 on 2003/02/27 by viviana@viviana_crayola_linux_orl

Removed redundancy from the tracker due to adding two sx's at the unit block level test.

Change 87339 on 2003/02/27 by bhankins@fl_bhankins_r400_win

support for 2 sx's

Change 87329 on 2003/02/27 by bhankins@fl_bhankins_r400_win

back up work in progress

Change 87268 on 2003/02/27 by asutkows@asutkows_r400_sun_marlboro

Removed SX_pipe_we, SX_pipe_re, SX_pipe_a, SX_pipe_wd, and RBBM_be

Change 87265 on 2003/02/27 by asutkows@asutkows_r400_sun_marlboro

Added assign sx_rb_quad_pixel = 4'b0;

Change 87260 on 2003/02/27 by asutkows@asutkows_r400_sun_marlboro

Commented out the the statement assign sx_rb_quad_pixel = 4'b0;

Change 86979 on 2003/02/26 by bhankins@fl_bhankins_r400_win

initial checkin of misc files; starting some VERY initial and basic work on a test bench for sx

Change 86706 on 2003/02/25 by asutkows@asutkows_r400_sun_marlboro

duplicates of input1_x, and input2_x in the default section.

Change 86688 on 2003/02/25 by viviana@viviana_crayola_linux_orl

Increased the number of registration stages for the trigger signal to compare the outputs.

Corrected the size of the actual mantisa buses.

Change 86459 on 2003/02/24 by viviana@viviana_crayola_linux_orl

Orlando Interface trackers for the sx/pa sx/sp interfaces.

Change 86404 on 2003/02/24 by asutkows@asutkows_r400_sun_marlboro

Enabled the pipeline outputs for instance sx_stdfrfsdks2p80x128cm2sw0.

Change 86025 on 2003/02/21 by jmarsano@R400_FUSEBOXES_JMARSANO

PTA files for sp and sx

Change 85816 on 2003/02/21 by askende@askende_r400_linux_marlboro

checking in a change related to rb index request buffers.

fixed a typo in the export allocation logic.

Change 84866 on 2003/02/17 by askende@askende_r400_linux_marlboro

various changes

Change 84718 on 2003/02/17 by askende@askende_r400_linux_marlboro

releasing fixes related to :

1. FLOOR and FRACT scalar opcodes

2. inserted output flops on the vertex parameter bus going from SX to SP

interpolators

Change 84462 on 2003/02/14 by markf@markf_r400_linux_marlboro

Removed double flopping of `sx_rb_index_rtr` in `sx_export_control.v`. Added a couple of simple fill rate tests.

Change 83140 on 2003/02/09 by askende@askende_r400_linux_marlboro

removed the old `alloc/dealloc` export interface

Change 83131 on 2003/02/09 by askende@askende_r400_linux_marlboro

fix related to `alloc/dealloc` logic

Change 82449 on 2003/02/06 by asutkows@asutkows_r400_sun_marlboro

`set pipe_line_read = 0`

Change 82435 on 2003/02/06 by asutkows@asutkows_r400_sun_marlboro

`set pipe_line_read = 1`

Change 82432 on 2003/02/06 by asutkows@asutkows_r400_sun_marlboro

`set pipe_line_read = 0`

Change 81866 on 2003/02/04 by askende@askende_r400_linux_marlboro

fixed a problem related to `alloc/dealloc` of the dummy quads in `SX`

Change 81733 on 2003/02/04 by askende@askende_r400_linux_marlboro

releasing a fix related to accounting for dummy quads in the export allocation/deallocation logic

Change 81151 on 2003/01/31 by askende@askende_r400_linux_marlboro

releasing the top level change for the export `SX-SQ` interface

Change 81102 on 2003/01/31 by rramsey@rramsey_crayola_linux_orl

update `pc_ctl` to handle prims with duplicate `ptrs` (lines)

Change 80320 on 2003/01/29 by askende@askende_r400_linux_marlboro

added a ``include` for the define types

Change 80003 on 2003/01/28 by askende@askende_r400_linux_marlboro

releasing sx changes related to pix kill mask logic

Change 79903 on 2003/01/28 by askende@askende_r400_linux_marlboro

releasing a new top level

Change 79710 on 2003/01/27 by askende@askende_r400_linux_marlboro

submitting changes

Change 79573 on 2003/01/26 by askende@askende_r400_linux_marlboro

releasing changes to the interpolator logic to fix a timing path

Change 79519 on 2003/01/26 by askende@askende_r400_linux_marlboro

changes/fixes related to :

1. cylindrical wrap logic in SX
2. behavior of log_ieee when dealing with corner values inputs like -0,+0 and

inf

Change 79190 on 2003/01/24 by askende@askende_r400_linux_marlboro

removed a v2k only statement

Change 79150 on 2003/01/24 by askende@askende_r400_linux_marlboro

releasing a fix for the read back strobe of the rbbm interface

Change 78998 on 2003/01/23 by scamlin@scamlin_crayola_win

unit test of virage star test bus

Change 78905 on 2003/01/23 by desiree@desiree_r400_sun_marlboro

added assigns and memory changes for WEMA addition due to swl

Change 78881 on 2003/01/23 by desiree@desiree_r400_sun_marlboro

Fixed MEB in memory by tieing signals and removing vdd

Change 78842 on 2003/01/23 by desiree@desiree_r400_sun_marlboro

Ripped out Fusebox, related signals, and matched Test Team spec

Change 78517 on 2003/01/22 by desiree@desiree_r400_sun_marlboro

128x128 with subword of 1

Change 78512 on 2003/01/22 by desiree@desiree_r400_sun_marlboro

new fusebox ctmc generated with virage/3220 release

Change 78509 on 2003/01/22 by desiree@desiree_r400_sun_marlboro

new fusebox for Jan. 14th release with name change

Change 78502 on 2003/01/22 by desiree@desiree_r400_sun_marlboro

Updated for virage/3220 conventions

Change 78496 on 2003/01/22 by desiree@desiree_r400_sun_marlboro

Changed memory to subword of 1

Change 78493 on 2003/01/22 by desiree@desiree_r400_sun_marlboro

processor generated from Jan. 14th virage/3220 release

Change 78491 on 2003/01/22 by desiree@desiree_r400_sun_marlboro

Jan. 14th release of virage SW

Change 78488 on 2003/01/22 by desiree@desiree_r400_sun_marlboro

Generated with Jan. 14th release of virage SW

Change 78477 on 2003/01/22 by desiree@desiree_r400_sun_marlboro

reflects Jan 14th virage/3220

Change 78461 on 2003/01/22 by askende@askende_r400_linux_marlboro

fixes related to:

1. mask/kill logic
2. alloc/dealloc logic in SX

Change 78092 on 2003/01/20 by askende@askende_r400_linux_marlboro

1. modified tied in the SX - SQ export status interface
2. fixed a bug related to using the free_done signal to increament the number of full outstanding positon vectors

Change 77214 on 2003/01/16 by desiree@desiree_r400_sun_marlboro

SX control file that conforms to virage/3220 and project conventions

Change 76621 on 2003/01/14 by brianf@brianf_r400_linux_marlboro

SX performance counters

Change 76620 on 2003/01/14 by brianf@brianf_r400_linux_marlboro

Resolved merge issue

Change 76619 on 2003/01/14 by brianf@brianf_r400_linux_marlboro

Added Performance counters

Change 76112 on 2003/01/13 by askende@askende_r400_linux_marlboro

releasing changes related to :

1. scalar engine
2. export quad index logic in SX

Change 76039 on 2003/01/11 by askende@askende_r400_linux_marlboro

releasing fixes related to :

1. daizy chain rbbm read back strobe
2. detailed quad buffer instance updated to the latest ati_fifo

Change 75503 on 2003/01/09 by askende@askende_r400_linux_marlboro

releasing changes related to :

1. scalar add precision
2. sx unit id assignemet

Change 75478 on 2003/01/09 by askende@askende_r400_linux_marlboro

changes related to RBBM interface

Change 75237 on 2003/01/08 by askende@askende_r400_linux_marlboro

fixed a typo related to rbbm_busy

Change 74891 on 2003/01/07 by askende@askende_r400_linux_marlboro

released a fix for the SX_RBBM_busy signal

Change 73806 on 2002/12/30 by askende@askende_r400_linux_marlboro

releasing changes

Change 72600 on 2002/12/19 by askende@askende_r400_linux_marlboro

removed the extra flop at the output quad bus from SX to RB

Change 71550 on 2002/12/16 by askende@askende_r400_linux_marlboro

top level clean up

Change 71365 on 2002/12/16 by desiree@desiree_r400_sun_marlboro

Fixed STAR_rf_recen from linter

Change 70724 on 2002/12/12 by askende@askende_r400_linux_marlboro

submitting changes related to virage memories, fuse box and processor

Change 70656 on 2002/12/12 by desiree@desiree_r400_sun_marlboro

submitting additional logic related to fuse boxes and other stuff related to start memories

Change 70618 on 2002/12/11 by askende@askende_r400_linux_marlboro

submitting changes related to top leve IO

Change 70477 on 2002/12/11 by desiree@desiree_r400_sun_marlboro

fusebox for sx

Change 70476 on 2002/12/11 by desiree@desiree_r400_sun_marlboro

ctmc for fusebox for sx

Change 69950 on 2002/12/10 by askende@askende_r400_linux_marlboro

checking top level changes and renaming

Change 68632 on 2002/12/04 by desiree@desiree_r400_sun_marlboro

processor model

Change 68627 on 2002/12/04 by desiree@desiree_r400_sun_marlboro

new ctmc for processor, fusebox, and delay chain

Change 68626 on 2002/12/04 by desiree@desiree_r400_sun_marlboro

new ctmc for processor, fusebox, and delay chain

Change 68625 on 2002/12/04 by desiree@desiree_r400_sun_marlboro

new ctmc for processor, fusebox, and delay chain

Change 68616 on 2002/12/04 by desiree@desiree_r400_sun_marlboro

new ctmc file with processor, fusebox, and turning off output registers

Change 68610 on 2002/12/04 by desiree@desiree_r400_sun_marlboro

added changes for processor signals

Change 68609 on 2002/12/04 by desiree@desiree_r400_sun_marlboro

added changes for processor signals

Change 68607 on 2002/12/04 by desiree@desiree_r400_sun_marlboro

added changes to memories for processor signals

Change 68605 on 2002/12/04 by desiree@desiree_r400_sun_marlboro

added changes to memories for processor signals

Change 68604 on 2002/12/04 by desiree@desiree_r400_sun_marlboro

added changes to memories to support processor

Change 68603 on 2002/12/04 by desiree@desiree_r400_sun_marlboro

new version of file for 128x78 memory to match ctmc

Change 68602 on 2002/12/04 by desiree@desiree_r400_sun_marlboro

added processor, fusebox, and delay chain

Change 67620 on 2002/11/29 by askende@askende_r400_linux_marlboro

releasing changes related to support special FP values like NaN and Inf

Change 66775 on 2002/11/25 by askende@askende_r400_linux_marlboro

submitting changes related to special value inputs (NaNs and Inf) in the interpolators
as well as macc unit

Change 65215 on 2002/11/19 by askende@askende_r400_linux_marlboro

fixed bug768 and 772 (skid depth set to 4 and fixed a typo related to rb_ready signal
looking at rtr(s) coming from all RBs)

Change 64865 on 2002/11/18 by pmitchel@pmitchel_entire_depot_win

mv files for Desiree

Change 64722 on 2002/11/17 by desiree@desiree_r400_sun_marlboro

adding star memeory logic

Change 64657 on 2002/11/16 by askende@askende_r400_linux_marlboro

changed the reg [127:0] q0_vtx_diff20 to reg [131:0] q0_vtx_diff20

Change 64146 on 2002/11/14 by desiree@desiree_r400_sun_marlboro

submitting start memory related logic

Change 64145 on 2002/11/14 by desiree@desiree_r400_sun_marlboro

submitting start memory related logic

Change 64134 on 2002/11/14 by desiree@desiree_r400_sun_marlboro

submitting star memory related logic

Change 64133 on 2002/11/14 by desiree@desiree_r400_sun_marlboro

submitting star memory related logic

Change 64131 on 2002/11/14 by desiree@desiree_r400_sun_marlboro

merging in the star compiler related changes

Change 64074 on 2002/11/14 by askende@askende_r400_linux_marlboro

added a few default assignments in missing in one of the case statements

Change 61222 on 2002/11/02 by askende@askende_r400_linux_marlboro

backing up changes

Change 56454 on 2002/10/10 by askende@askende_r400_linux_marlboro

1. fix in SX clipper request data routing between two different banks of export
bufffers

2. fix in sp_macc32 related to FRACT opcode

Change 55974 on 2002/10/08 by askende@askende_r400_linux_marlboro

fixed a bug related to false detection of the pixel exports from SP to SX

Change 55805 on 2002/10/08 by askende@askende_r400_linux_marlboro

fixed a problem related to PA requesting position from two different export buffer banks sequentially

Change 55142 on 2002/10/03 by askende@askende_r400_linux_marlboro

fixed a bug related to parameterized instantiation of dum_mem_p2 modules
from sx_export_buffers.v

Change 54684 on 2002/10/01 by askende@askende_r400_linux_marlboro

changed the width of read_index in alloc_dealloc to 7

Change 54664 on 2002/10/01 by askende@askende_r400_linux_marlboro

brought back a fix that was removed by mistake

Change 54633 on 2002/10/01 by askende@askende_r400_linux_marlboro

syntax fixes

Change 54629 on 2002/10/01 by askende@askende_r400_linux_marlboro

changes related to default cases within a case statement