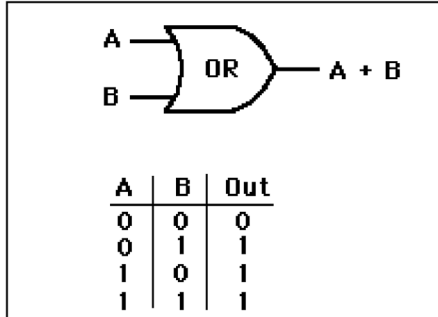


The Wayback Machine - <https://web.archive.org/web/20040303155843/http://hyperphysics.phy-astr.gsu.edu:8...>

OR Gate

The output is high when either or both of inputs A or B is high. This is logically different from the [exclusive OR](#)



The OR operation will be signified by $A + B$. Other common mathematical notations for it are $A \vee B$ and $A \cup B$, called the union of A and B.

[How do you make one?](#)

[IC Example: IC 7432](#)

[Basic Gates](#)

[Index](#)

[Electronics concepts](#)

[Digital Circuits](#)

[HyperPhysics](#)*****[Electricity and magnetism](#)

R Nave

[Go Back](#)

Making an OR Gate

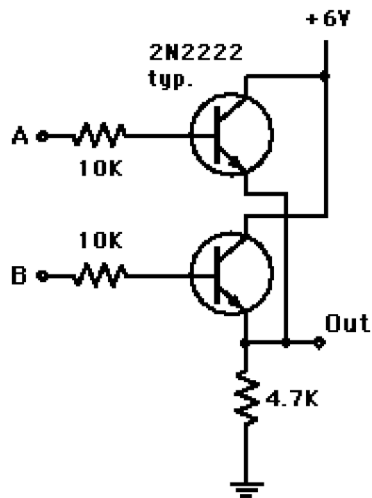
These are ways to make an [OR gate](#) from transistor switches or diodes.

[Index](#)

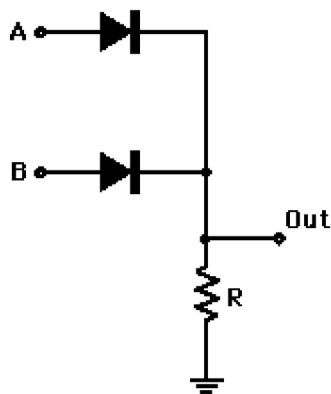
[Electronics concepts](#)

[Digital Circuits](#)

Transistor OR Gate

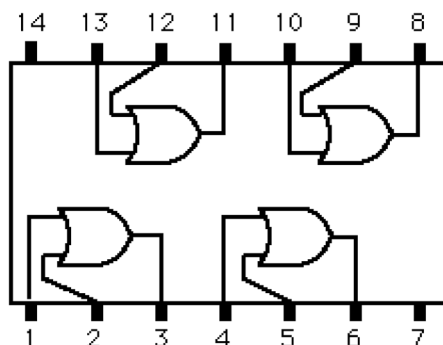


Diode OR Gate


[Basic Gates](#)
[HyperPhysics](#)*****[Electricity and magnetism](#)
*R
Nave*
[Go Back](#)

IC 7432 OR Gates

This is an example of convenient packaging of [OR gates](#) in integrated circuit form.


[Basic Gates](#) [IC Logic Circuits](#)
[Index](#)
[Electronics
concepts](#)
[Digital
Circuits](#)
[HyperPhysics](#)*****[Electricity and magnetism](#)
*R
Nave*
[Go Back](#)

