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APPLICATION NO.	ISSUE DATE	PATENT NO.	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/818,765	10/16/2018	10102449		6002

116094 7590 09/26/2018

Jasmin Cosic
108 Woodbury Street
Pawtucket, RI 02861

ISSUE NOTIFICATION

The projected patent number and issue date are specified above.

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)
(application filed on or after May 29, 2000)

The Patent Term Adjustment is 0 day(s). Any patent to issue from the above-identified application will include an indication of the adjustment on the front page.

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Application Assistance Unit (AAU) of the Office of Data Management (ODM) at (571)-272-4200.

APPLICANT(s) (Please see PAIR WEB site http://pair.uspto.gov for additional applicants):

Jasmin Cosic, Miami, FL;

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Tesla Exhibit 1002.001

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	12	20150310041		2015-10-29	Kier; Scott ; et al.	
	13	20160328480		2016-11-10	Owens; Erich James ; et al.	
	14	20140075249		2014-03-13	SATO; Shuhei ; et al.	
	15	20110270794		2011-11-03	Drory; Tal ; et al.	
	16	20070050719		2007-03-01	Lui; Philip ; et al.	
	17	20070061735		2007-03-15	Hoffberg; Steven M. ; et al.	
Change(s) applied to document, /J.G./ 7/16/2018	18	20060184410		08-2006 ~~2017-08-17~~	Ramamurthy; Shankar ; et al.	
	19	20060265406		2006-11-23	Chkodrov; Gueorgui B. ; et al.	
	20	20090287643		2009-11-19	Corville; Allen O. ; et al.	
	21	20110007079		2011-01-13	Perez et al.	
	22	20070050606		2007-03-01	Ferren et al.	

EFS Web 2.1.17

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail **Mail Stop ISSUE FEE**
Commissioner for Patents
P.O. Box 1450
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INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

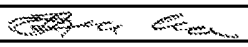
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116094 7590 07/06/2018

Jasmin Cosic
108 Woodbury Street
Pawtucket, RI 02861

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I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

Jasmin Cosic	(Depositor's name)
	(Signature)
September 1, 2018	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/818,765	11/21/2017	Jasmin Cosic		6002

TITLE OF INVENTION: DEVICES, SYSTEMS, AND METHODS FOR USE IN AUTOMATION

APPLN. TYPE	ENTITY STATUS	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	SMALL	$500	$0	$0	$500	10/09/2018

EXAMINER	ART UNIT	CLASS-SUBCLASS
PARK, SOO JIN	2668	382-218000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

❑ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

❑ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. **Use of a Customer Number is required.**

2. For printing on the patent front page, list

(1) The names of up to 3 registered patent attorneys or agents OR, alternatively,

(2) The name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 _____
2 _____
3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent) : ❑ Individual ❑ Corporation or other private group entity ❑ Government

4a. The following fee(s) are submitted:

☒ Issue Fee

❑ Publication Fee (No small entity discount permitted)

❑ Advance Order - # of Copies _____

4b. Payment of Fee(s): **(Please first reapply any previously paid issue fee shown above)**

❑ A check is enclosed.

☒ Payment ~~by credit card. Form PTO-2038 is attached~~. Online payment via EFS.

❑ The director is hereby authorized to charge the required fee(s), any deficiency, or credits any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).

5. **Change in Entity Status** (from status indicated above)

❑ Applicant certifying micro entity status. See 37 CFR 1.29

NOTE: Absent a valid certification of Micro Entity Status (see forms PTO/SB/15A and 15B), issue fee payment in the micro entity amount will not be accepted at the risk of application abandonment.

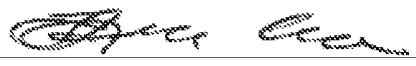
❑ Applicant asserting small entity status. See 37 CFR 1.27

NOTE: If the application was previously under micro entity status, checking this box will be taken to be a notification of loss of entitlement to micro entity status.

❑ Applicant changing to regular undiscounted fee status.

NOTE: Checking this box will be taken to be a notification of loss of entitlement to small or micro entity status, as applicable.

NOTE: This form must be signed in accordance with 37 CFR 1.31 and 1.33. See 37 CFR 1.4 for signature requirements and certifications.

Authorized Signature  Date September 1, 2018

Typed or printed name Jasmin Cosic Registration No. _____

PTOL-85 Part B (10-13) Approved for use through 10/31/2013. OMB 0651-0033 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Tesla Exhibit 1002.003

Electronic Patent Application Fee Transmittal

Application Number:	15818765
Filing Date:	21-Nov-2017
Title of Invention:	DEVICES, SYSTEMS, AND METHODS FOR USE IN AUTOMATION
First Named Inventor/Applicant Name:	Jasmin Cosic
Filer:	Jasmin Cosic
Attorney Docket Number:	

Filed as Small Entity

Filing Fees for Utility under 35 USC 111(a)

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
UTILITY APPL ISSUE FEE	2501	1	500	500

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Extension-of-Time:				
Miscellaneous:				
			Total in USD ($)	**500**

Electronic Acknowledgement Receipt

EFS ID:	33609437
Application Number:	15818765
International Application Number:	
Confirmation Number:	6002
Title of Invention:	DEVICES, SYSTEMS, AND METHODS FOR USE IN AUTOMATION
First Named Inventor/Applicant Name:	Jasmin Cosic
Customer Number:	116094
Filer:	Jasmin Cosic
Filer Authorized By:	
Attorney Docket Number:	
Receipt Date:	01-SEP-2018
Filing Date:	21-NOV-2017
Time Stamp:	22:06:42
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	CARD
Payment was successfully received in RAM	$500
RAM confirmation Number	090418INTEFSW22090500
Deposit Account	
Authorized User	
The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	PTOL-85B_Issue_Fee_Form_flattened.pdf	2830046 ffa32bd9840117b1e6e0d123cd5f386e49c18728	no	1
Warnings:					
Information:					
2	Fee Worksheet (SB06)	fee-info.pdf	29561 ad3305245a1775cf9e1be505fa1fcc095704 47e9	no	2
Warnings:					
Information:					
		Total Files Size (in bytes):	2859607		

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/818,765	11/21/2017	Jasmin Cosic		6002

116094 7590 08/02/2018

Jasmin Cosic
108 Woodbury Street
Pawtucket, RHODE ISLAND 02861
UNITED STATES OF AMERICA

EXAMINER	
PARK, SOO JIN	

ART UNIT	PAPER NUMBER
2668	

MAIL DATE	DELIVERY MODE
08/02/2018	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Tesla Exhibit 1002.008

Supplemental ***Notice of Allowability***	**Application No.** 15/818,765	**Applicant(s)** Cosic, Jasmin	
	Examiner SOO JIN PARK	**Art Unit** 2668	**AIA Status** Yes

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☑ This communication is responsive to interview on 08/01/2018.
   ☐ A declaration(s)/affidavit(s) under **37 CFR 1.130(b)** was/were filed on _____.

2. ☐ An election was made by the applicant in response to a restriction requirement set forth during the interview on _____; the restriction requirement and election have been incorporated into this action.

3. ☑ The allowed claim(s) is/are 1-20 . As a result of the allowed claim(s), you may be eligible to benefit from the **Patent Prosecution Highway** program at a participating intellectual property office for the corresponding application. For more information, please see **http://www.uspto.gov/patents/init_events/pph/index.jsp** or send an inquiry to **PPHfeedback@uspto.gov.**

4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

   Certified copies:

   a) ☐ All b) ☐ Some *c) ☐ None of the:

   1. ☐ Certified copies of the priority documents have been received.
   2. ☐ Certified copies of the priority documents have been received in Application No. _____ .
   3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

   * Certified copies not received: _____ .

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file areply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
   ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____ .

   Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date _____.
3. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material _____.
4. ☑ Interview Summary (PTO-413), Paper No./Mail Date. 1 August 2018.
5. ☑ Examiner's Amendment/Comment
6. ☑ Examiner's Statement of Reasons for Allowance
7. ☐ Other _____.

/Soo Jin Park/
Primary Examiner, Art Unit 2668

Tesla Exhibit 1002.009

Notice of Pre-AIA or AIA Status

The present application, filed on or after March 16, 2013, is being examined under the first inventor to file provisions of the AIA.

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee. Authorization for this examiner's amendment was given in an interview with Jasmin Cosic on 08/01/2018.

Claims 1-20 are amended as follows:

1. A system ~~for autonomous device operating, the system~~ comprising:

one or more processor circuits;

a memory ~~unit~~ that stores at least a first one or more digital pictures correlated with a first one or more instructions sets for operating a first physical device, wherein the first physical device includes an actuator for moving at least a portion of the first physical device, and wherein at least a portion of the first one or more digital pictures or at least a portion of the first one or more instruction sets for operating the first physical device are learned in a learning process that includes operating the first physical device at least partially by a user;

an optical camera that captures digital pictures; and

an artificial intelligence unit that:

receives a new one or more digital pictures from the optical camera;

anticipates the first one or more instruction sets for operating the first physical device based on at least partial match between the new one or more digital pictures and the first one or more digital pictures, wherein the anticipates includes at least one of: determining that a number of at least partially matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold number, or determining that a percentage of at least partially matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold percentage; and

causes the one or more processor circuits to execute the first one or more instruction sets for operating the first physical device, wherein the causes is performed in response to the anticipates of the artificial intelligence unit, and wherein the first physical device or a second physical device autonomously performs one or more operations defined by the first one or more instruction sets for operating the first physical device.

2. The system of Claim 1, wherein the first one or more instruction sets for operating the first physical device are obtained from an application for operating the first physical device using at least a tracing of the application for operating the first physical device, wherein the application for operating the first physical device runs on the one or more processor circuits or on another one or more processor circuits.

3. ~~The system of Claim 1, wherein the first digital picture is not used for identifying a user's behavior.~~ The system of Claim 1, wherein the first one or more instruction sets for operating the first physical device are obtained, via a hardwired connection, from at least one portion of the one or more processor circuits or from at least one portion of another one or more processor circuits, and wherein the one or more processor circuits or the another one or more processor circuits are used at least for operating the first physical device.

4. The system of Claim 1, wherein the causes the one or more processor circuits to execute the first one or more instruction sets for operating the first physical device includes: ~~inserting~~

instrumenting an application for operating the first physical device with the first one or more instruction sets for operating the first physical device, or

instrumenting an application for operating the second physical device with the first one or more instruction sets for operating the first physical device ~~into an application for operating the physical device using at least an instrumentation of the application for operating the physical device~~, and wherein the application for operating the first physical device or the application for operating the second physical device runs on the one or more processor circuits.

5. ~~The system of Claim 1, wherein the physical device does not include an avatar of an application program, and wherein the optical camera does not include a renderer~~

~~configured to render streams of digital pictures of a surrounding of the avatar of the application program.~~ The system of Claim 1, wherein the causes the one or more processor circuits to execute the first one or more instruction sets for operating the first physical device includes inserting, via a hardwired connection, the first one or more instruction sets for operating the first physical device into at least one portion of the one or more processor circuits.

6. ~~The system of Claim 1, wherein the memory unit further stores at least a second digital picture correlated with a second one or more instruction sets for operating the physical device.~~ The system of Claim 1, wherein the second physical device autonomously performs one or more operations defined by the first one or more instruction sets for operating the first physical device, and wherein the first one or more instruction sets for operating the first physical device are modified so that the second physical device can autonomously perform one or more operations defined by the first one or more instruction sets for operating the first physical device.

7. ~~The system of Claim 1, wherein the first digital picture is a comparative digital picture whose at least one portion is compared with at least one portion of one or more digital pictures subsequent to the first digital picture.~~ The system of Claim 1, wherein the portions of the new one or more digital pictures include portions of the new one or more digital pictures that depict recognized objects and the portions of the first one or more digital pictures include portions of the first one or more digital pictures that depict recognized objects.

8. ~~The system of Claim 1, wherein the new digital picture is an anticipatory digital picture whose at least one portion is compared with at least one portion of one or more digital pictures prior to the new digital picture.~~ The system of Claim 1, wherein the first physical device includes a robot, a vehicle, an appliance, an electronic device, or a mechanical machine and the second physical device includes a robot, a vehicle, an appliance, an electronic device, or a mechanical machine, and wherein the one or more processor circuits are included in at least one of: the first physical device, the second physical device, or a third device.

9. ~~The system of Claim 1, wherein the first digital picture correlated with the first one or more instruction sets for operating the physical device are learned at least by determining that the first one or more instruction sets for operating the physical device temporally correspond to the first digital picture.~~ The system of Claim 1, wherein the memory further stores at least a second one or more digital pictures correlated with a second one or more instruction sets for operating the second physical device, and wherein at least a portion of the second one or more digital pictures or at least a portion of the second one or more instruction sets for operating the second physical device are learned in a second learning process that includes operating the second physical device at least partially by a second user.

10. The system of Claim 1, wherein the first one or more digital pictures includes ~~a~~ one or more still digital pictures or [[a]] one or more motion digital pictures and the

new one or more digital pictures include one or more still digital pictures or one or more motion digital pictures.

11. The system of Claim 1, wherein the artificial intelligence unit includes at least one of: a hardware element that is included in the one or more processor circuits, a hardware element that is included in ~~another~~ a second one or more processor circuits, an application operating on the one or more processor circuits, an application operating on ~~another~~ a third one or more processor circuits, or an element coupled to the one or more processor circuits, and wherein the one or more processor circuits include one or more microcontrollers.

12. The system of Claim 1, wherein the memory ~~unit~~ further stores at least a knowledge structure~~, and wherein the knowledge structure~~ that includes: a first knowledge cell including the first one or more digital pictures correlated with the first one or more instruction sets for operating the first physical device, and a second knowledge cell including a second one or more digital pictures correlated with a second one or more instruction sets for operating the first physical device, and wherein the first knowledge cell is connected with the second knowledge cell by a directed connection that indicates temporal order of the first knowledge cell and the second knowledge cell.

13. A non-transitory ~~computer storage~~ machine readable medium having ~~a computer program~~ stored thereon, ~~the computer program including~~ instructions that

when executed by one or more processor circuits cause the one or more processor circuits to perform operations comprising:

accessing a memory ~~unit~~ that stores at least a first one or more digital pictures correlated with a first one or more instructions sets for operating a first physical device, wherein the first physical device includes an actuator for moving at least a portion of the first physical device, and wherein at least a portion of the first one or more digital pictures or at least a portion of the first one or more instruction sets for operating the first physical device are learned in a learning process that includes operating the first physical device at least partially by a user;

receiving a new one or more digital pictures from an optical camera;

anticipating the first one or more instruction sets for operating the first physical device based on at least partial match between the new one or more digital pictures and the first one or more digital pictures, wherein the anticipating includes at least one of: determining that a number of at least partially matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold number, or determining that a percentage of at least partially matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold percentage; and

causing the one or more processor circuits or another one or more processor circuits ~~an execution of~~ to execute the first one or more instruction sets for operating the first physical device, the causing performed in response to the anticipating, wherein the first physical device or a second physical device autonomously performs one or more

operations defined by the first one or more instruction sets for operating the first physical device.

14. ~~The non-transitory computer storage medium of Claim 13, wherein the causing the execution of the first one or more instruction sets for operating the physical device includes inserting the first one or more instruction sets for operating the physical device into an application for operating the physical device using at least an instrumentation of the application for operating the physical device, wherein the application for operating the physical device runs on the one or more processor circuits or on another one or more processor circuits.~~ The non-transitory machine readable medium of Claim 13, wherein the second physical device autonomously performs one or more operations defined by the first one or more instruction sets for operating the first physical device, and wherein the first one or more instruction sets for operating the first physical device are modified so that the second physical device can autonomously perform one or more operations defined by the first one or more instruction sets for operating the first physical device.

15. ~~The non-transitory computer storage medium of Claim 13, wherein the causing the execution of the first one or more instruction sets for operating the physical device includes inserting, via a hardwired connection, the first one or more instruction sets for operating the physical device into at least one portion of the one or more processor circuits or into at least one portion of another one or more processor circuits.~~ The non-transitory machine readable medium of Claim 13, wherein the portions of the

new one or more digital pictures include portions of the new one or more digital pictures that depict recognized objects and the portions of the first one or more digital pictures include portions of the first one or more digital pictures that depict recognized objects.

16. ~~The non-transitory computer storage medium of Claim 13, wherein the first digital picture includes a still digital picture or a motion digital picture.~~ The non-transitory machine readable medium of Claim 13, wherein the memory further stores at least a knowledge structure that includes: a first knowledge cell including the first one or more digital pictures correlated with the first one or more instruction sets for operating the first physical device, and a second knowledge cell including a second one or more digital pictures correlated with a second one or more instruction sets for operating the first physical device, and wherein the first knowledge cell is connected with the second knowledge cell by a directed connection that indicates temporal order of the first knowledge cell and the second knowledge cell.

17. A method comprising:

(a) accessing a memory ~~unit~~ that stores at least a first one or more digital pictures correlated with a first one or more instructions sets for operating a first physical device, wherein the first physical device includes an actuator for moving at least a portion of the first physical device, and wherein at least a portion of the first one or more digital pictures or at least a portion of the first one or more instruction sets for operating the first physical device are learned in a learning process that includes operating the

first physical device at least partially by a user, the accessing of (a) performed by one or more processor circuits;

(b) receiving a new one or more digital pictures from an optical camera, the receiving of (b) performed by the one or more processor circuits;

(c) anticipating the first one or more instruction sets for operating the first physical device based on at least partial match between the new one or more digital pictures and the first one or more digital pictures, wherein the anticipating of (c) includes at least one of: determining that a number of at least partially matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold number, or determining that a percentage of at least partially matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold percentage, the anticipating of (c) performed by the one or more processor circuits;

(d) executing the first one or more instruction sets for operating the first physical device, the executing of (d) performed by the one or more processor circuits or by another one or more processor circuits in response to the anticipating of (c); and

(e) autonomously performing, by the first physical device or by a second physical device, one or more operations defined by the first one or more instructions ets for operating the first physical device.

18. ~~The method of Claim 17, wherein the executing of (d) includes inserting the first one or more instruction sets for operating the physical device into an application for operating the physical device using at least an instrumentation of the application for~~

~~operating the physical device, wherein the application for operating the physical device runs on the one or more processor circuits or on another one or more processor circuits.~~ The method of Claim 17, wherein the autonomously performing of (e) is performed by the second physical device, and wherein the first one or more instruction sets for operating the first physical device are modified so that the second physical device can autonomously perform one or more operations defined by the first one or more instruction sets for operating the first physical device.

19. ~~The method of Claim 17, wherein the executing of (d) includes inserting, via a hardwired connection, the first one or more instruction sets for operating the physical device into at least one portion of the one or more processor circuits or into at least one portion of another one or more processor circuits.~~ The method of Claim 17, wherein the portions of the new one or more digital pictures include portions of the new one or more digital pictures that depict recognized objects and the portions of the first one or more digital pictures include portions of the first one or more digital pictures that depict recognized objects.

20. ~~The method of Claim 17, wherein the first digital picture includes a still digital picture or a motion digital picture.~~ The method of Claim 17, wherein the memory further stores at least a knowledge structure that includes: a first knowledge cell including the first one or more digital pictures correlated with the first one or more instruction sets for operating the first physical device, and a second knowledge cell including a second one or more digital pictures correlated with a second one or more instruction sets for

operating the first physical device, and wherein the first knowledge cell is connected with the second knowledge cell by a directed connection that indicates temporal order of the first knowledge cell and the second knowledge cell.

Claim Interpretation

The claims in this application are given their broadest reasonable interpretation using the plain meaning of the claim language in light of the specification as it would be understood by one of ordinary skill in the art. The broadest reasonable interpretation of a claim element (also commonly referred to as a claim limitation) is limited by the description in the specification when 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, is invoked.

As explained in MPEP § 2181, subsection I, claim limitations that meet the following three-prong test will be interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph:

(A) the claim limitation uses the term "means" or "step" or a term used as a substitute for "means" that is a generic placeholder (also called a nonce term or a non-structural term having no specific structural meaning) for performing the claimed function;

(B) the term "means" or "step" or the generic placeholder is modified by functional language, typically, but not always linked by the transition word "for" (e.g., "means for") or another linking word or phrase, such as "configured to" or "so that"; and

(C) the term "means" or "step" or the generic placeholder is not modified by sufficient structure, material, or acts for performing the claimed function.

Use of the word "means" (or "step") in a claim with functional language creates a rebuttable presumption that the claim limitation is to be treated in accordance with 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph. The presumption that the claim limitation is interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, is rebutted when the claim limitation recites sufficient structure, material, or acts to entirely perform the recited function.

Absence of the word "means" (or "step") in a claim creates a rebuttable presumption that the claim limitation is not to be treated in accordance with 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph. The presumption that the claim limitation is not interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, is rebutted when the claim limitation recites function without reciting sufficient structure, material or acts to entirely perform the recited function.

Claim limitations in this application that use the word "means" (or "step") are being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, except as otherwise indicated in an Office action. Conversely, claim limitations in this application that do not use the word "means" (or "step") are not being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, except as otherwise indicated in an Office action.

This application includes one or more claim limitations that do not use the word "means," but are nonetheless being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, because the claim limitation(s) uses a generic placeholder

that is coupled with functional language without reciting sufficient structure to perform the recited function and the generic placeholder is not preceded by a structural modifier. Such claim limitation(s) is/are: "an artificial intelligence unit" in **claims 1-12**.

Because this/these claim limitation(s) is/are being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, it/they is/are being interpreted to cover the corresponding structure described in the specification as performing the claimed function, and equivalents thereof.

If applicant does not intend to have this/these limitation(s) interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, applicant may: (1) amend the claim limitation(s) to avoid it/them being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph (e.g., by reciting sufficient structure to perform the claimed function); or (2) present a sufficient showing that the claim limitation(s) recite(s) sufficient structure to perform the claimed function so as to avoid it/them being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph.

Allowable Subject Matter

Claims 1-20 are allowed. The following is an examiner's statement of reasons for allowance:

Regarding **claim 1**, Perez et al. (USPAPN 2011/0007079) discloses:

one or more processor circuits (see para [3], [19], and fig 2 and 6, a computer rendering an avatar and its background of an application);

a memory that stores at least a first one or more digital pictures correlated with a first one or more instructions sets for operating a first physical device, and wherein at

least a portion of the first one or more digital pictures or at least a portion of the first one or more instruction sets for operating the first physical device are learned in a learning process that includes operating the first physical device at least partially by a user (see para [41] and [131], a camera and the computer for capturing and storing a user's first gesture and their corresponding first instructions for the computer during a learning stage);

an optical camera that captures digital pictures (see para [41] and [131], the camera); and

an artificial intelligence unit that: receives a new one or more digital pictures from the optical camera; anticipates the first one or more instruction sets for operating the first physical device based on at least partial match between the new one or more digital pictures and the first one or more digital pictures; and causes the one or more processor circuits to execute the first one or more instruction sets for operating the first physical device, wherein the causes is performed in response to the anticipates of the artificial intelligence unit, and wherein the first physical device or a second physical device autonomously performs one or more operations defined by the first one or more instruction sets for operating the first physical device (see [108], [130], and [131], the camera and the computer capturing and recognizing a user's second gesture as corresponding to the first gesture based on matching gesture parameters, and implementing the first instruction on the avatar of the computer upon recognition).

However, Perez does not disclose: wherein the first physical device includes an actuator for moving at least a portion of the first physical device; and wherein the anticipates includes at least one of: determining that a number of at least partially

matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold number, or determining that a percentage of at least partially matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold percentage. Similar reasons apply to **claims 13 and 17**.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to SOO JIN PARK whose telephone number is (571)270-3569. The examiner can normally be reached on M-F 8:00 AM - 5:00 PM.

Examiner interviews are available via telephone, in-person, and video conferencing using a USPTO supplied web-based collaboration tool. To schedule an interview, applicant is encouraged to use the USPTO Automated Interview Request (AIR) at http://www.uspto.gov/interviewpractice.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, VU LE can be reached on (571)272-7332. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Soo Jin Park/
Primary Examiner, Art Unit 2668

Applicant-Initiated Interview Summary	**Application No.** 15/818,765	**Applicant(s)** Cosic, Jasmin	
	Examiner SOO JIN PARK	**Art Unit** 2668	**AIA Status** Yes

All participants (applicant, applicants representative, PTO personnel):

(1) SOO JIN PARK. (3) _____.

(2) JASMIN COSIC. (4) _____.

Date of Interview: 01 August 2018.

Type: ☑ Telephonic ☐ Video Conference
☐ Personal [copy given to: ☐ applicant ☐ applicant's representative]

Exhibit shown or demonstration conducted: ☐ Yes ☐ No.
If Yes, brief description: _____.

Issues Discussed ☐101 ☐112 ☐102 ☐103 ☑Others
(For each of the checked box(es) above, please describe below the issue and detailed description of the discussion)

Claim(s) discussed: 1.

Identification of prior art discussed: none.

Substance of Interview
(For each issue discussed, provide a detailed description and indicate if agreement was reached. Some topics may include: identification or clarification of a reference or a portion thereof, claim interpretation, proposed amendments, arguments of any applied references etc...)

The applicant pointed out that claim 1 in the examiner's amendment is inconsistent with what was agreed previously. The examiner agreed to make a correction and send a Corrected Notice of Allowability..

Applicant recordation instructions: The formal written reply to the last Office action must include the substance of the interview. (See MPEP section 713.04). If a reply to the last Office action has already been filed, applicant is given a non-extendable period of the longer of one month or thirty days from this interview date, or the mailing date of this interview summary form, whichever is later, to file a statement of the substance of the interview

Examiner recordation instructions: Examiners must summarize the substance of any interview of record. A complete and proper recordation of the substance of an interview should include the items listed in MPEP 713.04 for complete and proper recordation including the identification of the general thrust of each argument or issue discussed, a general indication of any other pertinent matters discussed regarding patentability and the general results or outcome of the interview, to include an indication as to whether or not agreement was reached on the issues raised.

☐ Attachment

/Soo Jin Park/ Primary Examiner, Art Unit 2668	

Tesla Exhibit 1002.027

Summary of Record of Interview Requirements

Manual of Patent Examining Procedure (MPEP), Section 713.04, Substance of Interview Must be Made of Record
A complete written statement as to the substance of any face-to-face, video conference, or telephone interview with regard to an application must be made of record in the application whether or not an agreement with the examiner was reached at the interview.

Title 37 Code of Federal Regulations (CFR) 1.133 Interviews
Paragraph (b)

In every instance where reconsideration is requested in view of an interview with an examiner, a complete written statement of the reasons presented at the interview as warranting favorable action must be filed by the applicant. An interview does not remove the necessity for reply to Office action as specified in §§ 1.111, 1.135. (35 U.S.C. 132)

37 CFR §1.2 Business to be transacted in writing.
All business with the Patent or Trademark Office should be transacted in writing. The personal attendance of applicants or their attorneys or agents at the Patent and Trademark Office is unnecessary. The action of the Patent and Trademark Office will be based exclusively on the written record in the Office. No attention will be paid to any alleged oral promise, stipulation, or understanding in relation to which there is disagreement or doubt.

The action of the Patent and Trademark Office cannot be based exclusively on the written record in the Office if that record is itself incomplete through the failure to record the substance of interviews.

It is the responsibility of the applicant or the attorney or agent to make the substance of an interview of record in the application file, unless the examiner indicates he or she will do so. It is the examiners responsibility to see that such a record is made and to correct material inaccuracies which bear directly on the question of patentability.

Examiners must complete an Interview Summary Form for each interview held where a matter of substance has been discussed during the interview by checking the appropriate boxes and filling in the blanks. Discussions regarding only procedural matters, directed solely to restriction requirements for which interview recordation is otherwise provided for in Section 812.01 of the Manual of Patent Examining Procedure, or pointing out typographical errors or unreadable script in Office actions or the like, are excluded from the interview recordation procedures below. Where the substance of an interview is completely recorded in an Examiners Amendment, no separate Interview Summary Record is required.

The Interview Summary Form shall be given an appropriate Paper No., placed in the right hand portion of the file, and listed on the "Contents" section of the file wrapper. In a personal interview, a duplicate of the Form is given to the applicant (or attorney or agent) at the conclusion of the interview. In the case of a telephone or video-conference interview, the copy is mailed to the applicants correspondence address either with or prior to the next official communication. If additional correspondence from the examiner is not likely before an allowance or if other circumstances dictate, the Form should be mailed promptly after the interview rather than with the next official communication.

The Form provides for recordation of the following information:
- Application Number (Series Code and Serial Number)
- Name of applicant
- Name of examiner
- Date of interview
- Type of interview (telephonic, video-conference, or personal)
- Name of participant(s) (applicant, attorney or agent, examiner, other PTO personnel, etc.)
- An indication whether or not an exhibit was shown or a demonstration conducted
- An identification of the specific prior art discussed
- An indication whether an agreement was reached and if so, a description of the general nature of the agreement (may be by attachment of a copy of amendments or claims agreed as being allowable). Note: Agreement as to allowability is tentative and does not restrict further action by the examiner to the contrary.
- The signature of the examiner who conducted the interview (if Form is not an attachment to a signed Office action)

It is desirable that the examiner orally remind the applicant of his or her obligation to record the substance of the interview of each case. It should be noted, however, that the Interview Summary Form will not normally be considered a complete and proper recordation of the interview unless it includes, or is supplemented by the applicant or the examiner to include, all of the applicable items required below concerning the substance of the interview.

A complete and proper recordation of the substance of any interview should include at least the following applicable items:
1) A brief description of the nature of any exhibit shown or any demonstration conducted,-
2) an identification of the claims discussed,
3) an identification of the specific prior art discussed,
4) an identification of the principal proposed amendments of a substantive nature discussed, unless these are already described on the Interview Summary Form completed by the Examiner,
5) a brief identification of the general thrust of the principal arguments presented to the examiner,
(The identification of arguments need not be lengthy or elaborate. A verbatim or highly detailed description of the arguments is not required. The identification of the arguments is sufficient if the general nature or thrust of the principal arguments made to the examiner can be understood in the context of the application file. Of course, the applicant may desire to emphasize and fully describe those arguments which he or she feels were or might be persuasive to the examiner.)
6) a general indication of any other pertinent matters discussed, and
7) if appropriate, the general results or outcome of the interview unless already described in the Interview Summary Form completed by the examiner.

Examiners are expected to carefully review the applicants record of the substance of an interview. If the record is not complete and accurate, the examiner will give the applicant an extendable one month time period to correct the record.

Examiner to Check for Accuracy

If the claims are allowable for other reasons of record, the examiner should send a letter setting forth the examiners version of the statement attributed to him or her. If the record is complete and accurate, the examiner should place the indication, Interview Record OK on the paper recording the substance of the interview along with the date and the examiners initials.

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

NOTICE OF ALLOWANCE AND FEE(S) DUE

116094 7590 07/06/2018

Jasmin Cosic
108 Woodbury Street
Pawtucket, RI 02861

EXAMINER
PARK, SOO JIN

ART UNIT	PAPER NUMBER
2668	

DATE MAILED: 07/06/2018

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/818,765	11/21/2017	Jasmin Cosic		6002

TITLE OF INVENTION: DEVICES, SYSTEMS, AND METHODS FOR USE IN AUTOMATION

APPLN. TYPE	ENTITY STATUS	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	SMALL	$500	$0	$0	$500	10/09/2018

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the ENTITY STATUS shown above. If the ENTITY STATUS is shown as SMALL or MICRO, verify whether entitlement to that entity status still applies.

If the ENTITY STATUS is the same as shown above, pay the TOTAL FEE(S) DUE shown above.

If the ENTITY STATUS is changed from that shown above, on PART B - FEE(S) TRANSMITTAL, complete section number 5 titled "Change in Entity Status (from status indicated above)".

For purposes of this notice, small entity fees are 1/2 the amount of undiscounted fees, and micro entity fees are 1/2 the amount of small entity fees.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Maintenance fees are due in utility patents issuing on applications filed on or after Dec. 12, 1980. It is patentee's responsibility to ensure timely payment of maintenance fees when due. More information is available at www.uspto.gov/PatentMaintenanceFees.

PTOL-85 (Rev. 02/11)

Tesla Exhibit 1002.029

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail **Mail Stop ISSUE FEE**
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450
or Fax (571)-273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)

116094 7590 07/06/2018

Jasmin Cosic
108 Woodbury Street
Pawtucket, RI 02861

Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

Certificate of Mailing or Transmission

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

(Depositor's name)
(Signature)
(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/818,765	11/21/2017	Jasmin Cosic		6002

TITLE OF INVENTION: DEVICES, SYSTEMS, AND METHODS FOR USE IN AUTOMATION

APPLN. TYPE	ENTITY STATUS	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	SMALL	$500	$0	$0	$500	10/09/2018

EXAMINER	ART UNIT	CLASS-SUBCLASS
PARK, SOO JIN	2668	382-218000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

❑ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

❑ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. **Use of a Customer Number is required.**

2. For printing on the patent front page, list

(1) The names of up to 3 registered patent attorneys or agents OR, alternatively,

(2) The name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 _____

2 _____

3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent) : ❑ Individual ❑ Corporation or other private group entity ❑ Government

4a. The following fee(s) are submitted:

❑ Issue Fee

❑ Publication Fee (No small entity discount permitted)

❑ Advance Order - # of Copies _____

4b. Payment of Fee(s): **(Please first reapply any previously paid issue fee shown above)**

❑ A check is enclosed.

❑ Payment by credit card. Form PTO-2038 is attached.

❑ The director is hereby authorized to charge the required fee(s), any deficiency, or credits any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).

5. **Change in Entity Status** (from status indicated above)

❑ Applicant certifying micro entity status. See 37 CFR 1.29

NOTE: Absent a valid certification of Micro Entity Status (see forms PTO/SB/15A and 15B), issue fee payment in the micro entity amount will not be accepted at the risk of application abandonment.

❑ Applicant asserting small entity status. See 37 CFR 1.27

NOTE: If the application was previously under micro entity status, checking this box will be taken to be a notification of loss of entitlement to micro entity status.

❑ Applicant changing to regular undiscounted fee status.

NOTE: Checking this box will be taken to be a notification of loss of entitlement to small or micro entity status, as applicable.

NOTE: This form must be signed in accordance with 37 CFR 1.31 and 1.33. See 37 CFR 1.4 for signature requirements and certifications.

Authorized Signature _____ Date _____

Typed or printed name _____ Registration No. _____

PTOL-85 Part B (10-13) Approved for use through 10/31/2013. OMB 0651-0033 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Tesla Exhibit 1002.030

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/818,765	11/21/2017	Jasmin Cosic		6002

116094 7590 07/06/2018

Jasmin Cosic
108 Woodbury Street
Pawtucket, RI 02861

EXAMINER	
PARK, SOO JIN	

ART UNIT	PAPER NUMBER
2668	

DATE MAILED: 07/06/2018

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)
(Applications filed on or after May 29, 2000)

The Office has discontinued providing a Patent Term Adjustment (PTA) calculation with the Notice of Allowance.

Section 1(h)(2) of the AIA Technical Corrections Act amended 35 U.S.C. 154(b)(3)(B)(i) to eliminate the requirement that the Office provide a patent term adjustment determination with the notice of allowance. See Revisions to Patent Term Adjustment, 78 Fed. Reg. 19416, 19417 (Apr. 1, 2013). Therefore, the Office is no longer providing an initial patent term adjustment determination with the notice of allowance. The Office will continue to provide a patent term adjustment determination with the Issue Notification Letter that is mailed to applicant approximately three weeks prior to the issue date of the patent, and will include the patent term adjustment on the patent. Any request for reconsideration of the patent term adjustment determination (or reinstatement of patent term adjustment) should follow the process outlined in 37 CFR 1.705.

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

PTOL-85 (Rev. 02/11)

Tesla Exhibit 1002.031

OMB Clearance and PRA Burden Statement for PTOL-85 Part B

The Paperwork Reduction Act (PRA) of 1995 requires Federal agencies to obtain Office of Management and Budget approval before requesting most types of information from the public. When OMB approves an agency request to collect information from the public, OMB (i) provides a valid OMB Control Number and expiration date for the agency to display on the instrument that will be used to collect the information and (ii) requires the agency to inform the public about the OMB Control Number's legal significance in accordance with 5 CFR 1320.5(b).

The information collected by PTOL-85 Part B is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450. Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether disclosure of these records is required by the Freedom of Information Act.
2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.
4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspection or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

Notice of Allowability	Application No. 15/818,765	Applicant(s) COSIC, JASMIN	
	Examiner SOO JIN PARK	Art Unit 2668	AIA (First Inventor to File) Status Yes

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to *11 March 2018*.
   ☐ A declaration(s)/affidavit(s) under **37 CFR 1.130(b)** was/were filed on_____.
2. ☐ An election was made by the applicant in response to a restriction requirement set forth during the interview on _____; the restriction requirement and election have been incorporated into this action.
3. ☒ The allowed claim(s) is/are *1-20*. As a result of the allowed claim(s), you may be eligible to benefit from the **Patent Prosecution Highway** program at a participating intellectual property office for the corresponding application. For more information, please see http://www.uspto.gov/patents/init_events/pph/index.jsp or send an inquiry to PPHfeedback@uspto.gov.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

   Certified copies:

   a) ☐ All b) ☐ Some *c) ☐ None of the:
   1. ☐ Certified copies of the priority documents have been received.
   2. ☐ Certified copies of the priority documents have been received in Application No. _____ .
   3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

   * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
   ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

   Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☒ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date _____
3. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
4. ☒ Interview Summary (PTO-413), Paper No./Mail Date *06/18/2018* .
5. ☒ Examiner's Amendment/Comment
6. ☒ Examiner's Statement of Reasons for Allowance
7. ☐ Other _____.

Tesla Exhibit 1002.033

The present application, filed on or after March 16, 2013, is being examined under the first inventor to file provisions of the AIA.

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in an interview with Jasmin Cosic on 06/18/2018.

Claims 1-20 are amended as follows:

1. A system for autonomous device operating, the system comprising:

one or more processor circuits;

a memory ~~unit~~ that stores at least a first one or more digital pictures correlated with a first one or more instructions sets for operating a first physical device, wherein the first physical device includes an actuator for moving at least a portion of the first physical device, and wherein at least a portion of the first one or more digital pictures or at least a portion of the first one or more instruction sets for operating the first physical device are learned in a learning process that includes operating the first physical device at least partially by a user;

an optical camera that captures digital pictures; and

an artificial intelligence unit that:

receives a new one or more digital pictures from the optical camera;

anticipates the first one or more instruction sets for operating the first physical device based on at least partial match between the new one or more digital pictures and the first one or more digital pictures, wherein the anticipates includes at least one of: determining that a number of at least partially matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold number, or determining that a percentage of at least partially matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold percentage; and

causes the one or more processor circuits to execute the first one or more instruction sets for operating the first physical device, wherein the causes is performed in response to the anticipates of the artificial intelligence unit, and wherein the first physical device or a second physical device autonomously performs one or more operations defined by the first one or more instruction sets for operating the first physical device.

2. The system of Claim 1, wherein the first one or more instruction sets for operating the first physical device are obtained from an application for operating the first physical device using at least a tracing of the application for operating the first physical

device, wherein the application for operating the first physical device runs on the one or more processor circuits or on another one or more processor circuits.

3. ~~The system of Claim 1, wherein the first digital picture is not used for identifying a user's behavior.~~ The system of Claim 1, wherein the first one or more instruction sets for operating the first physical device are obtained, via a hardwired connection, from at least one portion of the one or more processor circuits or from at least one portion of another one or more processor circuits, and wherein the one or more processor circuits or the another one or more processor circuits are used at least for operating the first physical device.

4. The system of Claim 1, wherein the causes the one or more processor circuits to execute the first one or more instruction sets for operating the first physical device includes: ~~inserting~~

instrumenting an application for operating the first physical device with the first one or more instruction sets for operating the first physical device, or

instrumenting an application for operating the second physical device with the first one or more instruction sets for operating the first physical device ~~into an application for operating the physical device using at least an instrumentation of the application for operating the physical device~~, and wherein the application for operating the first physical device or the application for operating the second physical device runs on the one or more processor circuits.

5. ~~The system of Claim 1, wherein the physical device does not include an avatar of an application program, and wherein the optical camera does not include a renderer configured to render streams of digital pictures of a surrounding of the avatar of the application program.~~ The system of Claim 1, wherein the causes the one or more processor circuits to execute the first one or more instruction sets for operating the first physical device includes inserting, via a hardwired connection, the first one or more instruction sets for operating the first physical device into at least one portion of the one or more processor circuits.

6. ~~The system of Claim 1, wherein the memory unit further stores at least a second digital picture correlated with a second one or more instruction sets for operating the physical device.~~ The system of Claim 1, wherein the second physical device autonomously performs one or more operations defined by the first one or more instruction sets for operating the first physical device, and wherein the first one or more instruction sets for operating the first physical device are modified so that the second physical device can autonomously perform one or more operations defined by the first one or more instruction sets for operating the first physical device.

7. ~~The system of Claim 1, wherein the first digital picture is a comparative digital picture whose at least one portion is compared with at least one portion of one or more digital pictures subsequent to the first digital picture.~~ The system of Claim 1, wherein the

portions of the new one or more digital pictures include portions of the new one or more digital pictures that depict recognized objects and the portions of the first one or more digital pictures include portions of the first one or more digital pictures that depict recognized objects.

8. ~~The system of Claim 1, wherein the new digital picture is an anticipatory digital picture whose at least one portion is compared with at least one portion of one or more digital pictures prior to the new digital picture.~~ The system of Claim 1, wherein the first physical device includes a robot, a vehicle, an appliance, an electronic device, or a mechanical machine and the second physical device includes a robot, a vehicle, an appliance, an electronic device, or a mechanical machine, and wherein the one or more processor circuits are included in at least one of: the first physical device, the second physical device, or a third device.

9. ~~The system of Claim 1, wherein the first digital picture correlated with the first one or more instruction sets for operating the physical device are learned at least by determining that the first one or more instruction sets for operating the physical device temporally correspond to the first digital picture.~~ The system of Claim 1, wherein the memory further stores at least a second one or more digital pictures correlated with a second one or more instruction sets for operating the second physical device, and wherein at least a portion of the second one or more digital pictures or at least a portion of the second one or more instruction sets for operating the second physical device are

learned in a second learning process that includes operating the second physical device at least partially by a second user.

10. The system of Claim 1, wherein the first one or more digital pictures includes ~~a~~ one or more still digital pictures or [[a]] one or more motion digital pictures and the new one or more digital pictures include one or more still digital pictures or one or more motion digital pictures.

11. The system of Claim 1, wherein the artificial intelligence unit includes at least one of: a hardware element that is included in the one or more processor circuits, a hardware element that is included in ~~another~~ a second one or more processor circuits, an application operating on the one or more processor circuits, an application operating on ~~another~~ a third one or more processor circuits, or an element coupled to the one or more processor circuits, and wherein the one or more processor circuits include one or more microcontrollers.

12. The system of Claim 1, wherein the memory ~~unit~~ further stores at least a knowledge structure~~, and wherein the knowledge structure~~ that includes: a first knowledge cell including the first one or more digital pictures correlated with the first one or more instruction sets for operating the first physical device, and a second knowledge cell including a second one or more digital pictures correlated with a second one or more instruction sets for operating the first physical device, and wherein the first

knowledge cell is connected with the second knowledge cell by a directed connection that indicates temporal order of the first knowledge cell and the second knowledge cell.

13. A non-transitory ~~computer storage~~ machine readable medium having ~~a computer program~~ stored thereon, ~~the computer program including~~ instructions that when executed by one or more processor circuits cause the one or more processor circuits to perform operations comprising:

accessing a memory ~~unit~~ that stores at least a first one or more digital pictures correlated with a first one or more instructions sets for operating a first physical device, wherein the first physical device includes an actuator for moving at least a portion of the first physical device, and wherein at least a portion of the first one or more digital pictures or at least a portion of the first one or more instruction sets for operating the first physical device are learned in a learning process that includes operating the first physical device at least partially by a user;

receiving a new one or more digital pictures from an optical camera;

anticipating the first one or more instruction sets for operating the first physical device based on at least partial match between the new one or more digital pictures and the first one or more digital pictures, wherein the anticipating includes at least one of: determining that a number of at least partially matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold number, or determining that a percentage of at least partially matching portions of the

new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold percentage; and

causing the one or more processor circuits or another one or more processor circuits ~~an execution of~~ to execute the first one or more instruction sets for operating the first physical device, the causing performed in response to the anticipating, wherein the first physical device or a second physical device autonomously performs one or more operations defined by the first one or more instruction sets for operating the first physical device.

14. ~~The non-transitory computer storage medium of Claim 13, wherein the causing the execution of the first one or more instruction sets for operating the physical device includes inserting the first one or more instruction sets for operating the physical device into an application for operating the physical device using at least an instrumentation of the application for operating the physical device, wherein the application for operating the physical device runs on the one or more processor circuits or on another one or more processor circuits.~~ The non-transitory machine readable medium of Claim 13, wherein the second physical device autonomously performs one or more operations defined by the first one or more instruction sets for operating the first physical device, and wherein the first one or more instruction sets for operating the first physical device are modified so that the second physical device can autonomously perform one or more operations defined by the first one or more instruction sets for operating the first physical device.

15. ~~The non-transitory computer storage medium of Claim 13, wherein the causing the execution of the first one or more instruction sets for operating the physical device includes inserting, via a hardwired connection, the first one or more instruction sets for operating the physical device into at least one portion of the one or more processor circuits or into at least one portion of another one or more processor circuits.~~ The non-transitory machine readable medium of Claim 13, wherein the portions of the new one or more digital pictures include portions of the new one or more digital pictures that depict recognized objects and the portions of the first one or more digital pictures include portions of the first one or more digital pictures that depict recognized objects.

16. ~~The non-transitory computer storage medium of Claim 13, wherein the first digital picture includes a still digital picture or a motion digital picture.~~ The non-transitory machine readable medium of Claim 13, wherein the memory further stores at least a knowledge structure that includes: a first knowledge cell including the first one or more digital pictures correlated with the first one or more instruction sets for operating the first physical device, and a second knowledge cell including a second one or more digital pictures correlated with a second one or more instruction sets for operating the first physical device, and wherein the first knowledge cell is connected with the second knowledge cell by a directed connection that indicates temporal order of the first knowledge cell and the second knowledge cell.

17. A method comprising:

(a) accessing a memory ~~unit~~ that stores at least a first one or more digital pictures correlated with a first one or more instructions sets for operating a first physical device, wherein the first physical device includes an actuator for moving at least a portion of the first physical device, and wherein at least a portion of the first one or more digital pictures or at least a portion of the first one or more instruction sets for operating the first physical device are learned in a learning process that includes operating the first physical device at least partially by a user, the accessing of (a) performed by one or more processor circuits;

(b) receiving a new one or more digital pictures from an optical camera, the receiving of (b) performed by the one or more processor circuits;

(c) anticipating the first one or more instruction sets for operating the first physical device based on at least partial match between the new one or more digital pictures and the first one or more digital pictures, wherein the anticipating of (c) includes at least one of: determining that a number of at least partially matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold number, or determining that a percentage of at least partially matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold percentage, the anticipating of (c) performed by the one or more processor circuits;

(d) executing the first one or more instruction sets for operating the first physical device, the executing of (d) performed by the one or more processor circuits or by another one or more processor circuits in response to the anticipating of (c); and

(e) autonomously performing, by the first physical device or by a second physical device, one or more operations defined by the first one or more instructions ets for operating the first physical device.

18. ~~The method of Claim 17, wherein the executing of (d) includes inserting the first one or more instruction sets for operating the physical device into an application for operating the physical device using at least an instrumentation of the application for operating the physical device, wherein the application for operating the physical device runs on the one or more processor circuits or on another one or more processor circuits.~~ The method of Claim 17, wherein the autonomously performing of (e) is performed by the second physical device, and wherein the first one or more instruction sets for operating the first physical device are modified so that the second physical device can autonomously perform one or more operations defined by the first one or more instruction sets for operating the first physical device.

19. ~~The method of Claim 17, wherein the executing of (d) includes inserting, via a hardwired connection, the first one or more instruction sets for operating the physical device into at least one portion of the one or more processor circuits or into at least one portion of another one or more processor circuits.~~ The method of Claim 17, wherein the

portions of the new one or more digital pictures include portions of the new one or more digital pictures that depict recognized objects and the portions of the first one or more digital pictures include portions of the first one or more digital pictures that depict recognized objects.

20. ~~The method of Claim 17, wherein the first digital picture includes a still digital picture or a motion digital picture.~~ The method of Claim 17, wherein the memory further stores at least a knowledge structure that includes: a first knowledge cell including the first one or more digital pictures correlated with the first one or more instruction sets for operating the first physical device, and a second knowledge cell including a second one or more digital pictures correlated with a second one or more instruction sets for operating the first physical device, and wherein the first knowledge cell is connected with the second knowledge cell by a directed connection that indicates temporal order of the first knowledge cell and the second knowledge cell.

Claim Interpretation

The claims in this application are given their broadest reasonable interpretation using the plain meaning of the claim language in light of the specification as it would be understood by one of ordinary skill in the art. The broadest reasonable interpretation of a claim element (also commonly referred to as a claim limitation) is limited by the description in the specification when 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, is invoked.

As explained in MPEP § 2181, subsection I, claim limitations that meet the following three-prong test will be interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph:

(A) the claim limitation uses the term “means” or “step” or a term used as a substitute for “means” that is a generic placeholder (also called a nonce term or a non-structural term having no specific structural meaning) for performing the claimed function;

(B) the term “means” or “step” or the generic placeholder is modified by functional language, typically, but not always linked by the transition word “for” (e.g., “means for”) or another linking word or phrase, such as “configured to” or “so that”; and

(C) the term “means” or “step” or the generic placeholder is not modified by sufficient structure, material, or acts for performing the claimed function.

Use of the word “means” (or “step”) in a claim with functional language creates a rebuttable presumption that the claim limitation is to be treated in accordance with 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph. The presumption that the claim limitation is interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, is rebutted when the claim limitation recites sufficient structure, material, or acts to entirely perform the recited function.

Absence of the word “means” (or “step”) in a claim creates a rebuttable presumption that the claim limitation is not to be treated in accordance with 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph. The presumption that the claim

limitation is not interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, is rebutted when the claim limitation recites function without reciting sufficient structure, material or acts to entirely perform the recited function.

Claim limitations in this application that use the word “means” (or “step”) are being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, except as otherwise indicated in an Office action. Conversely, claim limitations in this application that do not use the word “means” (or “step”) are not being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, except as otherwise indicated in an Office action.

This application includes one or more claim limitations that do not use the word “means,” but are nonetheless being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, because the claim limitation(s) uses a generic placeholder that is coupled with functional language without reciting sufficient structure to perform the recited function and the generic placeholder is not preceded by a structural modifier. Such claim limitation(s) is/are: “an artificial intelligence unit” in **claims 1-12**.

Because this/these claim limitation(s) is/are being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, it/they is/are being interpreted to cover the corresponding structure described in the specification as performing the claimed function, and equivalents thereof.

If applicant does not intend to have this/these limitation(s) interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, applicant may: (1) amend the claim limitation(s) to avoid it/them being interpreted under 35 U.S.C. 112(f) or pre-AIA

35 U.S.C. 112, sixth paragraph (e.g., by reciting sufficient structure to perform the claimed function); or (2) present a sufficient showing that the claim limitation(s) recite(s) sufficient structure to perform the claimed function so as to avoid it/them being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph.

Allowable Subject Matter

Claims 1-20 are allowed. The following is an examiner's statement of reasons for allowance:

Regarding **claim 1**, Perez et al. (USPAPN 2011/0007079) discloses:

one or more processor circuits (see para [3], [19], and fig 2 and 6, a computer rendering an avatar and its background of an application);

a memory that stores at least a first one or more digital pictures correlated with a first one or more instructions sets for operating a first physical device, and wherein at least a portion of the first one or more digital pictures or at least a portion of the first one or more instruction sets for operating the first physical device are learned in a learning process that includes operating the first physical device at least partially by a user (see para [41] and [131], a camera and the computer for capturing and storing a user's first gesture and their corresponding first instructions for the computer during a learning stage);

an optical camera that captures digital pictures (see para [41] and [131], the camera); and

an artificial intelligence unit that: receives a new one or more digital pictures from the optical camera; anticipates the first one or more instruction sets for operating the first physical device based on at least partial match between the new one or more digital pictures and the first one or more digital pictures; and causes the one or more processor circuits to execute the first one or more instruction sets for operating the first physical device, wherein the causes is performed in response to the anticipates of the artificial intelligence unit, and wherein the first physical device or a second physical device autonomously performs one or more operations defined by the first one or more instruction sets for operating the first physical device (see [108], [130], and [131], the camera and the computer capturing and recognizing a user's second gesture as corresponding to the first gesture based on matching gesture parameters, and implementing the first instruction on the avatar of the computer upon recognition).

However, Perez does not disclose: wherein the first physical device includes an actuator for moving at least a portion of the first physical device; and wherein the anticipates includes at least one of: determining that a number of at least partially matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold number, or determining that a percentage of at least partially matching portions of the new one or more digital pictures and portions of the first one or more digital pictures exceeds a threshold percentage. Similar reasons apply to **claims 13 and 17**.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to SOO JIN PARK whose telephone number is (571)270-3569. The examiner can normally be reached on M-F 8:00am-5:00pm ET.

Examiner interviews are available via telephone, in-person, and video conferencing using a USPTO supplied web-based collaboration tool. To schedule an interview, applicant is encouraged to use the USPTO Automated Interview Request (AIR) at http://www.uspto.gov/interviewpractice.

If attempts to reach the examiner by telephone are unsuccessful, the examiner’s supervisor, VU LE can be reached on (571)272-7332. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/SOO JIN PARK/
Primary Examiner, Art Unit 2668

Applicant-Initiated Interview Summary	Application No.	Applicant(s)	
	15/818,765	COSIC, JASMIN	
	Examiner	Art Unit	
	SOO JIN PARK	2668	

All participants (applicant, applicant's representative, PTO personnel):

(1) *SOO JIN PARK*. (3)_____.

(2) *JASMIN COSIC*. (4)_____.

Date of Interview: *18 June 2018*.

Type: ☒ Telephonic ☐ Video Conference
☐ Personal [copy given to: ☐ applicant ☐ applicant's representative]

Exhibit shown or demonstration conducted: ☐ Yes ☐ No.
If Yes, brief description: _____.

Issues Discussed ☐101 ☐112 ☒102 ☐103 ☒Others
(For each of the checked box(es) above, please describe below the issue and detailed description of the discussion)

Claim(s) discussed: _____.

Identification of prior art discussed: *Perez(2011/0007079)*.

Substance of Interview
(For each issue discussed, provide a detailed description and indicate if agreement was reached. Some topics may include: identification or clarification of a reference or a portion thereof, claim interpretation, proposed amendments, arguments of any applied references etc...)

The applicant agreed to amend the claims (as shown in the Notice of Allowance) via Examiner's Amendment, to overcome the prior art rejection and the double patenting rejection.

Applicant recordation instructions: The formal written reply to the last Office action must include the substance of the interview. (See MPEP section 713.04). If a reply to the last Office action has already been filed, applicant is given a non-extendable period of the longer of one month or thirty days from this interview date, or the mailing date of this interview summary form, whichever is later, to file a statement of the substance of the interview

Examiner recordation instructions: Examiners must summarize the substance of any interview of record. A complete and proper recordation of the substance of an interview should include the items listed in MPEP 713.04 for complete and proper recordation including the identification of the general thrust of each argument or issue discussed, a general indication of any other pertinent matters discussed regarding patentability and the general results or outcome of the interview, to include an indication as to whether or not agreement was reached on the issues raised.

☐ Attachment

/SOO JIN PARK/ Primary Examiner, Art Unit 2668	

Tesla Exhibit 1002.052

Summary of Record of Interview Requirements

Manual of Patent Examining Procedure (MPEP), Section 713.04, Substance of Interview Must be Made of Record
A complete written statement as to the substance of any face-to-face, video conference, or telephone interview with regard to an application must be made of record in the application whether or not an agreement with the examiner was reached at the interview.

Title 37 Code of Federal Regulations (CFR) § 1.133 Interviews
Paragraph (b)

In every instance where reconsideration is requested in view of an interview with an examiner, a complete written statement of the reasons presented at the interview as warranting favorable action must be filed by the applicant. An interview does not remove the necessity for reply to Office action as specified in §§ 1.111, 1.135. (35 U.S.C. 132)

37 CFR §1.2 Business to be transacted in writing.
All business with the Patent or Trademark Office should be transacted in writing. The personal attendance of applicants or their attorneys or agents at the Patent and Trademark Office is unnecessary. The action of the Patent and Trademark Office will be based exclusively on the written record in the Office. No attention will be paid to any alleged oral promise, stipulation, or understanding in relation to which there is disagreement or doubt.

The action of the Patent and Trademark Office cannot be based exclusively on the written record in the Office if that record is itself incomplete through the failure to record the substance of interviews.

It is the responsibility of the applicant or the attorney or agent to make the substance of an interview of record in the application file, unless the examiner indicates he or she will do so. It is the examiner's responsibility to see that such a record is made and to correct material inaccuracies which bear directly on the question of patentability.

Examiners must complete an Interview Summary Form for each interview held where a matter of substance has been discussed during the interview by checking the appropriate boxes and filling in the blanks. Discussions regarding only procedural matters, directed solely to restriction requirements for which interview recordation is otherwise provided for in Section 812.01 of the Manual of Patent Examining Procedure, or pointing out typographical errors or unreadable script in Office actions or the like, are excluded from the interview recordation procedures below. Where the substance of an interview is completely recorded in an Examiners Amendment, no separate Interview Summary Record is required.

The Interview Summary Form shall be given an appropriate Paper No., placed in the right hand portion of the file, and listed on the "Contents" section of the file wrapper. In a personal interview, a duplicate of the Form is given to the applicant (or attorney or agent) at the conclusion of the interview. In the case of a telephone or video-conference interview, the copy is mailed to the applicant's correspondence address either with or prior to the next official communication. If additional correspondence from the examiner is not likely before an allowance or if other circumstances dictate, the Form should be mailed promptly after the interview rather than with the next official communication.

The Form provides for recordation of the following information:
- Application Number (Series Code and Serial Number)
- Name of applicant
- Name of examiner
- Date of interview
- Type of interview (telephonic, video-conference, or personal)
- Name of participant(s) (applicant, attorney or agent, examiner, other PTO personnel, etc.)
- An indication whether or not an exhibit was shown or a demonstration conducted
- An identification of the specific prior art discussed
- An indication whether an agreement was reached and if so, a description of the general nature of the agreement (may be by attachment of a copy of amendments or claims agreed as being allowable). Note: Agreement as to allowability is tentative and does not restrict further action by the examiner to the contrary.
- The signature of the examiner who conducted the interview (if Form is not an attachment to a signed Office action)

It is desirable that the examiner orally remind the applicant of his or her obligation to record the substance of the interview of each case. It should be noted, however, that the Interview Summary Form will not normally be considered a complete and proper recordation of the interview unless it includes, or is supplemented by the applicant or the examiner to include, all of the applicable items required below concerning the substance of the interview.

A complete and proper recordation of the substance of any interview should include at least the following applicable items:
1) A brief description of the nature of any exhibit shown or any demonstration conducted,
2) an identification of the claims discussed,
3) an identification of the specific prior art discussed,
4) an identification of the principal proposed amendments of a substantive nature discussed, unless these are already described on the Interview Summary Form completed by the Examiner,
5) a brief identification of the general thrust of the principal arguments presented to the examiner,
   (The identification of arguments need not be lengthy or elaborate. A verbatim or highly detailed description of the arguments is not required. The identification of the arguments is sufficient if the general nature or thrust of the principal arguments made to the examiner can be understood in the context of the application file. Of course, the applicant may desire to emphasize and fully describe those arguments which he or she feels were or might be persuasive to the examiner.)
6) a general indication of any other pertinent matters discussed, and
7) if appropriate, the general results or outcome of the interview unless already described in the Interview Summary Form completed by the examiner.

Examiners are expected to carefully review the applicant's record of the substance of an interview. If the record is not complete and accurate, the examiner will give the applicant an extendable one month time period to correct the record.

Examiner to Check for Accuracy

If the claims are allowable for other reasons of record, the examiner should send a letter setting forth the examiner's version of the statement attributed to him or her. If the record is complete and accurate, the examiner should place the indication, "Interview Record OK" on the paper recording the substance of the interview along with the date and the examiner's initials.

Issue Classification	Application/Control No.	Applicant(s)/Patent Under Reexamination
	15818765	COSIC, JASMIN
	Examiner	**Art Unit**
	SOO PARK	2668

CPC							
Symbol						**Type**	**Version**
G06K		9	/	6215		F	2013-01-01
G06N		5	/	045		I	2013-01-01
G06K		9	/	00973		I	2013-01-01
G06K		9	/	66		I	2013-01-01
G06K		9	/	6202		I	2013-01-01
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CPC Combination Sets								
Symbol					**Type**	**Set**	**Ranking**	**Version**
			/					
			/					

NONE (Assistant Examiner)	(Date)	**Total Claims Allowed:** 20	
/SOO PARK/ Primary Examiner.Art Unit 2668 (Primary Examiner)	06/19/2018 (Date)	O.G. Print Claim(s) 1	O.G. Print Figure 38

Tesla Exhibit 1002.054

Issue Classification	Application/Control No.	Applicant(s)/Patent Under Reexamination
	15818765	COSIC, JASMIN
	Examiner	**Art Unit**
	SOO PARK	2668

US ORIGINAL CLASSIFICATION		INTERNATIONAL CLASSIFICATION	
CLASS	SUBCLASS	CLAIMED	NON-CLAIMED
		G 0 6 K 9 / 00 (2006.01.01)	

CROSS REFERENCE(S)					
CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				

NONE (Assistant Examiner)	(Date)	**Total Claims Allowed:** 20	
/SOO PARK/ Primary Examiner.Art Unit 2668 (Primary Examiner)	06/19/2018 (Date)	O.G. Print Claim(s) 1	O.G. Print Figure 38

Tesla Exhibit 1002.055

Issue Classification	Application/Control No.	Applicant(s)/Patent Under Reexamination
	15818765	COSIC, JASMIN
	Examiner	**Art Unit**
	SOO PARK	2668

☒ Claims renumbered in the same order as presented by applicant ☐ CPA ☐ T.D. ☐ R.1.47

Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original

NONE (Assistant Examiner)	(Date)	**Total Claims Allowed:** 20	
/SOO PARK/ Primary Examiner.Art Unit 2668 (Primary Examiner)	06/19/2018 (Date)	O.G. Print Claim(s) 1	O.G. Print Figure 38

Tesla Exhibit 1002.056

Doc code: IDS
Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (01-10)
Approved for use through 07/31/2012. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

U.S.PATENTS Remove

Examiner Initial*	Cite No	Patent Number	Kind Code[1]	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1					

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Examiner Initial*	Cite No	Publication Number	Kind Code[1]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	20130007532		2013-01-03	Miller et al.	
	2	20050240412		2005-10-27	Fujita	
	3	20160167226		2016-06-16	Schnittman	
	4	20090136095		2009-05-28	Marcon et al.	

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FOREIGN PATENT DOCUMENTS Remove

Examiner Initial*	Cite No	Foreign Document Number[3]	Country Code[2] i	Kind Code[4]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear	T[5]

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	1							

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NON-PATENT LITERATURE DOCUMENTS Remove

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T[5]
	1	Mohsen Hayati and Yazdan Shirvany, "Artificial Neural Network Approach for Short Term Load Forecasting for Illam Region", January 2007, 5 pages	
	2	Hasim Sak, Andrew Senior, and Francoise Beaufays, "Long Short-Term Memory Recurrent Neural Network Architectures for Large Scale Acoustic Modeling", January 2014, 5 pages	
	3	Jorg Walter and Klaus Schulten, "Implementation of self-organizing neural networks for visuo-motor control of an industrial robot", January 1993, 10 pages	
	4	Heikki Hyotyniemi and Aarno Lehtola, "A Universal Relation Database Interface for Knowledge Based Systems", April 1991, 5 pages	

If you wish to add additional non-patent literature document citation information please click the Add button Add

EXAMINER SIGNATURE

Examiner Signature	/Soo Jin Park/	Date Considered	06/19/2018

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1] See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. [2] Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). [3] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [4] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. [5] Applicant is to place a check mark here if English language translation is attached.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.058

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

OR

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Jasmin Cosic/	Date (YYYY-MM-DD)	2018-04-17
Name/Print	Jasmin Cosic	Registration Number	

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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Tesla Exhibit 1002.059

Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether the Freedom of Information Act requires disclosure of these record s.

2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.

3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.

4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).

5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.

6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).

7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.

8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspections or an issued patent.

9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

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Tesla Exhibit 1002.060

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2446674	(actuator motor) with (operat$4 control$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2018/06/19 14:51
L2	175215	g06k9/$.cpc.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2018/06/19 14:51
L3	3014	(video picture image) same 1 same (learn$4 train$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2018/06/19 14:52
L4	245	2 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2018/06/19 14:52
L5	55	("20010014781" | "20020165638" | "20040093118" | "20050177276" | "20050271279" | "20060056678" | "20060253223" | "20070070738" | "20070146371" | "20070162164" | "20070255454" | "20080243307" | "20080252248" | "20090125146" | "20090132088" | "20100222924" | "20110037839" | "20110071675" | "20110208354" | "20110211754" | "20110221974" | "20110288667" | "20120053728" | "20120070070" | "20120123590" | "20120185090" | "20120235903" | "20120290132" | "20130050410" | "20130076522" | "20130115578" | "20130151442" | "5280436" | "5495410" | "5974643" | "6816755" | "7113848" | "7139642" | "7152050" | "7308112" | "7353082" | "7383100" | "7478073" | "7672913" | "7853356" | "7953683" | "8005263" | "8195333" | "8290883" | "8401242" | "8478901" | "8639644" | "8843235" | "8965576" | "8984136").PN. OR ("9753453").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2018/06/19 15:02
L6	3	("20090268818" | "20120239174" | "20140314310").PN. OR ("9734424").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2018/06/19 15:37
L7	0	(digital picture physical device actuator instruction optical camera anticipat$4	US-PGPUB;	AND	ON	2018/06/19 15:46

Tesla Exhibit 1002.061

		threshold partially number percentage autonomous$2).clm.	USPAT; USOCR				

6/ 19/ 2018 3:47:05 PM
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Tesla Exhibit 1002.062

Search Notes	Application/Control No.	Applicant(s)/Patent Under Reexamination
	15818765	COSIC, JASMIN
	Examiner	Art Unit
	SOO PARK	2668

CPC- SEARCHED		
Symbol	Date	Examiner

CPC COMBINATION SETS - SEARCHED		
Symbol	Date	Examiner

US CLASSIFICATION SEARCHED			
Class	Subclass	Date	Examiner

* See search history printout included with this form or the SEARCH NOTES box below to determine the scope of the search.

SEARCH NOTES		
Search Notes	Date	Examiner
inventors search	2/6/2018	SJP
EAST - pose/posture/gesture, recognition, correlate/similarity/matching, threshold	2/6/2018	SJP
G06K 9/00335-00355 (narrowed search)	2/6/2018	SJP
G06K 9/all (narrowed search)	6/19/2018	SJP
EAST - actuator/motor, operate/control, video/picture/image, train/learn	6/19/2018	SJP

INTERFERENCE SEARCH			
US Class/ CPC Symbol	US Subclass / CPC Group	Date	Examiner
	see search history	6/19/2018	SJP

Tesla Exhibit 1002.063

Doc code: IDS
Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (01-10)
Approved for use through 07/31/2012. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

U.S.PATENTS Remove

Examiner Initial*	Cite No	Patent Number	Kind Code[1]	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	8996432		2015-03-31	Fu; Jicheng	
	2	6754631		2004-06-22	Din	
	3	9305216		2016-04-05	Mishra	
	4	5560011		1996-09-23	Uyama	
	5	6842877		2005-01-11	Robarts , et al.	
	6	7565340		2009-07-21	Herlocker , et al.	

If you wish to add additional U.S. Patent citation information please click the Add button. Add

U.S.PATENT APPLICATION PUBLICATIONS Remove

Examiner Initial*	Cite No	Publication Number	Kind Code[1]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear

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Tesla Exhibit 1002.064

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	1	20120290347		2012-11-15	ELAZOUNI; ASHRAF ; et al.	
	2	20150324685		2015-11-12	Bohn; Richard Esten ; et al.	
	3	20100114746		2010-05-06	Bobbitt; Russell Patrick ; et al.	
	4	20120284026		2012-11-08	Cardillo; Peter S. ; et al.	
	5	20140211988		2014-07-31	Fan; Quanfu ; et al.	
	6	20150006171		2015-01-01	Westby; Michael C. ; et al.	
	7	20150264306		2015-09-17	Marilly; Emmanuel ; et al.	
	8	20110085734		2011-04-14	Berg; Jared S. ; et al.	
	9	20150269415		2015-09-24	Gelbman; Alexander	
	10	20160274187		2016-09-22	MENON; SANKARAN M. ; et al.	
	11	20150339213		2015-11-22	LEE; Christopher Stephen ; et al.	

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	12	20150310041		2015-10-29	Kier; Scott ; et al.	
	13	20160328480		2016-11-10	Owens; Erich James ; et al.	
	14	20140075249		2014-03-13	SATO; Shuhei ; et al.	
	15	20110270794		2011-11-03	Drory; Tal ; et al.	
	16	20070050719		2007-03-01	Lui; Philip ; et al.	
	17	20070061735		2007-03-15	Hoffberg; Steven M. ; et al.	
	18	20060184410		2017-08-17	Ramamurthy; Shankar ; et al.	
	19	20060265406		2006-11-23	Chkodrov; Gueorgui B. ; et al.	
	20	20090287643		2009-11-19	Corville; Allen O. ; et al.	
	21	20110007079		2011-01-13	Perez et al.	
	22	20070050606		2007-03-01	Ferren et al.	

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.066

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	23	20090067727		2009-03-12	HIROHATA; Hitoshi	

If you wish to add additional U.S. Published Application citation information please click the Add button. Add

FOREIGN PATENT DOCUMENTS Remove

Examiner Initial*	Cite No	Foreign Document Number[3]	Country Code[2]	Kind Code[4]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear	T[5]
	1							

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NON-PATENT LITERATURE DOCUMENTS Remove

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T[5]
	1	Chen et al. "Case-Based Reasoning System and Artificial Neural Networks: A Review Neural Comput & Applic (2001) 10: pp 264-276, 13 pages	
	2	JOHN J. GREFENSTETTE, CONNIE LOGGIA RAMSEY, ALAN C. SCHULTZ, Learning Sequential Decision Rules Using Simulation Models and Competition, 1990, Navy Center for Applied Research in Artificial Intelligence, Naval Research Laboratory, Washington, DC, 27 pages	
	3	ALAN C. SCHULTZ, JOHN J. GREFENSTETTE, Using a Genetic Algorithm to Learn Behaviors for Autonomous Vehicles, 1992, Navy Center for Applied Research in Artificial Intelligence, Naval Research Laboratory, Washington, DC, 12 pages	
	4	Koppula et al., "Anticipating human activities using object affordances for reactive robotic response", IEEE TRAMI 2016, published 5 May 2015, 16 pages	
	5	Orme, "System design tips for entry level smartphones - part 3", found online at "https://community.arm.com/processors/b/blog/posts/system-design-tips-for-entry-level-smartphones---part-3", Oct 21, 2013, 12 pages	

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.067

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	6	NOWOSTAWSKI, MARIUSZ et al., "Dynamic Demes Parallel Genetic Algorithm", May 13, 1999, 6 pages	
	7	Stack Overflow, How can I quantify difference between two images?, accessed 2 August 2017 at https://stackoverflow.com/questions/189943/how-can-i-quantify-difference-between-two-images, 8 pages	
	8	SKOLICKI, ZBIGNIEW et al., "The Influence of Migration Sizes and Intervals on Island Models", June 29, 2005, 8 pages	
	9	Vahid Lari, et al., "Decentralized dynamic resource management support for massively parallel processor arrays", September 11, 2011, 8 pages	
	10	Vahid Lari, et al., "Distributed resource reservation in massively parallel processor arrays", May 16, 2011, 4 pages	

If you wish to add additional non-patent literature document citation information please click the Add button Add

EXAMINER SIGNATURE

Examiner Signature	/Soo Jin Park/	Date Considered	06/19/2018

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1] See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. [2] Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). [3] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [4] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. [5] Applicant is to place a check mark here if English language translation is attached.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.068

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

OR

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Jasmin Cosic/	Date (YYYY-MM-DD)	2018-03-12
Name/Print	Jasmin Cosic	Registration Number	

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.069

Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether the Freedom of Information Act requires disclosure of these record s.

2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.

3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.

4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).

5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.

6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).

7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.

8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspections or an issued patent.

9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.070

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/818,765	11/21/2017	Jasmin Cosic		6002

116094 7590 06/20/2018

Jasmin Cosic
108 Woodbury Street
Pawtucket, RI 02861

EXAMINER	
PARK, SOO JIN	

ART UNIT	PAPER NUMBER
2668	

MAIL DATE	DELIVERY MODE
06/20/2018	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Tesla Exhibit 1002.071

Applicant-Initiated Interview Summary	Application No.	Applicant(s)	
	15/818,765	COSIC, JASMIN	
	Examiner	Art Unit	
	SOO JIN PARK	2668	

All participants (applicant, applicant's representative, PTO personnel):

(1) *SOO JIN PARK*. (3)_____.

(2) *JASMIN COSIC*. (4)_____.

Date of Interview: *15 June 2018*.

Type: ☒ Telephonic ☐ Video Conference
☐ Personal [copy given to: ☐ applicant ☐ applicant's representative]

Exhibit shown or demonstration conducted: ☐ Yes ☐ No.
If Yes, brief description: _____.

Issues Discussed ☐101 ☐112 ☒102 ☐103 ☒Others
(For each of the checked box(es) above, please describe below the issue and detailed description of the discussion)

Claim(s) discussed: *1, 13, 17*.

Identification of prior art discussed: *Perez(2011/0007079)* .

Substance of Interview
(For each issue discussed, provide a detailed description and indicate if agreement was reached. Some topics may include: identification or clarification of a reference or a portion thereof, claim interpretation, proposed amendments, arguments of any applied references etc...)

The applicant clarified the differences between the claimed invention and the prior art recited, however, no agreement has been reached. Multiple possible claim amendments have been discussed regarding the prior art rejection and the double patenting rejection.

Applicant recordation instructions: The formal written reply to the last Office action must include the substance of the interview. (See MPEP section 713.04). If a reply to the last Office action has already been filed, applicant is given a non-extendable period of the longer of one month or thirty days from this interview date, or the mailing date of this interview summary form, whichever is later, to file a statement of the substance of the interview

Examiner recordation instructions: Examiners must summarize the substance of any interview of record. A complete and proper recordation of the substance of an interview should include the items listed in MPEP 713.04 for complete and proper recordation including the identification of the general thrust of each argument or issue discussed, a general indication of any other pertinent matters discussed regarding patentability and the general results or outcome of the interview, to include an indication as to whether or not agreement was reached on the issues raised.

☐ Attachment

/SOO JIN PARK/ Primary Examiner, Art Unit 2668	

Tesla Exhibit 1002.072

Summary of Record of Interview Requirements

Manual of Patent Examining Procedure (MPEP), Section 713.04, Substance of Interview Must be Made of Record
A complete written statement as to the substance of any face-to-face, video conference, or telephone interview with regard to an application must be made of record in the application whether or not an agreement with the examiner was reached at the interview.

Title 37 Code of Federal Regulations (CFR) § 1.133 Interviews
Paragraph (b)

In every instance where reconsideration is requested in view of an interview with an examiner, a complete written statement of the reasons presented at the interview as warranting favorable action must be filed by the applicant. An interview does not remove the necessity for reply to Office action as specified in §§ 1.111, 1.135. (35 U.S.C. 132)

37 CFR §1.2 Business to be transacted in writing.
All business with the Patent or Trademark Office should be transacted in writing. The personal attendance of applicants or their attorneys or agents at the Patent and Trademark Office is unnecessary. The action of the Patent and Trademark Office will be based exclusively on the written record in the Office. No attention will be paid to any alleged oral promise, stipulation, or understanding in relation to which there is disagreement or doubt.

The action of the Patent and Trademark Office cannot be based exclusively on the written record in the Office if that record is itself incomplete through the failure to record the substance of interviews.

It is the responsibility of the applicant or the attorney or agent to make the substance of an interview of record in the application file, unless the examiner indicates he or she will do so. It is the examiner's responsibility to see that such a record is made and to correct material inaccuracies which bear directly on the question of patentability.

Examiners must complete an Interview Summary Form for each interview held where a matter of substance has been discussed during the interview by checking the appropriate boxes and filling in the blanks. Discussions regarding only procedural matters, directed solely to restriction requirements for which interview recordation is otherwise provided for in Section 812.01 of the Manual of Patent Examining Procedure, or pointing out typographical errors or unreadable script in Office actions or the like, are excluded from the interview recordation procedures below. Where the substance of an interview is completely recorded in an Examiners Amendment, no separate Interview Summary Record is required.

The Interview Summary Form shall be given an appropriate Paper No., placed in the right hand portion of the file, and listed on the "Contents" section of the file wrapper. In a personal interview, a duplicate of the Form is given to the applicant (or attorney or agent) at the conclusion of the interview. In the case of a telephone or video-conference interview, the copy is mailed to the applicant's correspondence address either with or prior to the next official communication. If additional correspondence from the examiner is not likely before an allowance or if other circumstances dictate, the Form should be mailed promptly after the interview rather than with the next official communication.

The Form provides for recordation of the following information:
- Application Number (Series Code and Serial Number)
- Name of applicant
- Name of examiner
- Date of interview
- Type of interview (telephonic, video-conference, or personal)
- Name of participant(s) (applicant, attorney or agent, examiner, other PTO personnel, etc.)
- An indication whether or not an exhibit was shown or a demonstration conducted
- An identification of the specific prior art discussed
- An indication whether an agreement was reached and if so, a description of the general nature of the agreement (may be by attachment of a copy of amendments or claims agreed as being allowable). Note: Agreement as to allowability is tentative and does not restrict further action by the examiner to the contrary.
- The signature of the examiner who conducted the interview (if Form is not an attachment to a signed Office action)

It is desirable that the examiner orally remind the applicant of his or her obligation to record the substance of the interview of each case. It should be noted, however, that the Interview Summary Form will not normally be considered a complete and proper recordation of the interview unless it includes, or is supplemented by the applicant or the examiner to include, all of the applicable items required below concerning the substance of the interview.

A complete and proper recordation of the substance of any interview should include at least the following applicable items:
1) A brief description of the nature of any exhibit shown or any demonstration conducted,
2) an identification of the claims discussed,
3) an identification of the specific prior art discussed,
4) an identification of the principal proposed amendments of a substantive nature discussed, unless these are already described on the Interview Summary Form completed by the Examiner,
5) a brief identification of the general thrust of the principal arguments presented to the examiner,
   (The identification of arguments need not be lengthy or elaborate. A verbatim or highly detailed description of the arguments is not required. The identification of the arguments is sufficient if the general nature or thrust of the principal arguments made to the examiner can be understood in the context of the application file. Of course, the applicant may desire to emphasize and fully describe those arguments which he or she feels were or might be persuasive to the examiner.)
6) a general indication of any other pertinent matters discussed, and
7) if appropriate, the general results or outcome of the interview unless already described in the Interview Summary Form completed by the examiner.

Examiners are expected to carefully review the applicant's record of the substance of an interview. If the record is not complete and accurate, the examiner will give the applicant an extendable one month time period to correct the record.

Examiner to Check for Accuracy

If the claims are allowable for other reasons of record, the examiner should send a letter setting forth the examiner's version of the statement attributed to him or her. If the record is complete and accurate, the examiner should place the indication, "Interview Record OK" on the paper recording the substance of the interview along with the date and the examiner's initials.

Doc code: IDS
Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (01-10)
Approved for use through 07/31/2012. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

U.S.PATENTS Remove

Examiner Initial*	Cite No	Patent Number	Kind Code[1]	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1					

If you wish to add additional U.S. Patent citation information please click the Add button. Add

U.S.PATENT APPLICATION PUBLICATIONS Remove

Examiner Initial*	Cite No	Publication Number	Kind Code[1]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	20130007532		2013-01-03	Miller et al.	
	2	20050240412		2005-10-27	Fujita	
	3	20160167226		2016-06-16	Schnittman	
	4	20090136095		2009-05-28	Marcon et al.	

If you wish to add additional U.S. Published Application citation information please click the Add button. Add

FOREIGN PATENT DOCUMENTS Remove

Examiner Initial*	Cite No	Foreign Document Number[3]	Country Code[2] i	Kind Code[4]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear	T[5]

Tesla Exhibit 1002.074

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	1							

If you wish to add additional Foreign Patent Document citation information please click the Add button Add

NON-PATENT LITERATURE DOCUMENTS Remove

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T[5]
	1	Mohsen Hayati and Yazdan Shirvany, "Artificial Neural Network Approach for Short Term Load Forecasting for Illam Region", January 2007, 5 pages	
	2	Hasim Sak, Andrew Senior, and Francoise Beaufays, "Long Short-Term Memory Recurrent Neural Network Architectures for Large Scale Acoustic Modeling", January 2014, 5 pages	
	3	Jorg Walter and Klaus Schulten, "Implementation of self-organizing neural networks for visuo-motor control of an industrial robot", January 1993, 10 pages	
	4	Heikki Hyotyniemi and Aarno Lehtola, "A Universal Relation Database Interface for Knowledge Based Systems", April 1991, 5 pages	

If you wish to add additional non-patent literature document citation information please click the Add button Add

EXAMINER SIGNATURE

Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1] See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. [2] Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). [3] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [4] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. [5] Applicant is to place a check mark here if English language translation is attached.

Tesla Exhibit 1002.075

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

OR

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Jasmin Cosic/	Date (YYYY-MM-DD)	2018-04-17
Name/Print	Jasmin Cosic	Registration Number	

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

Tesla Exhibit 1002.076

Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether the Freedom of Information Act requires disclosure of these record s.

2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.

3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.

4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).

5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.

6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).

7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.

8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspections or an issued patent.

9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

Tesla Exhibit 1002.077

Electronic Acknowledgement Receipt

EFS ID:	32365121
Application Number:	15818765
International Application Number:	
Confirmation Number:	6002
Title of Invention:	DEVICES, SYSTEMS, AND METHODS FOR USE IN AUTOMATION
First Named Inventor/Applicant Name:	Jasmin Cosic
Customer Number:	116094
Filer:	Jasmin Cosic
Filer Authorized By:	
Attorney Docket Number:	
Receipt Date:	17-APR-2018
Filing Date:	21-NOV-2017
Time Stamp:	17:04:43
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Form (SB08)	IDS_Part6.pdf	612751 c27bde929e912bec2d75ef47500ea63eed062a6a	no	4

Warnings:

Information:					
2	Non Patent Literature	NPL_Part6.pdf	2933997 8456bc6c2484a87a7798f7884d1951c4d472de49	no	25
Warnings:					
Information:					
			Total Files Size (in bytes):	3546748	

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

PTO/SB/08a (01-10)
Approved for use through 07/31/2012. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

U.S.PATENTS Remove

Examiner Initial*	Cite No	Patent Number	Kind Code[1]	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	8996432		2015-03-31	Fu; Jicheng	
	2	6754631		2004-06-22	Din	
	3	9305216		2016-04-05	Mishra	
	4	5560011		1996-09-23	Uyama	
	5	6842877		2005-01-11	Robarts , et al.	
	6	7565340		2009-07-21	Herlocker , et al.	

If you wish to add additional U.S. Patent citation information please click the Add button. Add

U.S.PATENT APPLICATION PUBLICATIONS Remove

Examiner Initial*	Cite No	Publication Number	Kind Code[1]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear

Tesla Exhibit 1002.080

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	1	20120290347		2012-11-15	ELAZOUNI; ASHRAF ; et al.	
	2	20150324685		2015-11-12	Bohn; Richard Esten ; et al.	
	3	20100114746		2010-05-06	Bobbitt; Russell Patrick ; et al.	
	4	20120284026		2012-11-08	Cardillo; Peter S. ; et al.	
	5	20140211988		2014-07-31	Fan; Quanfu ; et al.	
	6	20150006171		2015-01-01	Westby; Michael C. ; et al.	
	7	20150264306		2015-09-17	Marilly; Emmanuel ; et al.	
	8	20110085734		2011-04-14	Berg; Jared S. ; et al.	
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Tesla Exhibit 1002.081

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

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Tesla Exhibit 1002.082

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	23	20090067727		2009-03-12	HIROHATA; Hitoshi	

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Examiner Initial*	Cite No	Foreign Document Number[3]	Country Code[2] i	Kind Code[4]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear	T[5]
	1							

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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T[5]
	1	Chen et al. "Case-Based Reasoning System and Artificial Neural Networks: A Review Neural Comput & Applic (2001) 10: pp 264-276, 13 pages	
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	4	Koppula et al., "Anticipating human activities using object affordances for reactive robotic response", IEEE TRAMI 2016, published 5 May 2015, 16 pages	
	5	Orme, "System design tips for entry level smartphones - part 3", found online at "https://community.arm.com/processors/b/blog/posts/system-design-tips-for-entry-level-smartphones---part-3", Oct 21, 2013, 12 pages	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	6	NOWOSTAWSKI, MARIUSZ et al., "Dynamic Demes Parallel Genetic Algorithm", May 13, 1999, 6 pages	
	7	Stack Overflow, How can I quantify difference between two images?, accessed 2 August 2017 at https://stackoverflow.com/questions/189943/how-can-i-quantify-difference-between-two-images, 8 pages	
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If you wish to add additional non-patent literature document citation information please click the Add button Add

EXAMINER SIGNATURE

Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1] See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. [2] Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). [3] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [4] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. [5] Applicant is to place a check mark here if English language translation is attached.

Tesla Exhibit 1002.084

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

OR

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Jasmin Cosic/	Date (YYYY-MM-DD)	2018-03-12
Name/Print	Jasmin Cosic	Registration Number	

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether the Freedom of Information Act requires disclosure of these record s.

2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.

3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.

4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).

5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.

6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).

7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.

8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspections or an issued patent.

9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

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Electronic Acknowledgement Receipt

EFS ID:	32031430
Application Number:	15818765
International Application Number:	
Confirmation Number:	6002
Title of Invention:	DEVICES, SYSTEMS, AND METHODS FOR USE IN AUTOMATION
First Named Inventor/Applicant Name:	Jasmin Cosic
Customer Number:	116094
Filer:	Jasmin Cosic
Filer Authorized By:	
Attorney Docket Number:	
Receipt Date:	12-MAR-2018
Filing Date:	21-NOV-2017
Time Stamp:	23:50:26
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Form (SB08)	IDS_Part5.pdf	614344 34051c79ca6a26872354ae2fd4409b7f94046bf9	no	7

Warnings:

Information:					
2	Non Patent Literature	NPL_Part5.pdf	11341185 68e45daa71c666ffb88664c86f9d22d61ea77de7	no	114
Warnings:					
Information:					
			Total Files Size (in bytes):	11955529	

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor:	Jasmin Cosic	Confirmation No.: 6002
Title:	"DEVICES, SYSTEMS, AND METHODS FOR USE IN AUTOMATION"	
Serial No.:	15/818,765	Filed: November 21, 2017
Examiner:	PARK, SOO JIN	Group Art Unit: 2668

Via EFS-Web March 11, 2018
Mail Stop Amendment
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Commissioner:

In response to non-final office action dated February 9, 2018 ("Office Action") and an interview with the Examiner on February 22, 2018, the applicant submits this amendment, and respectfully requests reconsideration of the Claims.

Amendments to the Claims begin on page 2.

Remarks begin on page 11.

Listing of Claims

1. (currently amended) A system **for autonomous device operating, the system** comprising:

one or more processor circuits;

a memory unit that stores at least ~~**a plurality of digital pictures correlated with one or more instruction sets for operating a device, wherein the plurality of digital pictures correlated with one or more instruction sets for operating the device include**~~ a first digital picture correlated with a first one or more instruction sets for operating a **physical** device;

~~**a picture capturing apparatus**~~ **an optical camera that captures digital pictures**; and

~~**a decision-making unit configured to**~~ **an artificial intelligence unit that**:

~~**access, in the memory unit, the first digital picture correlated with the first one or more instruction sets for operating the device;**~~

receive**s** a new digital picture from the ~~**picture capturing apparatus**~~ **optical camera**;

anticipate**s** the first one or more instruction sets for operating the **physical** device based on at least partial match between the new digital picture and the first digital picture**, wherein the anticipates includes at least one of:**

determining that a number of at least partially matching portions of the new digital picture and portions of the first digital picture exceeds a threshold

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number, or determining that a percentage of at least partially matching portions of the new digital picture and portions of the first digital picture exceeds a threshold percentage; and

cause**s** the one or more processor circuits to execute the first one or more instruction sets for operating the **physical** device, **wherein** the ~~**causing**~~ **causes is** performed in response to the ~~**anticipating**~~ **anticipates** of the ~~**decision-making**~~ **artificial intelligence** unit, **and** wherein the **physical** device performs one or more operations defined by the first one or more instruction sets for operating the **physical** device.

2. (currently amended) The system of Claim 1, wherein the first one or more instruction sets for operating the **physical** device are obtained from an application for operating the **physical** device using at least a tracing of the application for operating the **physical** device, wherein the application for operating the **physical** device runs on the one or more processor circuits or on another one or more processor circuits.

3. (currently amended) ~~**The system of Claim 1, the first one or more instruction sets for operating the device are obtained, via a hardwired connection, from at least one portion of the one or more processor circuits or from at least one portion of another one or more processor circuits.**~~ **The system of Claim 1, wherein the first digital picture is not used for identifying a user's behavior.**

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4. (currently amended) The system of Claim 1, wherein the ~~causing~~ **causes** the one or more processor circuits to execute the first one or more instruction sets for operating the **physical** device includes inserting the first one or more instruction sets for operating the **physical** device into an application for operating the **physical** device using at least an instrumentation of the application for operating the **physical** device, wherein the application for operating the **physical** device runs on the one or more processor circuits.

5. (currently amended) ~~The system of Claim 1, wherein the causing the one or more processor circuits to execute the first one or more instruction sets for operating the device includes inserting, via a hardwired connection, the first one or more instruction sets for operating the device into at least one portion of the one or more processor circuits.~~ **The system of Claim 1, wherein the physical device does not include an avatar of an application program, and wherein the optical camera does not include a renderer configured to render streams of digital pictures of a surrounding of the avatar of the application program.**

6. (currently amended) ~~The system of Claim 1, wherein the first digital picture correlated with the first one or more instruction sets for operating the device includes a unit of knowledge of how the device operated in a visual surrounding.~~ **The system of Claim 1, wherein the memory unit**

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further stores at least a second digital picture correlated with a second one or more instruction sets for operating the physical device.

7. (currently amended) ~~**The system of Claim 1, wherein the anticipating the first one or more instruction sets for operating the device based on at least partial match between the new digital picture and the first digital picture includes determining at least partial match between at least a threshold number or a threshold percentage of portions of the new digital picture and portions of the first digital picture.**~~ **The system of Claim 1, wherein the first digital picture is a comparative digital picture whose at least one portion is compared with at least one portion of one or more digital pictures subsequent to the first digital picture.**

8. (currently amended) ~~**The system of Claim 1, wherein the decision-making unit includes an interface for attaching to at least one of: the device, the one or more processor circuits, another one or more processor circuits, or an application for operating the device, wherein the application for operating the device runs on the one or more processor circuits or on another one or more processor circuits.**~~ **The system of Claim 1, wherein the new digital picture is an anticipatory digital picture whose at least one portion is compared with at least one portion of one or more digital pictures prior to the new digital picture.**

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9. (currently amended) ~~**The system of Claim 1, wherein the processor circuit includes a microcontroller.**~~ **The system of Claim 1, wherein the first digital picture correlated with the first one or more instruction sets for operating the physical device are learned at least by determining that the first one or more instruction sets for operating the physical device temporally correspond to the first digital picture.**

10. (original) The system of Claim 1, wherein the first digital picture includes a still digital picture or a motion digital picture.

11. (currently amended) The system of Claim 1, wherein the ~~**decision-making**~~ **artificial intelligence** unit includes at least one of: a hardware element that is included in the one or more processor circuits, a hardware element that is included in another one or more processor circuits, an application operating on the one or more processor circuits, an application operating on another one or more processor circuits, or an element coupled to the one or more processor circuits.

12. (currently amended) ~~**The system of Claim 1, wherein at least one of: the one or more processor circuits, the memory unit, the picture capturing apparatus, or the decision-making unit are included in the device.**~~ **The system of Claim 1, wherein the memory unit further stores at least a knowledge structure, and wherein the knowledge structure includes: a first**

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knowledge cell including the first digital picture correlated with the first one or more instruction sets for operating the physical device, and a second knowledge cell including a second digital picture correlated with a second one or more instruction sets for operating the physical device, and wherein the first knowledge cell is connected with the second knowledge cell by a connection.

13. (currently amended) A non-transitory computer storage medium having a computer program stored thereon, the **computer** program including instructions that when executed by one or more processor circuits cause the one or more processor circuits to perform operations comprising:

accessing a memory unit that stores at least ~~a plurality of digital pictures correlated with one or more instruction sets for operating a device, wherein the plurality of digital pictures correlated with one or more instruction sets for operating the device include~~ a first digital picture correlated with a first one or more instruction sets for operating ~~the~~ **a physical** device;

receiving a new digital picture from ~~a picture capturing apparatus~~ **an optical camera**;

anticipating the first one or more instruction sets for operating the **physical** device based on at least partial match between the new digital picture and the first digital picture**, wherein the anticipating includes at least one of: determining that a number of at least partially matching portions of the new**

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digital picture and portions of the first digital picture exceeds a threshold number, or determining that a percentage of at least partially matching portions of the new digital picture and portions of the first digital picture exceeds a threshold percentage; and

causing an execution of the first one or more instruction sets for operating the **physical** device, the causing performed in response to the anticipating, wherein the **physical** device performs one or more operations defined by the first one or more instruction sets for operating the **physical** device.

14. (currently amended) The non-transitory computer storage medium of Claim 13, wherein the causing the execution of the first one or more instruction sets for operating the **physical** device includes inserting the first one or more instruction sets for operating the **physical** device into an application for operating the **physical** device using at least an instrumentation of the application for operating the **physical** device, wherein the application for operating the **physical** device runs on the one or more processor circuits or on another one or more processor circuits.

15. (currently amended) The non-transitory computer storage medium of Claim 13, wherein **the causing** the execution of the first one or more instruction sets for operating the **physical** device includes inserting, via a hardwired connection, the first one or more instruction sets for operating the **physical** device into at least

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one portion of the one or more processor circuits or into at least one portion of another one or more processor circuits.

16. (original) The non-transitory computer storage medium of Claim 13, wherein the first digital picture includes a still digital picture or a motion digital picture.

17. (currently amended) A method comprising:

(a) accessing a memory unit that stores at least ~~a plurality of digital pictures correlated with one or more instruction sets for operating a device, wherein the plurality of digital pictures correlated with one or more instruction sets for operating the device include~~ a first digital picture correlated with a first one or more instruction sets for operating ~~the~~ **a physical** device, the accessing of (a) performed by one or more processor circuits;

(b) receiving a new digital picture from ~~a picture capturing apparatus~~ **an optical camera**, the receiving of (b) performed by the one or more processor circuits;

(c) anticipating the first one or more instruction sets for operating the **physical** device based on at least partial match between the new digital picture and the first digital picture**, wherein the anticipating of (c) includes at least one of: determining that a number of at least partially matching portions of the new digital picture and portions of the first digital picture exceeds a threshold number, or determining that a percentage of at least partially matching portions of the new digital picture and portions of the first digital**

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picture exceeds a threshold percentage, the anticipating of (c) performed by the one or more processor circuits;

(d) executing the first one or more instruction sets for operating the **physical** device, the executing of (d) performed in response to the anticipating of (c); and

(e) performing, by the **physical** device, one or more operations defined by the first one or more instruction sets for operating the **physical** device.

18. (currently amended) The method of Claim 17, wherein the executing of (d) includes inserting the first one or more instruction sets for operating the **physical** device into an application for operating the **physical** device using at least an instrumentation of the application for operating the **physical** device, wherein the application for operating the **physical** device runs on the one or more processor circuits or on another one or more processor circuits.

19. (currently amended) The method of Claim 17, wherein the executing of (d) includes inserting, via a hardwired connection, the first one or more instruction sets for operating the **physical** device into at least one portion of the one or more processor circuits or into at least one portion of another one or more processor circuits.

20. (original) The method of Claim 17, wherein the first digital picture includes a still digital picture or a motion digital picture.

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Remarks

Upon entry of this amendment, Claims 1-20 will be pending in the present application. Claims 1-9, 11-15, and 17-19 are amended. The applicant respectfully requests reconsideration and allowance of all Claims.

I. Interpretation Under 35 U.S.C. 112(f)

The Examiner has interpreted “units” in Claims 1-12 under 35 U.S.C. 112(f) to cover the corresponding structure described in the specification as performing the claimed function, and equivalents thereof. [Office Action 4:7-16].

First, "artificial intelligence unit" is specifically described in the specification to include hardware, programs, or a combination thereof. In some embodiments, “the artificial intelligence unit is part of, operating on, or coupled to the processor circuit” [Cosic 2:19-20]. The “is part of” includes a reference to a hardware element of the processor circuit. The “operating on” includes a reference to a program operating on the processor circuit. The “coupled to” includes a reference to a hardware element or program coupled to or interfaced with the processor circuit. Other embodiments of artificial intelligence unit including hardware, programs, and/or a combination thereof can be found in [Cosic 77:24-26; 2:18-39]. Additionally, “artificial intelligence unit’s” structure is specifically recited in dependent Claim 11. To Clarify, amended dependent Claim 11 recites “the artificial intelligence unit includes at least one of: a hardware element that is included in the one or more processor circuits, a hardware element that is included in another one or more processor circuits, an application operating on the one or more processor circuits, an application operating on another one or more processor circuits, or an element coupled to the one or more processor circuits”.

Second, more generally and applicable to all elements of the system in Claim 1, “In some embodiments, the disclosed artificially intelligent devices, systems, and/or methods …, or elements thereof, can be implemented entirely or

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in part in a device…" [Cosic 59:18-20] and "In further embodiments, the disclosed artificially intelligent devices, systems, and/or methods …, or elements thereof, can be implemented entirely or in part as a computer program…" [Cosic 59:32-34]. Other embodiments of the disclosed artificially intelligent devices, systems, and methods, or elements thereof, including hardware, programs, and/or a combination thereof can be found in [Cosic 59:18-60:6].

Therefore, the correct interpretation of "units" in Claims 1-12 includes hardware, programs, and/or a combination thereof.

II. 35 U.S.C. 102 and 103

Claims 1, 3-6, and 8-20 are rejected under 35 U.S.C. 102(a)(1) as allegedly being anticipated by Perez et al. (USPAPN 2011/0007079) [Office Action 5:10-11]. Claim 2 is rejected under 35 U.S.C. 103 as allegedly being unpatentable over Perez in view of Orme ("System design tips for entry level smartphones - part 3") [Office Action 9:1-2].

The Examiner did not reject dependent Claim 7 under 35 U.S.C. 102 and 103. Also, the Examiner stated in the interview on February 22, 2018 that dependent Claim 7 is allowable under 35 U.S.C. 102 and 103 if included in the independent Claim 1 (see the interview summary below). Per the Examiner's statement, the applicant has amended independent Claim 1 to include the limitations recited in Claim 7.

The applicant respectfully disagrees with the 35 U.S.C. 102 and 103 rejections and absolutely maintains that all Claims as previously presented are allowable. However, to expedite allowance of the application and to avoid additional fees, the applicant has amended independent Claim 1 to include dependent Claim 7. Accordingly, allowance of the amended independent Claim 1 is respectfully requested.

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III. Nonstatutory Double Patenting

Claims 1-20 are rejected on the ground of nonstatutory double patenting as allegedly being unpatentable over applications 15/191517, 15/227971, 15/228599, 15/239817, 15/245046, 15/829984, 15/835434, 15/340991, 15/382743, 15/663751, 15/822150, and 15/842885, as the claims of these applications, either by themselves or in view of Perez, are not patentably distinct from the claims of the current application [Office Action 10:34-11:3]. The applicant respectfully disagrees that double patenting exists.

In the interview on February 22, 2018, the Examiner withdrew the double patenting rejection for all of the above-listed applications except for applications 15/191517 and 15/245046 (see the interview summary below).

In reconsideration of the double patenting rejection, the Examiner is reminded of the MPEP rules:

A. as in 35 U.S.C. 102 and 103, the same rules for determining whether elements are the same or not the same apply in double patenting analysis.
B. as in 35 U.S.C. 103, if a combination of a claim of the Application 15/245046 and a prior art reference is used, the prior art reference must show a suggestion of how elements from the claim of the Application 15/245046 and from the prior art reference can BOTH:
    i. be combined at all, AND
    ii. produce results claimed in a claim of the Instant Application.

Perez cannot be combined with Claim 1 of the Application 15/245046 for a valid obviousness double patenting rejection.

First, the Examiner uses Claim 1 of the Application 15/245046 and Perez in her double patenting rejection, and tersely states “in view of Perez” [Office Action 10:34-11:3]. ***The applicant cannot find a suggestion of how elements***

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in Claim 1 of the Application 15/245046 can be combined with Perez at all. Also, the applicant cannot find a suggestion of how Claim 1 of the Application 15/245046 and Perez, if they were to be combined, produce the results claimed in Claim 1 of the Instant Application.

Second, the results claimed in Claim 1 of the Instant Application enable ***autonomous operating of a physical device***. To clarify, amended independent Claim 1 now recites "A system for autonomous device operating". On the other hand, it is important to understand that Perez discloses: "***Data captured with respect to a human may be analyzed and applied to a visual representation of a user such that the visual representation begins to reflect the behavioral characteristics of the user. ... Over time, the system may learn a user's tendencies and intelligently apply animations to the user's avatar such that the avatar behaves and responds in accordance with the identified behaviors of the user***" [Perez Abstract]. Also, Perez Fig. 1 below clearly shows ***an avatar being controlled by identified behaviors of a user***.

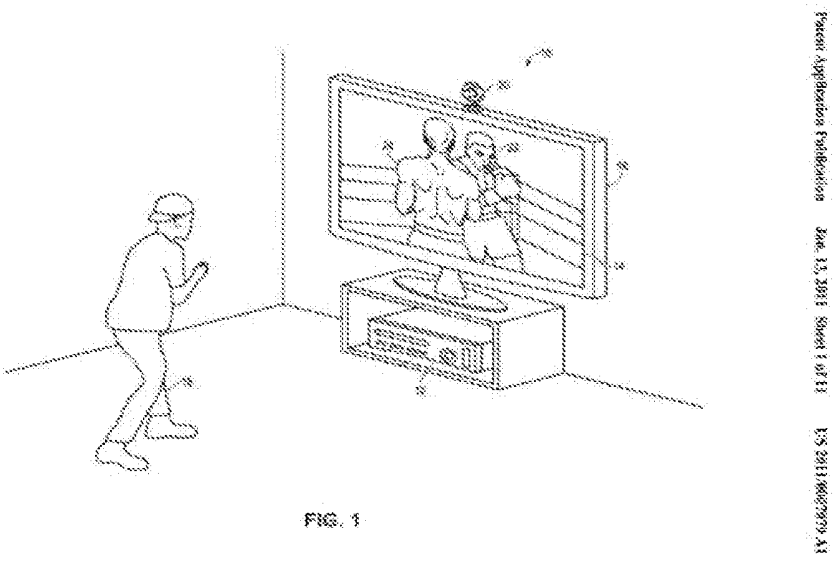


FIG. 1

Not only that a main actor (i.e. physical device) in Claim 1 of the Instant Application and a main actor (i.e. avatar) in Perez are different, but an actor that is autonomous is the opposite of an actor that is controlled by a user. Hence, the results claimed in Claim 1 of the Instant Application (i.e. autonomous

operating of a physical device) are different than the results disclosed in Perez (i.e. an avatar controlled by a user).

Third, ***the entire field of a user's control of an avatar is not relevant to the field of autonomous device operating***.

Fourth, not only that Perez discloses different results, but Perez uses different elements for achieving these different results. Claim 1 of the Instant Application recites "***a first digital picture correlated with a first one or more instruction sets for operating the physical device***". Conversely, Perez discloses "***user's behaviors correlated with contextual circumstances***" in "Correlate user's behaviors with contextual circumstances (e.g., state of the application, success/failure in a game)" [Perez Fig. 8 Step 807], "The computing environment 12 may analyze the captured data and correlate a user's behaviors with contextual circumstances, such as conditions of the user's environment, the status of the system or application, the success/result of the user in the application, the active or non-active status of the user, or any other detectable circumstance" [Perez par. 0027], and throughout Perez specification and drawings. Hence:

Claim 1 of the Instant Application		**Perez**
a first digital picture	**NOT SAME**	user's behaviors
correlated with		correlated with
a first one or more instruction sets for operating the physical device	**NOT SAME**	contextual circumstances

Clearly, the "first digital picture" recited in Claim 1 of the Instant Application is not the same as "user's behaviors" in Perez. While user's behaviors may be identified by processing digital pictures, user's behaviors are never the same as digital pictures themselves. ***Also, clearly, the "first one or more instruction sets for operating the physical device" recited in Claim 1 of the Instant Application are not the same as "contextual circumstances"***

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in Perez. In fact, nothing in "first digital picture correlated with a first one or more instruction sets for operating the physical device" recited in Claim 1 of the Instant Application is the same as in "user's behaviors correlated with contextual circumstances" in Perez. Perez is clearly motivated by user's behaviors correlated with contextual circumstances, **NOT BY** digital pictures correlated with instruction sets. Hence, not only that Perez is directed to a different field of endeavor and produces different results, but Perez achieves these different results using different elements than the elements recited in Claim 1 of the Instant Application. Although the applicant absolutely maintains that it is not necessary, the applicant has amended dependent Claim 3 to recite "wherein the first digital picture is not used for identifying a user's behavior".

Fifth, not only that Perez fails to disclose autonomous device operating, but ***Perez teaches away from autonomous device operating because the avatar in Perez is controlled by the user (i.e. "responds to user behaviors")*** as explicitly stated throughout Perez. ***Something that is autonomous is the opposite of something that is controlled by a user***. In fact, word "autonomous" does not exist in Perez at all. Hence, not only that Perez does not disclose even autonomous avatar and especially not autonomous device, but Perez teaches away from anything autonomous. ***A prior art reference that teaches away from the subject matter claimed in Claim 1 of the Instant Application cannot be combined with Claim 1 of the Application 15/245046 in an obviousness double patenting rejection.***

Therefore, the applicant has shown that Perez cannot be combined with Claim 1 of the Application 15/245046 for a valid obviousness double patenting rejection because: (i) there is no suggestion of how Claim 1 of the Application 15/245046 can be combined with Perez to produce results claimed in Claim 1 of the Instant Application, (ii) the results claimed in Claim 1 of the Instant Application (i.e. autonomous device operating) are different than the results (i.e. avatar controlled by a user) disclosed in Perez, (iii) the field of a user's control of an avatar in Perez is not relevant to the field of autonomous devices in Claim 1 of

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the Instant Application, (iv) Perez achieves the different results using different elements than the elements recited in Claim 1 of the Instant Application, and (v) Perez teaches away from autonomous device operating because the avatar in Perez is controlled by the user. Therefore, Claim 1 of the Application 15/245046 and Perez combination does not form a valid obviousness double patenting rejection.

Now that the applicant has proven that Perez cannot be combined with Claim 1 of the Application 15/245046 as discussed above, the applicant will contrast the elements of Claim 1 of the Instant Application and the elements of Claim 1 of the Application 15/245046. Although, the applicant has proven that Perez cannot be used in combination with Claim 1 of the Application 15/245046, the applicant will still refer to Perez in the following arguments for completeness of analysis.

"Device" in Claim 1 of the Instant Application is not the same as "application program" in Claim 1 of the Application 15/245046. (Does the Examiner assert that the "device" in Claim 1 of the Instant Application is the same as the "application program" in Claim 1 of the Application 15/245046?)

First, ***the "application program" in Claim 1 of the Application 15/245046 recites a "program"***. Thus, the "application program" in Claim 1 of the Application 15/245046 and the "device" in the Instant Application ***are vastly different concepts beyond comparison and clearly patentably distinct.*** The same logic applies in Perez as Perez discloses a computer game, not a device.

Second, it is important to understand that the environment within which a main actor (i.e. "device") in Claim 1 of the Instant Application inherently operates is the physical world, whereas, the environment within which a main actor (i.e. "avatar of the application program") in Claim 1 of the Application 15/245046 inherently operates is the application program. ***A device cannot even exist, let***

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alone operate, in an application program. Thus, ***Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 are clearly patentably distinct at least because of the completely different environments where main actors (i.e. "device" and "avatar") of the two Claims 1 operate.*** How can Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 be indistinct when they do not even share the same environment where main actors operate? The same logic applies in Perez as Perez discloses a computer game, which is a different environment than the physical world.

Therefore, the applicant has shown a clear patentable distinction between Claim 1of the Instant Application and Claim 1 of the Application 15/245046 even in view of Perez. Because of this high-level difference in the environment where main actors operate, the remaining elements of Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 are difficult to compare (i.e. beyond comparison), but the applicant will describe the differences to prove beyond a doubt a clear patentable distinction between the two Claims 1.

"Device" in Claim 1 of the Instant Application is not the same as "avatar of the application program" in Claim 1 of the Application 15/245046. (Does the Examiner assert that the "device" in Claim 1 of the Instant Application is the same as the "avatar of the application program" in Claim 1 of the Application 15/245046?)

First, the "device" recited in Claim 1 of the Instant Application is a physical thing, whereas, the "avatar of the application program" recited in Claim 1 of the Application 15/245046 is not a physical thing. ***A physical thing is always different from a non-physical thing and a physical thing is never indistinct from a non-physical thing.*** Although, the applicant absolutely maintains that "device" in Claim 1 of the Instant Application and "avatar of the application program" in Claim 1 of the Application 15/245046 are clearly patentably distinct, the applicant has amended Claim 1 of the Instant Application to recite ***"physical device", which is irrefutably patentably distinct from "avatar of the***

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application program" since "avatar of the application program" is not physical. Furthermore, although, the applicant absolutely maintains that it is not necessary, the applicant has amended Claim 5 of the Instant Application to recite "wherein the physical device does not include an avatar of an application program". The same logic applies in Perez as Perez discloses an avatar of a computer game, which is not a physical thing.

Second, the Examiner will find that ***all embodiments and examples in the Instant Application deal with physical devices, whereas, all embodiments and examples in the Application 15/245046 deal with objects of an application program***. Thus, the Instant Application and its Claim 1 are clearly directed to physical devices, which are irrefutably patentably distinct from the Application 15/245046 and its Claim 1 that are clearly directed to objects of application programs. The same logic applies in Perez as Perez is directed to an avatar of a computer game, not a physical device.

Therefore, the applicant has shown a clear patentable distinction between Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 even in view of Perez.

"Picture capturing apparatus" in Claim 1 of the Instant Application is not the same as "renderer" in Claim 1 of the Application 15/245046. (Does the Examiner assert that the "picture capturing apparatus" in Claim 1 of the Instant Application is the same as "renderer" in Claim 1 of the Application 15/245046?)

First, because the environments (i.e. physical world vs. application program) and actors (i.e. physical device vs. avatar of an application program) in Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 are different, the elements for picture acquisition are also different. ***"Renderer configured to render streams of digital pictures of a surrounding of the avatar of the application program"*** recited in Claim 1 of the Application 15/245046 is clearly different from the ***"picture capturing apparatus" recited in Claim 1 of the Instant Application that may capture light onto a light-***

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sensitive material, or, in the case of a digital camera, onto an electronic image sensor (i.e. CCD or CMOS). As such, "picture capturing apparatus" recited in Claim 1 of the Instant Application is used to capture pictures of the physical world. On the other hand, "renderer" recited in Claim 1 of the Application 15/245046 is used to render or generate digital pictures of a computer modeled or represented environment. Thus, the "picture capturing apparatus" recited in Claim 1 of the Instant Application that is used for capturing digital pictures of a physical world is clearly patentably distinct from the "renderer configured to render streams of digital pictures of a surrounding of the avatar of the application program" recited in Claim 1 of the Application 15/245046 that is used for rendering or generating digital pictures of a computer modeled or represented environment.

Second, ***since renderer is used for rendering or generating streams of digital pictures of a surrounding of the avatar of the application program as recited in Claim 1 of the Application 15/245046, it inherently uses mathematical techniques/calculations to process computer modeled or represented environments into digital pictures. This is unmistakably different than primarily mechanical capturing of light onto a light sensitive material or onto an image sensor of a picture capturing apparatus.*** Thus, the very functionality of a "picture capturing apparatus" is not possible with a "renderer", and the very functionality of a "renderer" is not possible with a "picture capturing apparatus" proving beyond a doubt that the two are patentably distinct. Although, the applicant absolutely maintains that it is not necessary, to further clarify beyond a doubt and show a clear patentable distinction, amended Claim 1 of the Instant Application now recites "optical camera that captures digital pictures". Even further, amended Claim 5 now recites "wherein the optical camera does not include a renderer configured to render streams of digital pictures of a surrounding of the avatar of the application program".

Therefore, the applicant has shown a clear patentable distinction between Claim 1 of the Instant Application and Claim 1 of the Application 15/245046.

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"Operating the device" in Claim 1 of the Instant Application is not the same as "operating the avatar of the application program" in Claim 1 of the Application 15/245046. (Does the Examiner assert that "operating the device" in Claim 1 of the Instant Application is the same as "operating the avatar of the application program" in Claim 1 of the Application 15/245046?)

First, ***the "device" in Claim 1 of the Instant Application may operate by performing physical operations as clearly described throughout the Instant Application***. On the other hand, ***"the avatar of the application program" operates by performing operations within an application program, which are not physical operations as clearly described throughout the Application 15/245046***. Thus, "operating the device" in Claim 1 of the Instant Application is clearly patentably distinct from "operating the avatar of the application program" in Claim 1 of the Application 15/245046. The same logic applies in Perez as Perez discloses an avatar that performs non-physical operations in a computer game.

Second, "operating the device" is subject to fixed physical laws such as gravity, acceleration, and friction, whereas, "operating the avatar of the application program" is subject to arbitrary computer-generated conditions. In one example, ***a 3D application program enables an avatar to walk through barriers (i.e. walls, fences, etc.), fly without a propellant, appear or disappear, and perform other operations impossible in the physical world***. In another example, ***a 2D application program enables an avatar to operate in a two dimensional plane which is beyond comparison with the physical world that is inherently three dimensional***. Thus, "operating the device" in Claim 1 of the Instant Application is clearly patentably distinct from "operating the avatar of the application program" in Claim 1 of the Application 15/245046. The same logic applies in Perez as Perez discloses an avatar that performs operations in a computer game.

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Therefore, the applicant has shown a clear patentable distinction between Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 even in view of Perez. Additionally, ***because "operating the device" and "operating the avatar of the application program" are frequently recited elements in Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 and are significant parts of multiple elements and/or operations, each of the elements and/or operations including these limitations are also patentably distinct.***

Claim 1 of the Application 15/245046 cannot be used at all for a valid obviousness double patenting rejection because it is directed to a different field of endeavor than Claim 1 of the Instant Application.

First, ***as all embodiments and examples of the Instant Application show, the Instant Application is directed to the field of robots, vehicles, and other physical devices***. On the other hand, ***as all embodiments and examples of the Application 15/245046 show, the Application 15/245046 is directed to the field of computer games and other computer generated environments***. For example, the field of vehicles is different from the field of kitchen appliances even though they share physical aspects. Hence, ***the field of robots, vehicles, and other physical devices is beyond comparison with the field of computer games and other computer generated environments.***

Second, because the fields of endeavor are different, ***the results of Claim 1 of the Instant Application are different than the results of Claim 1 of the Application 15/245046***, and ***Claim 1 of one Application cannot produce the other's results***. A definitive proof of this is attempting to enable "autonomous device operating" using the system in Claim 1 of the Application 15/245046, which would fail because Claim 1 of the Application 15/245046 does not have the capability to process the physical world and to operate in the physical world. Similarly, attempting to enable autonomous avatar operating using the system in Claim 1 of the Instant Application would fail because Claim 1

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of the Instant Application does not have the capability to process the computer generated environment and to operate in the computer generated environment. Hence, Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 do not produce one another's results.

Third, because the fields of endeavor and the results are different, the elements to enable these different results are also different. ***Claim 1 of the Application 15/245046 does not include anything like the "device", "optical camera that captures digital pictures", "operating the physical device", and/or other significant elements and/or operations as discussed above.*** On the other hand, ***Claim 1 of the Instant Application does not include anything like "an application program", "avatar of the application program", "renderer configured to render streams of digital pictures of a surrounding of the avatar of the application program", "operating the avatar of the application program", and/or other significant elements and/or operations as discussed above.*** Hence, the elements to enable the results in Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 are different.

Fourth, one of ordinary skill in the field of computer games and other computer generated environments is different than one of ordinary skill in robots, vehicles, and other physical devices and one does not have the training and knowledge of the other. A definitive proof of this is a realization that ***one of ordinary skill in the field of computer generated environments will not know much or anything about physical sensors and their complex signal processing, mechanical actuators, and device control***, whereas, ***one of ordinary skill in the field of physical devices will not know much or anything about 3D or 2D scenes and their complex mathematical processing, 3D or 2D models, and avatar control***. As a further proof and analogy, one of ordinary skill in mechanical arts who builds cars (i.e. car engineer) is different than and lacks knowledge of one of ordinary skill in mechanical arts who builds skyscrapers (i.e. building engineer). The Examiner is

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reminded that various analyses in patent examinations, including the analysis for double patenting, are performed from the perspective of one of ordinary skill in an art, and that one of ordinary skill in the art of computer games and other computer generated environments is different than and lacks training and knowledge of one of ordinary skill in the art of robots, vehicles, and other physical devices, proving that these are different arts/fields.

Fifth, Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 are directed to different practical uses. ***Claim 1 of the Instant Application is directed, for example, to robots and vehicles,*** whereas, ***Claim 1 of the Application 15/245046 is directed, for example, to dragons and other computer generated creatures.*** Robots and vehicles are clearly different from dragons and other computer generated creatures. ***Also, practical uses of Claim 1 of the Instant Application and practical uses of Claim 1 of the Application 15/245046 do not depend on nor even relate to one another.*** Hence, Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 are directed to different practical uses.

Therefore, for at least this reason alone, the double patenting rejection should be withdrawn because of the different fields of endeavor of Claim 1 of the Instant Application and Claim 1 of the Application 15/245046. The ***Examiner is reminded that showing that Claim 1 of the Application 15/245046 is in the same field of endeavor is only the most basic of several steps that must be met for inclusion of Claim 1 of the Application 15/245046 in a valid obviousness double patenting rejection. In addition to the showing that Claim 1 of the Application 15/245046 is in the same field of endeavor, Claim 1 of the Application 15/245046 must BOTH:***

i. ***show the same or obvious variants of elements in Claim 1 of the Instant Application, AND***
ii. ***produce results claimed in Claim 1 of the Instant Application.***

CLAIM 1 OF THE APPLICATION 15/245046 IS NOT EVEN IN THE SAME FIELD OF ENDEAVOR AS CLAIM 1 OF THE INSTANT APPLICATION, LET

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ALONE SHOWS THE SAME OR OBVIOUS ELEMENTS AS IN CLAIM 1 OF THE INSTANT APPLICATION, LET ALONE PRODUCES RESULTS OF CLAIM 1 OF THE INSTANT APPLICATION. Furthermore, the applicant has personally written every word of the Instant Application and the Application 15/245046 and has purposefully separated the two Applications because they are in different fields of endeavor, AND knows that they involve different elements, AND knows that one cannot produce results of the other. Therefore, the use of the Application 15/245046 and/or other prior art related to avatars of application programs does not form a valid obviousness double patenting rejection.

Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 do not infringe on each other.

First, it is important to understand that the original and especially amended Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 ***do not infringe on each other because of the many significant differences in their elements as discussed above*** with respect to "physical device", "optical camera that captures digital pictures", "operating the device", and other elements of Claim 1 of the Instant Application AND "application program", "avatar of the application program", "renderer configured to render streams of digital pictures of a surrounding of the avatar of the application program", "operating the avatar of the application program", and other elements of Claim 1 of the Application 15/245046.

Second, the Examiner is reminded that the same rules that courts use for determining infringement apply in determining obviousness in double patenting analysis. It is unfair to the applicant to impose common ownership of the Instant Application and the Application 15/245046 by forcing a filing of a terminal disclaimer, thereby limiting the applicant's ability to exercise his patent rights individually, when the fundamental purpose of the patent law is to grant patents that are not obvious over (i.e. do not infringe on) other prior art or the applicant's

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other patents or applications.

Therefore, for at least this reason alone, the double patenting rejection should be withdrawn because Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 do not infringe on each other.

In conclusion, the applicant has shown that:

- Perez cannot be combined with Claim 1 of the Application 15/245046 for a valid obviousness double patenting rejection.

Furthermore, the applicant has shown that:

(i) "device" in Claim 1 of the Instant Application is patentably distinct from "application program" in Claim 1 of the Application 15/245046,

(ii) "device" in Claim 1 of the Instant Application is patentably distinct from "avatar of the application program" in Claim 1 of the Application 15/245046,

(iii) "picture capturing apparatus" in Claim 1 of the Instant Application is patentably distinct from "renderer configured to render streams of digital pictures of a surrounding of the avatar of the application program" in Claim 1 of the Application 15/245046, and

(iv) "operating the device" in Claim 1 of the Instant Application is patentably distinct from "operating the avatar of the application program" in Claim 1 of the Application 15/245046.

Each of these distinct elements (i)-(iv) is a significant element of its respective system, which the system relies on for its correct functioning. Each of these significant elements alone, and especially all of them combined, is/are sufficient to render Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 patentably distinct on merit.

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Furthermore, although, the applicant absolutely maintains that original Claim 1 of the Instant Application and original Claim 1 of the Application 15/245046 are patentably distinct, the applicant has amended Claim 1 of the Instant Application to recite "system for autonomous device operating", "physical device", and "optical camera that captures digital pictures" to prove beyond a doubt a clear patentable distinction between Claims 1 of the Instant Application and the Application 15/245046.

Even further, the applicant has shown that:

- Claim 1 of the Application 15/245046 cannot be used at all for a valid obviousness double patenting rejection because it is directed to a different field of endeavor than Claim 1 of the Instant Application, and
- Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 do not infringe on each other.

Therefore, for any of these reasons alone, and especially all of them combined, Claim 1 of the Instant Application and Claim 1 of the Application 15/245046 are patentably distinct.

Independent Claims 13 and 17 correspond to the independent Claim 1. Thus, Claims 13 and 17 are allowable for at least the same reasons listed above regarding the independent Claim 1. Further, dependent Claims 2-12, 14-16, and 18-20 depend directly or indirectly from the independent Claims 1, 13, and 17 and are therefore allowable.

Dependent Claims 3, 5-9, and 12

First, although, the applicant absolutely maintains that independent Claim 1 is allowable per the arguments above, the applicant has amended dependent Claims 3, 5-9, and 12 to recite as follows.

Dependent Claim 3: "wherein the first digital picture is not used for

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identifying a user's behavior".

Dependent Claim 5: "wherein the physical device does not include an avatar of an application program, and wherein the optical camera does not include a renderer configured to render streams of digital pictures of a surrounding of the avatar of the application program".

Dependent Claim 6: "wherein the memory unit further stores at least a second digital picture correlated with a second one or more instruction sets for operating the physical device".

Dependent Claim 7: "wherein the first digital picture is a comparative digital picture whose at least one portion is compared with at least one portion of one or more digital pictures subsequent to the first digital picture".

Dependent Claim 8: "wherein the new digital picture is an anticipatory digital picture whose at least one portion is compared with at least one portion of one or more digital pictures prior to the new digital picture".

Dependent Claim 9: "wherein the first digital picture correlated with the first one or more instruction sets for operating the physical device are learned at least by determining that the first one or more instruction sets for operating the physical device temporally correspond to the first digital picture".

Dependent Claim 12: "wherein the memory unit further stores at least a knowledge structure, and wherein the knowledge structure includes: a first knowledge cell including the first digital picture correlated with the first one or more instruction sets for operating the physical device, and a second knowledge cell including a second digital picture correlated with a second one or more instruction sets for operating the physical device, and wherein the first knowledge cell is connected with the second knowledge cell by a connection".

Second, dependent Claims 3, 5-9, and 12 depend, directly or indirectly,

from independent Claim 1 and are therefore allowable for at least the same reasons that Claim 1 is allowable.

Therefore, the cited prior art as well as Claim 1 of the Application 15/245046 fail to disclose any of the limitations recited in the amended dependent Claims 3, 5-9, and 12.

Accordingly, in view of the arguments above, reconsideration of the double patenting rejection and allowance of dependent Claims 3, 5-9, and 12 is respectfully requested.

IV. Dependent Claims 2-12, 14-16, and 18-20

Dependent Claims 2-12, 14-16, and 18-20 depend, directly or indirectly, from independent Claims 1, 13, and 17 and are therefore allowable for at least the same reasons that Claims 1, 13, and 17 are allowable. Accordingly, allowance of dependent Claims 2-12, 14-16, and 18-20 is respectfully requested.

V. Interview Summary

The applicant thanks the Examiner for conducting a telephonic interview on February 22, 2018 with the applicant.

The Examiner agreed that dependent Claim 7 overcomes 35 U.S.C. 102 and 103 if included in the independent Claim 1. Also, concerning such independent Claim 1, the Examiner withdrew the double patenting rejection for all of the originally listed applications except for applications 15/191517 and 15/245046.

VI. Conclusion

The applicant respectfully submits that the amendments presented in this

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response should not be construed as an admission that the Claims as previously presented are not patentable, or that the cited prior art references anticipate or render obvious the Claims as previously presented. Indeed, the applicant respectfully submits that the Claims as previously presented, and/or further amendments thereto, also contain patentable subject material. Thus, the applicant reserves the right to pursue the Claims as previously presented, and/or further amendments thereto, in one or more continuation applications.

In view of the foregoing remarks, the applicant respectfully submits that the entire application is in condition for allowance. The applicant respectfully requests that a timely Notice of Allowance be issued in this case. If the Examiner would like to discuss any aspect of this application, the Examiner is respectfully requested to contact the undersigned at (317) 772-1312.

I hereby certify that this correspondence is being submitted electronically via EFS Web to the United States Patent and Trademark Office.

By /Jasmin Cosic/
Jasmin Cosic
Date submitted: March 11, 2018

Respectfully submitted,

/Jasmin Cosic/
Jasmin Cosic

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Electronic Acknowledgement Receipt

EFS ID:	32016447
Application Number:	15818765
International Application Number:	
Confirmation Number:	6002
Title of Invention:	DEVICES, SYSTEMS, AND METHODS FOR USE IN AUTOMATION
First Named Inventor/Applicant Name:	Jasmin Cosic
Customer Number:	116094
Filer:	Jasmin Cosic
Filer Authorized By:	
Attorney Docket Number:	
Receipt Date:	11-MAR-2018
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Time Stamp:	22:48:44
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		Answer.pdf	300461 2373b13d5381017893f750ff82dcb85ebf5e6586	yes	30

	Multipart Description/PDF files in .zip description		
	Document Description	**Start**	**End**
	Amendment/Req. Reconsideration-After Non-Final Reject	1	1
	Claims	2	10
	Applicant Arguments/Remarks Made in an Amendment	11	30
Warnings:			
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	Total Files Size (in bytes):	300461	

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.
National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.
New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

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PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875	Application or Docket Number 15/818,765	Filing Date 11/21/2017	☐ To be Mailed

ENTITY: ☐ LARGE ☒ SMALL ☐ MICRO

APPLICATION AS FILED – PART I

	(Column 1)	(Column 2)		
FOR	NUMBER FILED	NUMBER EXTRA	RATE ($)	FEE ($)
☐ BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A	N/A	
☐ SEARCH FEE (37 CFR 1.16(k), (i), or (m))	N/A	N/A	N/A	
☐ EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A	N/A	
TOTAL CLAIMS (37 CFR 1.16(i))	minus 20 =	*	X $ =	
INDEPENDENT CLAIMS (37 CFR 1.16(h))	minus 3 =	*	X $ =	
☐ APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is $310 ($155 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).			
☐ MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))				
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL	

APPLICATION AS AMENDED – PART II

AMENDMENT	03/11/2018	(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)
	Total (37 CFR 1.16(i))	* 20	Minus	** 20	= 0	X $50 =	0
	Independent (37 CFR 1.16(h))	* 3	Minus	***3	= 0	X $230 =	0
	☐ Application Size Fee (37 CFR 1.16(s))						
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))						
						TOTAL ADD'L FEE	0

AMENDMENT		(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)
	Total (37 CFR 1.16(i))	*	Minus	**	=	X $ =	
	Independent (37 CFR 1.16(h))	*	Minus	***	=	X $ =	
	☐ Application Size Fee (37 CFR 1.16(s))						
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))						
						TOTAL ADD'L FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

SLIE
TAMMY ACREE

This collection of information is required by 37 CFR 1.16. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**
If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/818,765	11/21/2017	Jasmin Cosic		6002

116094 7590 02/26/2018

Jasmin Cosic
108 Woodbury Street
Pawtucket, RI 02861

EXAMINER	
PARK, SOO JIN	

ART UNIT	PAPER NUMBER
2668	

MAIL DATE	DELIVERY MODE
02/26/2018	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Tesla Exhibit 1002.122

Applicant-Initiated Interview Summary	Application No.	Applicant(s)	
	15/818,765	COSIC, JASMIN	
	Examiner	Art Unit	
	SOO JIN PARK	2668	

All participants (applicant, applicant's representative, PTO personnel):

(1) *SOO JIN PARK*. (3)_____.

(2) *JASMIN COSIC*. (4)_____.

Date of Interview: *22 February 2018*.

Type: ☒ Telephonic ☐ Video Conference
☐ Personal [copy given to: ☐ applicant ☐ applicant's representative]

Exhibit shown or demonstration conducted: ☐ Yes ☐ No.
If Yes, brief description: _____.

Issues Discussed ☐101 ☐112 ☒102 ☒103 ☒Others
(For each of the checked box(es) above, please describe below the issue and detailed description of the discussion)

Claim(s) discussed: *1 and 7*.

Identification of prior art discussed: *Perez (USPAPN 2011/0007079) and Orme (NPL) listed in the Office Avtion of 02/09/2018*.

Substance of Interview
(For each issue discussed, provide a detailed description and indicate if agreement was reached. Some topics may include: identification or clarification of a reference or a portion thereof, claim interpretation, proposed amendments, arguments of any applied references etc...)

The examiner agreed that if claim 1 is further amended to incorporate the subject matter of claim 7, claim 1 would overcome the prior art rejection, and that the Double Patenting rejection would be withdrawn EXCEPT for applications 15/191517 and 15/245046. The examiner stated that the Double Patenting rejection would be maintained for these two applications, however, no agreement has been reached.

Applicant recordation instructions: The formal written reply to the last Office action must include the substance of the interview. (See MPEP section 713.04). If a reply to the last Office action has already been filed, applicant is given a non-extendable period of the longer of one month or thirty days from this interview date, or the mailing date of this interview summary form, whichever is later, to file a statement of the substance of the interview

Examiner recordation instructions: Examiners must summarize the substance of any interview of record. A complete and proper recordation of the substance of an interview should include the items listed in MPEP 713.04 for complete and proper recordation including the identification of the general thrust of each argument or issue discussed, a general indication of any other pertinent matters discussed regarding patentability and the general results or outcome of the interview, to include an indication as to whether or not agreement was reached on the issues raised.

☐ Attachment

/SOO JIN PARK/ Primary Examiner, Art Unit 2668	

Tesla Exhibit 1002.123

Summary of Record of Interview Requirements

Manual of Patent Examining Procedure (MPEP), Section 713.04, Substance of Interview Must be Made of Record
A complete written statement as to the substance of any face-to-face, video conference, or telephone interview with regard to an application must be made of record in the application whether or not an agreement with the examiner was reached at the interview.

Title 37 Code of Federal Regulations (CFR) § 1.133 Interviews
Paragraph (b)

In every instance where reconsideration is requested in view of an interview with an examiner, a complete written statement of the reasons presented at the interview as warranting favorable action must be filed by the applicant. An interview does not remove the necessity for reply to Office action as specified in §§ 1.111, 1.135. (35 U.S.C. 132)

37 CFR §1.2 Business to be transacted in writing.
All business with the Patent or Trademark Office should be transacted in writing. The personal attendance of applicants or their attorneys or agents at the Patent and Trademark Office is unnecessary. The action of the Patent and Trademark Office will be based exclusively on the written record in the Office. No attention will be paid to any alleged oral promise, stipulation, or understanding in relation to which there is disagreement or doubt.

The action of the Patent and Trademark Office cannot be based exclusively on the written record in the Office if that record is itself incomplete through the failure to record the substance of interviews.

It is the responsibility of the applicant or the attorney or agent to make the substance of an interview of record in the application file, unless the examiner indicates he or she will do so. It is the examiner's responsibility to see that such a record is made and to correct material inaccuracies which bear directly on the question of patentability.

Examiners must complete an Interview Summary Form for each interview held where a matter of substance has been discussed during the interview by checking the appropriate boxes and filling in the blanks. Discussions regarding only procedural matters, directed solely to restriction requirements for which interview recordation is otherwise provided for in Section 812.01 of the Manual of Patent Examining Procedure, or pointing out typographical errors or unreadable script in Office actions or the like, are excluded from the interview recordation procedures below. Where the substance of an interview is completely recorded in an Examiners Amendment, no separate Interview Summary Record is required.

The Interview Summary Form shall be given an appropriate Paper No., placed in the right hand portion of the file, and listed on the "Contents" section of the file wrapper. In a personal interview, a duplicate of the Form is given to the applicant (or attorney or agent) at the conclusion of the interview. In the case of a telephone or video-conference interview, the copy is mailed to the applicant's correspondence address either with or prior to the next official communication. If additional correspondence from the examiner is not likely before an allowance or if other circumstances dictate, the Form should be mailed promptly after the interview rather than with the next official communication.

The Form provides for recordation of the following information:
- Application Number (Series Code and Serial Number)
- Name of applicant
- Name of examiner
- Date of interview
- Type of interview (telephonic, video-conference, or personal)
- Name of participant(s) (applicant, attorney or agent, examiner, other PTO personnel, etc.)
- An indication whether or not an exhibit was shown or a demonstration conducted
- An identification of the specific prior art discussed
- An indication whether an agreement was reached and if so, a description of the general nature of the agreement (may be by attachment of a copy of amendments or claims agreed as being allowable). Note: Agreement as to allowability is tentative and does not restrict further action by the examiner to the contrary.
- The signature of the examiner who conducted the interview (if Form is not an attachment to a signed Office action)

It is desirable that the examiner orally remind the applicant of his or her obligation to record the substance of the interview of each case. It should be noted, however, that the Interview Summary Form will not normally be considered a complete and proper recordation of the interview unless it includes, or is supplemented by the applicant or the examiner to include, all of the applicable items required below concerning the substance of the interview.

A complete and proper recordation of the substance of any interview should include at least the following applicable items:
1) A brief description of the nature of any exhibit shown or any demonstration conducted,
2) an identification of the claims discussed,
3) an identification of the specific prior art discussed,
4) an identification of the principal proposed amendments of a substantive nature discussed, unless these are already described on the Interview Summary Form completed by the Examiner,
5) a brief identification of the general thrust of the principal arguments presented to the examiner,
   (The identification of arguments need not be lengthy or elaborate. A verbatim or highly detailed description of the arguments is not required. The identification of the arguments is sufficient if the general nature or thrust of the principal arguments made to the examiner can be understood in the context of the application file. Of course, the applicant may desire to emphasize and fully describe those arguments which he or she feels were or might be persuasive to the examiner.)
6) a general indication of any other pertinent matters discussed, and
7) if appropriate, the general results or outcome of the interview unless already described in the Interview Summary Form completed by the examiner.

Examiners are expected to carefully review the applicant's record of the substance of an interview. If the record is not complete and accurate, the examiner will give the applicant an extendable one month time period to correct the record.

Examiner to Check for Accuracy

If the claims are allowable for other reasons of record, the examiner should send a letter setting forth the examiner's version of the statement attributed to him or her. If the record is complete and accurate, the examiner should place the indication, "Interview Record OK" on the paper recording the substance of the interview along with the date and the examiner's initials.

Tesla Exhibit 1002.124

UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/818,765	11/21/2017	Jasmin Cosic		6002

116094 7590 02/09/2018

Jasmin Cosic
108 Woodbury Street
Pawtucket, RI 02861

EXAMINER	
PARK, SOO JIN	

ART UNIT	PAPER NUMBER
2668	

MAIL DATE	DELIVERY MODE
02/09/2018	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Tesla Exhibit 1002.125

Office Action Summary	Application No. 15/818,765	Applicant(s) COSIC, JASMIN	
	Examiner SOO JIN PARK	Art Unit 2668	AIA (First Inventor to File) Status Yes

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE *3* MONTHS FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *7 January 2018*.

☐ A declaration(s)/affidavit(s) under **37 CFR 1.130(b)** was/were filed on _____.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ An election was made by the applicant in response to a restriction requirement set forth during the interview on _____; the restriction requirement and election have been incorporated into this action.

4)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims*

5)☒ Claim(s) *1-20* is/are pending in the application.

5a) Of the above claim(s) _____ is/are withdrawn from consideration.

6)☐ Claim(s) _____ is/are allowed.

7)☒ Claim(s) *1-20* is/are rejected.

8)☐ Claim(s) _____ is/are objected to.

9)☐ Claim(s) _____ are subject to restriction and/or election requirement.

* If any claims have been determined allowable, you may be eligible to benefit from the **Patent Prosecution Highway** program at a participating intellectual property office for the corresponding application. For more information, please see http://www.uspto.gov/patents/init_events/pph/index.jsp or send an inquiry to PPHfeedback@uspto.gov.

Application Papers

10)☐ The specification is objected to by the Examiner.

11)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

Certified copies:

a)☐ All b)☐ Some** c)☐ None of the:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

** See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)

2) ☒ Information Disclosure Statement(s) (PTO/SB/08a and/or PTO/SB/08b) Paper No(s)/Mail Date _____.

3) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .

4) ☐ Other: _____.

Tesla Exhibit 1002.126

DETAILED ACTION

The present application, filed on or after March 16, 2013, is being examined under the first inventor to file provisions of the AIA.

Claim Interpretation

The following is a quotation of 35 U.S.C. 112(f):

> (f) Element in Claim for a Combination. – An element in a claim for a combination may be expressed as a means or step for performing a specified function without the recital of structure, material, or acts in support thereof, and such claim shall be construed to cover the corresponding structure, material, or acts described in the specification and equivalents thereof.

The following is a quotation of pre-AIA 35 U.S.C. 112, sixth paragraph:

> An element in a claim for a combination may be expressed as a means or step for performing a specified function without the recital of structure, material, or acts in support thereof, and such claim shall be construed to cover the corresponding structure, material, or acts described in the specification and equivalents thereof.

The claims in this application are given their broadest reasonable interpretation using the plain meaning of the claim language in light of the specification as it would be understood by one of ordinary skill in the art. The broadest reasonable interpretation of a claim element (also commonly referred to as a claim limitation) is limited by the description in the specification when 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, is invoked.

As explained in MPEP § 2181, subsection I, claim limitations that meet the following three-prong test will be interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph:

(A) the claim limitation uses the term “means” or “step” or a term used as a substitute for “means” that is a generic placeholder (also called a nonce term or a non-structural term having no specific structural meaning) for performing the claimed function;

(B) the term “means” or “step” or the generic placeholder is modified by functional language, typically, but not always linked by the transition word “for” (e.g., “means for”) or another linking word or phrase, such as “configured to” or “so that”; and

(C) the term “means” or “step” or the generic placeholder is not modified by sufficient structure, material, or acts for performing the claimed function.

Use of the word “means” (or “step”) in a claim with functional language creates a rebuttable presumption that the claim limitation is to be treated in accordance with 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph. The presumption that the claim limitation is interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, is rebutted when the claim limitation recites sufficient structure, material, or acts to entirely perform the recited function.

Absence of the word “means” (or “step”) in a claim creates a rebuttable presumption that the claim limitation is not to be treated in accordance with 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph. The presumption that the claim limitation is not interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, is rebutted when the claim limitation recites function without reciting sufficient structure, material or acts to entirely perform the recited function.

Claim limitations in this application that use the word “means” (or “step”) are being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, except as otherwise indicated in an Office action. Conversely, claim limitations in this application that do not use the word “means” (or “step”) are not being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, except as otherwise indicated in an Office action.

This application includes one or more claim limitations that do not use the word “means,” but are nonetheless being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, because the claim limitation(s) uses a generic placeholder that is coupled with functional language without reciting sufficient structure to perform the recited function and the generic placeholder is not preceded by a structural modifier. Such claim limitation(s) is/are: “units” in **claims 1-12**.

Because this/these claim limitation(s) is/are being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, it/they is/are being interpreted to cover the corresponding structure described in the specification as performing the claimed function, and equivalents thereof.

If applicant does not intend to have this/these limitation(s) interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph, applicant may: (1) amend the claim limitation(s) to avoid it/them being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph (e.g., by reciting sufficient structure to perform the claimed function); or (2) present a sufficient showing that the claim limitation(s) recite(s)

sufficient structure to perform the claimed function so as to avoid it/them being interpreted under 35 U.S.C. 112(f) or pre-AIA 35 U.S.C. 112, sixth paragraph.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (a)(1) the claimed invention was patented, described in a printed publication, or in public use, on sale or otherwise available to the public before the effective filing date of the claimed invention.

Claims 1, 3-6, and 8-20 are rejected under 35 U.S.C. 102(a)(1) as being anticipated by Perez et al. (USPAPN 2011/0007079).

Regarding **claim 1**, Perez discloses:

one or more processor circuits (see para [3], [19], and fig 2 and 6, a computer rendering an avatar and its background of an application);

a memory unit that stores at least a plurality of digital pictures correlated with one or more instruction sets for operating a device, wherein the plurality of digital pictures correlated with one or more instruction sets for operating the device include a first digital picture correlated with a first one or more instruction sets for operating the device; a picture capturing apparatus (see para [41] and [131], a camera and the computer for capturing and storing a user's first gesture and their corresponding first instructions); and

a decision-making unit configured to: access, in the memory unit, the first digital picture correlated with the first one or more instruction sets for operating the device; receive a new digital picture from the picture capturing apparatus; anticipate the first one or more instruction sets for operating the device based on at least partial match between the new digital picture and the first digital picture; and cause the one or more processor circuits to execute the first one or more instruction sets for operating the device, the causing performed in response to the anticipating of the decision-making unit, wherein the device performs one or more operations defined by the first one or more instruction sets for operating the device (see [108], [130], and [131], the camera and the computer capturing and recognizing a user's second gesture as corresponding to the first gesture based on matching gesture parameters, and implementing the first instruction on the avatar upon recognition).

Regarding **claim 3**, Perez further discloses the first one or more instruction sets for operating the device are obtained, via a hardwired connection, from at least one portion of the one or more processor circuits or from at least one portion of another one or more processor circuits (see para [42] and fig 2, wired connections).

Regarding **claim 4**, Perez further discloses wherein the causing the one or more processor circuits to execute the first one or more instruction sets for operating the device includes inserting the first one or more instruction sets for operating the device into an application for operating the device using at least an instrumentation of the application for operating the device, wherein the application for operating the device

runs on the one or more processor circuits (see rejection of claim 1, capturing and storing the user's first gesture and their corresponding first instructions).

Regarding **claim 5**, Perez further discloses wherein the causing the one or more processor circuits to execute the first one or more instruction sets for operating the device includes inserting, via a hardwired connection, the first one or more instruction sets for operating the device into at least one portion of the one or more processor circuits (see rejection of claims 3 and 4, capturing and storing the user's first gesture and their corresponding first instructions via wired connections).

Regarding **claim 6**, Perez further discloses wherein the first digital picture correlated with the first one or more instruction sets for operating the device includes a unit of knowledge of how the device operated in a visual surrounding (see para [124] and [125], identifying how the avatar of the application operated in accordance with the first instruction).

Regarding **claim 8**, Perez further discloses wherein the decision-making unit includes an interface for attaching to at least one of: the device, the one or more processor circuits, another one or more processor circuits, or an application for operating the device, wherein the application for operating the device runs on the one or more processor circuits or on another one or more processor circuits (see rejection of claim 1, the application and see fig 6, an interface for the application).

Regarding **claim 9**, Perez further discloses wherein the processor circuit includes a microcontroller (see para [40], a standardized processor, a specialized processor, a microprocessor, or the like).

Regarding **claim 10**, Perez further discloses wherein the first digital picture includes a still digital picture or a motion digital picture (see para [123], the user's first gesture may be static or dynamic).

Regarding **claim 11**, Perez further discloses wherein the decision-making unit includes at least one of: a hardware element that is included in the one or more processor circuits, a hardware element that is included in another one or more processor circuits, an application operating on the one or more processor circuits, an application operating on another one or more processor circuits, or an element coupled to the one or more processor circuits (see fig 2).

Regarding **claim 12**, Perez further discloses wherein at least one of: the one or more processor circuits, the memory unit, the picture capturing apparatus, or the decision-making unit are included in the device (see fig 2 and 6).

Regarding **claims 13-20**, Perez discloses everything claimed as applied above (see rejection of claims 1 4, 5, and 10).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103 which forms the basis for all obviousness rejections set forth in this Office action:

> A patent for a claimed invention may not be obtained, notwithstanding that the claimed invention is not identically disclosed as set forth in section 102, if the differences between the claimed invention and the prior art are such that the claimed invention as a whole would have been obvious before the effective filing date of the claimed invention to a person having ordinary skill in the art to which the claimed invention pertains. Patentability shall not be negated by the manner in which the invention was made.

Claim 2 is rejected under 35 U.S.C. 103 as being unpatentable over Prez in view of Orme ("System design tips for entry level smartphones – part 3").

Regarding **claim 2**, Perez further discloses wherein the first one or more instruction sets for operating the device are obtained from an application for operating the device (see para [131], the first instruction are obtained from the application), wherein the application for operating the device runs on the one or more processor circuits or on another one or more processor circuits (see fig 2 and 6).

However, Perez does not disclose using at least a tracing of the application for operating the device (Perez discloses, in para [19] and [73], that a CPU and GPU of a console, such as a portable telephone, are employed for the avatar application program, however, does not disclose a tracing of the avatar or the application program).

In a similar field of endeavor of running a gesture recognition application on a portable telephone, Orme discloses using at least a tracing of the application for operating the device (see sections "CoreSight debug and trace system speeds software development" and "CPU-GPU coherency", debugging, tracing, and profiling for smartphone applications).

Therefore, it would have been obvious to one of ordinary skill in the art before the effective filing date of the claimed invention to combine Perez with Orme, and employ a CPU and GPU of a portable telephone for recognizing a user's gesture and implementing instructions on an avatar, as disclosed by Perez, while further providing debugging, tracing, and profiling, as disclosed by Orme, for the purpose of achieving shorter software development cycles and lower costs while providing efficient support

for CPU-GPU coherency (see Orme sections “CoreSight debug and trace system speeds software development” and “CPU-GPU coherency”).

Double Patenting

The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the “right to exclude” granted by a patent and to prevent possible harassment by multiple assignees. A nonstatutory double patenting rejection is appropriate where the conflicting claims are not identical, but at least one examined application claim is not patentably distinct from the reference claim(s) because the examined application claim is either anticipated by, or would have been obvious over, the reference claim(s). See, e.g., *In re Berg*, 140 F.3d 1428, 46 USPQ2d 1226 (Fed. Cir. 1998); *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) or 1.321(d) may be used to overcome an actual or provisional rejection based on nonstatutory double patenting provided the reference application or patent either is shown to be commonly owned with the examined application, or claims an invention made as a result of activities undertaken within the scope of a joint research agreement. See MPEP § 717.02 for applications subject to examination under the first inventor to file provisions of the AIA as explained in MPEP § 2159. See MPEP §§ 706.02(l)(1) - 706.02(l)(3) for applications not subject to examination under the first inventor to file provisions of the AIA. A terminal disclaimer must be signed in compliance with 37 CFR 1.321(b).

The USPTO Internet website contains terminal disclaimer forms which may be used. Please visit www.uspto.gov/patent/patents-forms. The filing date of the application in which the form is filed determines what form (e.g., PTO/SB/25, PTO/SB/26, PTO/AIA/25, or PTO/AIA/26) should be used. A web-based eTerminal Disclaimer may be filled out completely online using web-screens. An eTerminal Disclaimer that meets all requirements is auto-processed and approved immediately upon submission. For more information about eTerminal Disclaimers, refer to www.uspto.gov/patents/process/file/efs/guidance/eTD-info-I.jsp.

Claims 1-20 are rejected on the ground of nonstatutory double patenting as being unpatentable over multiple applications 15/191517, 15/227971, 15/228599,

15/239817, 15/245046, 15/829984, 15/835434, 15/340991, 15/382743, 15/663751, 15/822150, and 15/842885, as the claims of these applications, either by themselves or in view of Perez, are not patentably distinct from the claims of the current application.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to SOO JIN PARK whose telephone number is (571)270-3569. The examiner can normally be reached on M-F 8:00am-5:00pm ET.

Examiner interviews are available via telephone, in-person, and video conferencing using a USPTO supplied web-based collaboration tool. To schedule an interview, applicant is encouraged to use the USPTO Automated Interview Request (AIR) at http://www.uspto.gov/interviewpractice.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, VU LE can be reached on (571)272-7332. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/SOO JIN PARK/
Primary Examiner, Art Unit 2668

Notice of References Cited	Application/Control No. 15/818,765	Applicant(s)/Patent Under Reexamination COSIC, JASMIN	
	Examiner SOO JIN PARK	Art Unit 2668	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	CPC Classification	US Classification
*	A	US-2011/0007079 A1	01-2011	Perez; Kathryn Stone	G06F3/011	345/473
	B	US-				
	C	US-				
	D	US-				
	E	US-				
	F	US-				
	G	US-				
	H	US-				
	I	US-				
	J	US-				
	K	US-				
	L	US-				
	M	US-				

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	CPC Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Orme, "System design tips for entry level smartphones - part 3", found online at "https://community.arm.com/processors/b/blog/posts/system-design-tips-for-entry-level-smartphones---part-3", Oct 21, 2013
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Tesla Exhibit 1002.138

Index of Claims	Application/Control No.	Applicant(s)/Patent Under Reexamination
	15818765	COSIC, JASMIN
	Examiner	**Art Unit**
	SOO PARK	2668

✓	Rejected	-	Cancelled	N	Non-Elected	A	Appeal
=	Allowed	÷	Restricted	I	Interference	O	Objected

☐ Claims renumbered in the same order as presented by applicant ☐ CPA ☐ T.D. ☐ R.1.47

CLAIM		DATE								
Final	Original	02/06/2018								
	1	✓								
	2	✓								
	3	✓								
	4	✓								
	5	✓								
	6	✓								
	7	✓								
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	14	✓								
	15	✓								
	16	✓								
	17	✓								
	18	✓								
	19	✓								
	20	✓								

Tesla Exhibit 1002.139

Doc code: IDS
Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (01-10)
Approved for use through 07/31/2012. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

U.S.PATENTS Remove

Examiner Initial*	Cite No	Patent Number	Kind Code[1]	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
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Examiner Initial*	Cite No	Publication Number	Kind Code[1]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	20030026588		2003-02-06	Elder, James H. ; et al.	
	2	20040249774		2004-12-09	Caid, William R. ; et al.	
	3	20040267521		2004-12-30	Cutler, Ross ; et al.	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	6	20070058856		2007-03-15	Boregowda; Lokesh R. ; et al.	
	7	20070106633		2007-05-10	Reiner; Bruce	
	8	20080144893		2008-06-19	GUO; Chun Biao ; et al.	
	9	20080288259		2008-11-20	Chambers; Robert L. ; et al.	
	10	20090110061		2009-04-30	LEE; Kang Yong ; et al.	
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	12	20090141969		2009-06-04	Yu; Kai ; et al.	
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	16	20100063949		2010-03-11	EATON; JOHN ERIC ; et al.	

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Tesla Exhibit 1002.141

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	17	20100241595		2010-09-23	Felsher; David Paul	
	18	20100278420		2010-11-04	Shet; Vinay Damodar ; et al.	
	19	20110030031		2011-02-03	Lussier; Paul ; et al.	
	20	20130156345		2013-06-20	Shmunk; Dmitry Valerievich	
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Tesla Exhibit 1002.142

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	28	20150055821		2015-02-26	Fotland; David Allen	

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FOREIGN PATENT DOCUMENTS Remove

Examiner Initial*	Cite No	Foreign Document Number[3]	Country Code[2]	Kind Code[4]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear	T[5]
	1							

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	1	ABOUT | OpenCV, retrieved from <URL: http://opencv.org/about.html> on Dec 13, 2014, 1 pages	
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Tesla Exhibit 1002.143

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

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Tesla Exhibit 1002.144

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.145

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	28	Outline of object recognition, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 7 pages	
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	36	Introduction Speech, retrieved from <URL: http://recognize-speech.com/speech> on Oct 18, 2015, 1 pages	
	37	Preprocessing, retrieved from <URL: http://recognize-speech.com/preprocessing> on Oct 18, 2015, 4 pages	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	39	Acoustic model, retrieved from <URL: http://recognize-speech.com/acoustic-model> on Oct 18, 2015, 2 pages	
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	43	Karen Simonyan, Andrew Zisserman, Two-Stream Convolutional Networks for Action Recognition in Videos, Nov 13, 2014, 11 pages, University of Oxford	

If you wish to add additional non-patent literature document citation information please click the Add button Add

EXAMINER SIGNATURE

Examiner Signature	/Soo Jin Park/	Date Considered	02/06/2018

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1] See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. [2] Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). [3] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [4] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. [5] Applicant is to place a check mark here if English language translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

OR

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Jasmin Cosic/	Date (YYYY-MM-DD)	2018-01-07
Name/Print	Jasmin Cosic	Registration Number	

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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Tesla Exhibit 1002.148

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The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether the Freedom of Information Act requires disclosure of these record s.

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3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.

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5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.

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7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.

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Tesla Exhibit 1002.149

Doc code: IDS
Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (01-10)
Approved for use through 07/31/2012. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

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Examiner Initial*	Cite No	Patent Number	Kind Code[1]	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	4370707		1983-01-25	Phillips , et al.	
	2	4730315		1988-03-08	Saito , et al.	
	3	4860203		1989-08-22	Corrigan , et al.	
	4	5602982		1997-02-11	Judd , et al.	
	5	6026234		2000-02-15	Hanson , et al.	
	6	6088731		2000-07-11	Kiraly , et al.	
	7	6106299		2000-08-22	Ackermann , et al.	
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Tesla Exhibit 1002.150

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	9	6314558		2001-11-06	Angel , et al.	
	10	6643842		2003-11-04	Angel , et al.	
	11	6728689		2004-04-27	Drissi , et al.	
	12	6735632		2004-05-11	Kiraly , et al.	
	13	6801912		2004-10-05	Moskowitz , et al.	
	14	6915105		2005-07-05	Masuda	
	15	6973446		2005-12-06	Mamitsuka , et al.	
	16	7017153		2006-03-21	Gouriou , et al.	
	17	7052277		2006-05-30	Kellman	
	18	7222127		2007-05-22	Bem , et al.	
	19	7240335		2007-07-03	Angel , et al.	

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Tesla Exhibit 1002.151

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	20	7249349		2007-07-24	Hundt , et al.	
	21	7424705		2008-09-09	Lewis , et al.	
	22	7478371		2009-01-13	Gove	
	23	7484205		2009-01-27	Venkatapathy	
	24	7721218		2010-05-18	Awe , et al.	
	25	7765537		2010-07-27	Havin , et al.	
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	27	7840060		2010-11-23	Podilchuk	
	28	7925984		2011-04-12	Awe , et al.	
	29	7987144		2011-07-26	Drissi , et al.	
	30	8005828		2011-08-23	Buchner , et al.	

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Tesla Exhibit 1002.152

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	31	8019699		2011-09-13	Baxter	
	32	8078556		2011-12-13	Adi , et al.	
	33	8090669		2012-01-03	Shahani , et al.	
	34	8137112		2012-03-20	Woolf , et al.	
	35	8166463		2012-04-24	Gill , et al.	
	36	8195674		2012-06-05	Bem , et al.	
	37	8196119		2012-06-05	Gill , et al.	
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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.153

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	42	8386401		2013-02-26	Virkar , et al.	
	43	8397227		2013-03-12	Fan , et al.	
	44	8464225		2013-06-11	Greifeneder	
	45	8549359		2013-10-01	Zheng	
	46	8589414		2013-11-19	Waite , et al.	
	47	8655260		2014-02-18	Yueh , et al.	
	48	8667472		2014-03-04	Molinari	

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U.S.PATENT APPLICATION PUBLICATIONS Remove

Examiner Initial*	Cite No	Publication Number	Kind Code[1]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	20120167057		2012-06-28	Schmich; Christopher P. ; et al.	
	2	20120150773		2012-06-14	DiCorpo; Phillip ; et al.	

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.154

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	3	20080254429		2008-10-16	Woolf; Susan D. ; et al.	
	4	20040117771		2004-06-17	Venkatapathy, Ramanathan	
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Examiner Initial*	Cite No	Foreign Document Number[3]	Country Code[2] i	Kind Code[4]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear	T[5]
	1							

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T[5]
	1	About Event Tracing, retrieved from <URL: http://msdn.microsoft.com/en-us/library/aa363668(d=default,l=en-us, v=vs.85).aspx> on Jan 12, 2014, 2 pages	
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Tesla Exhibit 1002.156

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	10	Program compilation and execution flow, retrieved from <URL: http://cs.stackexchange.com/questions/6187/program-compilation-and-execution-flow> on Jan 9, 2014, 2 pages	
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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.157

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.158

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.159

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	43	Program counter, retrieved from <URL: http://wikipedia.com> on Apr 24, 2014, 2 pages	
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	45	Reflection (computer programming), retrieved from <URL: http://wikipedia.com> on May 7, 2014, 5 pages	
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	49	System.CodeDom.Compiler Namespace, retrieved from <URL: http://msdn.microsoft.com/en-us/library/z6b99ydt (d=default,l=en-us,v=vs.110).aspx> on May 13, 2014, 2 pages	
	50	System.Reflection.Emit Namespace, retrieved from <URL: http://msdn.microsoft.com/en-us/library/xd5fw18y(d=default, l=en-us,v=vs.110).aspx> on May 13, 2014, 2 pages	

If you wish to add additional non-patent literature document citation information please click the Add button Add

EXAMINER SIGNATURE

Examiner Signature	/Soo Jin Park/	Date Considered	02/06/2018

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1] See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. [2] Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). [3] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [4] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. [5] Applicant is to place a check mark here if English language translation is attached.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.160

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

OR

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Jasmin Cosic/	Date (YYYY-MM-DD)	2018-01-07
Name/Print	Jasmin Cosic	Registration Number	

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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Tesla Exhibit 1002.161

Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

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2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.

3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.

4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).

5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.

6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).

7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.

8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspections or an issued patent.

9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

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Doc code: IDS
Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (01-10)
Approved for use through 07/31/2012. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

U.S.PATENTS Remove

Examiner Initial*	Cite No	Patent Number	Kind Code[1]	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	7113946		2006-09-26	Cosic	
	2	7117225		2006-10-03	Cosic	
	3	8335805		2012-12-18	Cosic	
	4	8417740		2013-04-09	Cosic	
	5	8572035		2013-10-29	Cosic	
	6	8655900		2014-02-18	Cosic	
	7	9047324		2015-06-02	Cosic	
	8	9282309		2016-03-08	Cosic	

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.163

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	9	9298749		2016-03-29	Cosic	
	10	9367806		2016-06-14	Cosic	
	11	9443192		2016-09-13	Cosic	
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	14	9697869		2017-07-04	Cosic	

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Examiner Initial*	Cite No	Publication Number	Kind Code[1]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	20030065662		2003-04-03	Cosic	
	2	20040194017		2004-09-30	Cosic	
	3	20050149517		2005-07-07	Cosic	

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Tesla Exhibit 1002.164

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	4	20050149542		2005-07-07	Cosic	
	5	20050289105		2005-12-29	Cosic	
	6	20100023541		2010-01-28	Cosic	
	7	20100082536		2010-04-01	Cosic	
	8	20130218932		2013-08-22	Cosic	
	9	20130226974		2013-08-29	Cosic	
	10	20160140999		2016-05-19	Cosic	
	11	20160142650		2016-05-19	Cosic	
	12	20160246819		2016-08-25	Cosic	
	13	20160246850		2016-08-25	Cosic	
	14	20160246868		2016-08-25	Cosic	

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Tesla Exhibit 1002.165

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	15	20160292185		2016-10-06	Cosic	

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Examiner Signature	/Soo Jin Park/	Date Considered	02/06/2018

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[1] See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. [2] Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). [3] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [4] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. [5] Applicant is to place a check mark here if English language translation is attached.

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Tesla Exhibit 1002.166

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

OR

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Jasmin Cosic/	Date (YYYY-MM-DD)	2018-01-07
Name/Print	Jasmin Cosic	Registration Number	

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.167

Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether the Freedom of Information Act requires disclosure of these record s.

2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.

3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.

4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).

5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.

6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).

7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.

8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspections or an issued patent.

9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.168

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	58031	(pose or postur$4 or gestur$4) with recogni$6	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2018/02/06 08:51
L2	9337	g06k9/00335-00355.cpc.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2018/02/06 08:52
L3	3855	(1 2) and avatar	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2018/02/06 08:52
L4	243874	(correlat$4 similar$4 match$4) with threshold$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2018/02/06 08:53
L5	671	3 and 4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2018/02/06 08:53

2/6/2018 9:06:47 AM
C:\Users\spark\Documents\EAST\Workspaces\15818765.wsp

Tesla Exhibit 1002.169

Search Notes	Application/Control No.	Applicant(s)/Patent Under Reexamination
	15818765	COSIC, JASMIN
	Examiner	Art Unit
	SOO PARK	2668

CPC- SEARCHED		
Symbol	Date	Examiner

CPC COMBINATION SETS - SEARCHED		
Symbol	Date	Examiner

US CLASSIFICATION SEARCHED			
Class	Subclass	Date	Examiner

* See search history printout included with this form or the SEARCH NOTES box below to determine the scope of the search.

SEARCH NOTES		
Search Notes	Date	Examiner
inventors search	2/6/2018	SJP
EAST - pose/posture/gesture, recognition, correlate/similarity/matching, threshold	2/6/2018	SJP
G06K 9/00335-00355 (narrowed search)	2/6/2018	SJP

INTERFERENCE SEARCH			
US Class/ CPC Symbol	US Subclass / CPC Group	Date	Examiner

Tesla Exhibit 1002.170

Doc code: IDS
Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (01-10)
Approved for use through 07/31/2012. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

U.S.PATENTS Remove

Examiner Initial*	Cite No	Patent Number	Kind Code[1]	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1					

If you wish to add additional U.S. Patent citation information please click the Add button. Add

U.S.PATENT APPLICATION PUBLICATIONS Remove

Examiner Initial*	Cite No	Publication Number	Kind Code[1]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1					

If you wish to add additional U.S. Published Application citation information please click the Add button. Add

FOREIGN PATENT DOCUMENTS Remove

Examiner Initial*	Cite No	Foreign Document Number[3]	Country Code[2] i	Kind Code[4]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear	T[5]
	1							

If you wish to add additional Foreign Patent Document citation information please click the Add button Add

NON-PATENT LITERATURE DOCUMENTS Remove

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T[5]

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

	1	Tracing (software), retrieved from <URL: http://wikipedia.com> on Jan 10, 2014, 3 pages	
	2	Tree (data structure), retrieved from <URL: http://wikipedia.com> on Jun 24, 2014, 6 pages	
	3	PTRACE(2), retrieved from <URL: http://unixhelp.ed.ac.uk/CGI/man-cgi?ptrace> on Mar 19, 2014, 5 pages	
	4	Wevtutil, retrieved from <URL: http://technet.microsoft.com/en-us/library/cc732848(d=default,l=en-us,v=ws.11).aspx> on Apr 28, 2014, 5 pages	
	5	Intel Processor Trace, retrieved from <URL: https://software.intel.com/en-us/blogs/2013/09/18/processor-tracing> on Apr 28, 2014, 3 pages	
	6	YOUNGHOON JUNG, JAVA DYNAMICS Reflection and a lot more, Oct 10, 2012, 55 pages, Columbia University	
	7	AMITABH SRIVASTAVA, ALAN EUSTACE, ATOM A System for Building Customized Program Analysis Tools, May 3, 2004, 12 pages	
	8	MATHEW SMITHSON, KAPIL ANAND, APARNA KOTHA, KHALED ELWAZEER, NATHAN GILES, RAJEEV BARUA, Binary Rewriting without Relocation Information, Nov 10, 2010, 11 pages, University of Maryland	
	9	MAREK OLSZEWSKI, KEIR MIERTE, ADAM CZAJKOWSKI, ANGELA DEMLE BROWN, JIT Instrumentation - A Novel Approach To Dynamically Instrument Operating Systems, Feb 12, 2007, 14 pages, University of Toronto	

If you wish to add additional non-patent literature document citation information please click the Add button Add

EXAMINER SIGNATURE

Examiner Signature	/Soo Jin Park/	Date Considered	02/06/2018

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

EFS Web 2.1.17

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

[1] See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. [2] Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). [3] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [4] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. [5] Applicant is to place a check mark here if English language translation is attached.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.173

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

OR

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Jasmin Cosic/	Date (YYYY-MM-DD)	2018-01-07
Name/Print	Jasmin Cosic	Registration Number	

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.174

Privacy Act Statement

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The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether the Freedom of Information Act requires disclosure of these record s.

2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.

3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.

4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).

5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.

6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).

7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.

8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspections or an issued patent.

9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SJP/

Tesla Exhibit 1002.175

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/818,765	11/21/2017	Jasmin Cosic		6002

116094 7590 01/25/2018

Jasmin Cosic
108 Woodbury Street
Pawtucket, RI 02861

EXAMINER	
PARK, CHAN S	

ART UNIT	PAPER NUMBER
2669	

MAIL DATE	DELIVERY MODE
01/25/2018	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Tesla Exhibit 1002.176

UNITED STATES PATENT AND TRADEMARK OFFICE

Commissioner for Patents
United States Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450
www.uspto.gov

Doc Code:
TRACK1.GRANT

Decision Granting Request for Prioritized Examination (Track I or After RCE)	Application No.: 15/818,765

1. THE REQUEST FILED November 21, 2017 IS **GRANTED.**

The above-identified application has met the requirements for prioritized examination
- A. ☒ for an original nonprovisional application (Track I).
- B. ☐ for an application undergoing continued examination (RCE).

2. **The above-identified application will undergo prioritized examination.** The application will be accorded special status throughout its entire course of prosecution until one of the following occurs:

- A. filing a **petition for extension of time** to extend the time period for filing a reply;
- B. filing an **amendment to amend the application to contain more than four independent claims, more than thirty total claims**, or a multiple dependent claim;
- C. filing a **request for continued examination**;
- D. filing a notice of appeal;
- E. filing a request for suspension of action;
- F. mailing of a notice of allowance;
- G. mailing of a final Office action;
- H. completion of examination as defined in 37 CFR 41.102; or
- I. abandonment of the application.

Telephone inquiries with regard to this decision should be directed to Irvin Dingle at (571)272-3210, Office of Petitions.

Irvin Dingle
/Irvin Dingle/
[*Signature*]

Lead Paralegal Specialist
(Title)

Tesla Exhibit 1002.177

Office of Petitions: Decision Count Sheet

Mailing Month: 1

Application No.: 15818765

15818765

For US serial numbers: enter number only, no slashes or commas. Ex: 10123456
For PCT: enter "51+single digit of year of filing+last 5 numbers", Ex. for PCT/US05/12345, enter 51512345

Deciding Official: Dingle, Irvin

Count (1) - Palm Credit 15818765

Decision: GRANT

FINANCE WORK NEEDED
☐ Select Check Box for YES

GRANT

Decision Type: 643 - Track One request

643

Notes:

Count (2)

Decision: n/a

FINANCE WORK NEEDED
☐ Select Check Box for YES

Decision Type: NONE

Notes:

Count (3)

Decision: n/a

FINANCE WORK NEEDED
☐ Select Check Box for YES

Decision Type: NONE

Notes:

Initials of Approving Official (if required)

If more than 3 decisions, attach 2nd count sheet & mark this box ☐

Printed on: 1/24/2018

Office of Petitions Internal Document - Ver. 5.0

Office of Petitions: Routing Sheet



Application No. 15818765

This application is being forwarded to your office for further processing. A decision has been rendered on a petition filed in this application, as indicated below. For details of this decision, please see the document PET.OP.DEC filed on the same date as this document.

- [X] **GRANTED**
- [ ] **DISMISSED**
- [ ] **DENIED**

PATENT APPLICATION FEE DETERMINATION RECORD

Substitute for Form PTO-875

Application or Docket Number: 15/818,765

APPLICATION AS FILED - PART I

FOR	(Column 1) NUMBER FILED	(Column 2) NUMBER EXTRA	SMALL ENTITY RATE($)	SMALL ENTITY FEE($)	OR	OTHER THAN SMALL ENTITY RATE($)	OTHER THAN SMALL ENTITY FEE($)
BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A	N/A	70		N/A	
SEARCH FEE (37 CFR 1.16(k), (i), or (m))	N/A	N/A	N/A	300		N/A	
EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A	N/A	360		N/A	
TOTAL CLAIMS (37 CFR 1.16(i))	20 minus 20 =	*	x 40 =	0.00	OR		
INDEPENDENT CLAIMS (37 CFR 1.16(h))	3 minus 3 =	*	x 210 =	0.00			
APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is $310 ($155 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).			200			
MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))				0.00			
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL	930		TOTAL	

APPLICATION AS AMENDED - PART II

AMENDMENT A

	(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	SMALL ENTITY RATE($)	SMALL ENTITY ADDITIONAL FEE($)	OR	OTHER THAN SMALL ENTITY RATE($)	OTHER THAN SMALL ENTITY ADDITIONAL FEE($)
Total (37 CFR 1.16(i))	*	Minus	**	=	x =		OR	x =	
Independent (37 CFR 1.16(h))	*	Minus	***	=	x =		OR	x =	
Application Size Fee (37 CFR 1.16(s))									
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
					TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	

AMENDMENT B

	(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	SMALL ENTITY RATE($)	SMALL ENTITY ADDITIONAL FEE($)	OR	OTHER THAN SMALL ENTITY RATE($)	OTHER THAN SMALL ENTITY ADDITIONAL FEE($)
Total (37 CFR 1.16(i))	*	Minus	**	=	x =		OR	x =	
Independent (37 CFR 1.16(h))	*	Minus	***	=	x =		OR	x =	
Application Size Fee (37 CFR 1.16(s))									
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
					TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.

** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".

*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".

The "Highest Number Previously Paid For" (Total or Independent) is the highest found in the appropriate box in column 1.

Tesla Exhibit 1002.180

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	TOT CLAIMS	IND CLAIMS
15/818,765	11/21/2017		930		20	3

CONFIRMATION NO. 6002

UPDATED FILING RECEIPT

116094
Jasmin Cosic
108 Woodbury Street
Pawtucket, RI 02861

OC000000096542907

Date Mailed: 01/09/2018

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please submit a written request for a Filing Receipt Correction. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections**

Inventor(s)

Jasmin Cosic, Miami, FL;

Applicant(s)

Jasmin Cosic, Miami, FL;

Power of Attorney: None

Domestic Applications for which benefit is claimed - None.
A proper domestic benefit claim must be provided in an Application Data Sheet in order to constitute a claim for domestic benefit. See 37 CFR 1.76 and 1.78.

Foreign Applications for which priority is claimed (You may be eligible to benefit from the **Patent Prosecution Highway** program at the USPTO. Please see http://www.uspto.gov for more information.) - None.
Foreign application information must be provided in an Application Data Sheet in order to constitute a claim to foreign priority. See 37 CFR 1.55 and 1.76.

Permission to Access Application via Priority Document Exchange: No

Permission to Access Search Results: No

Applicant may provide or rescind an authorization for access using Form PTO/SB/39 or Form PTO/SB/69 as appropriate.

If Required, Foreign Filing License Granted: 12/19/2017

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 15/818,765**

Tesla Exhibit 1002.181

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No
** **SMALL ENTITY** **
Title

DEVICES, SYSTEMS, AND METHODS FOR USE IN AUTOMATION

Preliminary Class

Statement under 37 CFR 1.55 or 1.78 for AIA (First Inventor to File) Transition Applications: No

PROTECTING YOUR INVENTION OUTSIDE THE UNITED STATES

Since the rights granted by a U.S. patent extend only throughout the territory of the United States and have no effect in a foreign country, an inventor who wishes patent protection in another country must apply for a patent in a specific country or in regional patent offices. Applicants may wish to consider the filing of an international application under the Patent Cooperation Treaty (PCT). An international (PCT) application generally has the same effect as a regular national patent application in each PCT-member country. The PCT process **simplifies** the filing of patent applications on the same invention in member countries, but **does not result** in a grant of "an international patent" and does not eliminate the need of applicants to file additional documents and fees in countries where patent protection is desired.

Almost every country has its own patent law, and a person desiring a patent in a particular country must make an application for patent in that country in accordance with its particular laws. Since the laws of many countries differ in various respects from the patent law of the United States, applicants are advised to seek guidance from specific foreign countries to ensure that patent rights are not lost prematurely.

Applicants also are advised that in the case of inventions made in the United States, the Director of the USPTO must issue a license before applicants can apply for a patent in a foreign country. The filing of a U.S. patent application serves as a request for a foreign filing license. The application's filing receipt contains further information and guidance as to the status of applicant's license for foreign filing.

Applicants may wish to consult the USPTO booklet, "General Information Concerning Patents" (specifically, the section entitled "Treaties and Foreign Patents") for more information on timeframes and deadlines for filing foreign patent applications. The guide is available either by contacting the USPTO Contact Center at 800-786-9199, or it can be viewed on the USPTO website at http://www.uspto.gov/web/offices/pac/doc/general/index.html.

For information on preventing theft of your intellectual property (patents, trademarks and copyrights), you may wish to consult the U.S. Government website, http://www.stopfakes.gov. Part of a Department of Commerce initiative, this website includes self-help "toolkits" giving innovators guidance on how to protect intellectual property in specific countries such as China, Korea and Mexico. For questions regarding patent enforcement issues, applicants may call the U.S. Government hotline at 1-866-999-HALT (1-866-999-4258).

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LICENSE FOR FOREIGN FILING UNDER

Title 35, United States Code, Section 184

Title 37, Code of Federal Regulations, 5.11 & 5.15

GRANTED

The applicant has been granted a license under 35 U.S.C. 184, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" followed by a date appears on this form. Such licenses are issued in all applications where the conditions for issuance of a license have been met, regardless of whether or not a license may be required as set forth in 37 CFR 5.15. The scope and limitations of this license are set forth in 37 CFR 5.15(a) unless an earlier license has been issued under 37 CFR 5.15(b). The license is subject to revocation upon written notification. The date indicated is the effective date of the license, unless an earlier license of similar scope has been granted under 37 CFR 5.13 or 5.14.

This license is to be retained by the licensee and may be used at any time on or after the effective date thereof unless it is revoked. This license is automatically transferred to any related applications(s) filed under 37 CFR 1.53(d). This license is not retroactive.

The grant of a license does not in any way lessen the responsibility of a licensee for the security of the subject matter as imposed by any Government contract or the provisions of existing laws relating to espionage and the national security or the export of technical data. Licensees should apprise themselves of current regulations especially with respect to certain countries, of other agencies, particularly the Office of Defense Trade Controls, Department of State (with respect to Arms, Munitions and Implements of War (22 CFR 121-128)); the Bureau of Industry and Security, Department of Commerce (15 CFR parts 730-774); the Office of Foreign AssetsControl, Department of Treasury (31 CFR Parts 500+) and the Department of Energy.

NOT GRANTED

No license under 35 U.S.C. 184 has been granted at this time, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" DOES NOT appear on this form. Applicant may still petition for a license under 37 CFR 5.12, if a license is desired before the expiration of 6 months from the filing date of the application. If 6 months has lapsed from the filing date of this application and the licensee has not received any indication of a secrecy order under 35 U.S.C. 181, the licensee may foreign file the application pursuant to 37 CFR 5.15(b).

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PTO/SB/08a (01-10)
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

U.S.PATENTS Remove

Examiner Initial*	Cite No	Patent Number	Kind Code[1]	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	4370707		1983-01-25	Phillips , et al.	
	2	4730315		1988-03-08	Saito , et al.	
	3	4860203		1989-08-22	Corrigan , et al.	
	4	5602982		1997-02-11	Judd , et al.	
	5	6026234		2000-02-15	Hanson , et al.	
	6	6088731		2000-07-11	Kiraly , et al.	
	7	6106299		2000-08-22	Ackermann , et al.	
	8	6126330		2000-10-03	Knight	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	9	6314558		2001-11-06	Angel , et al.	
	10	6643842		2003-11-04	Angel , et al.	
	11	6728689		2004-04-27	Drissi , et al.	
	12	6735632		2004-05-11	Kiraly , et al.	
	13	6801912		2004-10-05	Moskowitz , et al.	
	14	6915105		2005-07-05	Masuda	
	15	6973446		2005-12-06	Mamitsuka , et al.	
	16	7017153		2006-03-21	Gouriou , et al.	
	17	7052277		2006-05-30	Kellman	
	18	7222127		2007-05-22	Bem , et al.	
	19	7240335		2007-07-03	Angel , et al.	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
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	20	7249349		2007-07-24	Hundt , et al.	
	21	7424705		2008-09-09	Lewis , et al.	
	22	7478371		2009-01-13	Gove	
	23	7484205		2009-01-27	Venkatapathy	
	24	7721218		2010-05-18	Awe , et al.	
	25	7765537		2010-07-27	Havin , et al.	
	26	7797259		2010-09-14	Jiang , et al.	
	27	7840060		2010-11-23	Podilchuk	
	28	7925984		2011-04-12	Awe , et al.	
	29	7987144		2011-07-26	Drissi , et al.	
	30	8005828		2011-08-23	Buchner , et al.	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
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	32	8078556		2011-12-13	Adi , et al.	
	33	8090669		2012-01-03	Shahani , et al.	
	34	8137112		2012-03-20	Woolf , et al.	
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	40	8266608		2012-09-11	Hecht , et al.	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	42	8386401		2013-02-26	Virkar , et al.	
	43	8397227		2013-03-12	Fan , et al.	
	44	8464225		2013-06-11	Greifeneder	
	45	8549359		2013-10-01	Zheng	
	46	8589414		2013-11-19	Waite , et al.	
	47	8655260		2014-02-18	Yueh , et al.	
	48	8667472		2014-03-04	Molinari	

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Examiner Initial*	Cite No	Publication Number	Kind Code[1]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	20120167057		2012-06-28	Schmich; Christopher P. ; et al.	
	2	20120150773		2012-06-14	DiCorpo; Phillip ; et al.	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	3	20080254429		2008-10-16	Woolf; Susan D. ; et al.	
	4	20040117771		2004-06-17	Venkatapathy, Ramanathan	
	5	20130238533		2013-09-12	VIRKAR; Hemant ; et al.	
	6	20060190930		2006-08-24	Hecht; Daniel M. ; et al.	
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	10	20110218672		2011-09-08	Maisonnier; Bruno ; et al.	

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	Application Number	15818765
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	Art Unit	
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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T5
	1	About Event Tracing, retrieved from <URL: http://msdn.microsoft.com/en-us/library/aa363668(d=default,l=en-us, v=vs.85).aspx> on Jan 12, 2014, 2 pages	
	2	Add dynamic Java code to your application, retrieved from <URL: http://www.javaworld.com/article/2071777/design-patterns/add-dynamic-java-code-to-your-application.html> on May 13, 2014, 6 pages	
	3	BCEL, retrieved from <URL: http://commons.apache.org> on May 13, 2014, 2 pages	
	4	Artificial intelligence, retrieved from <URL: http://wikipedia.com> on May 21, 2014, 28 pages	
	5	Artificial neural network, retrieved from <URL: http://wikipedia.com> on May 21, 2014, 12 pages	
	6	Branch (computer science), retrieved from <URL: http://wikipedia.com> on May 6, 2014, 2 pages	
	7	Branch trace, retrieved from <URL: http://wikipedia.com> on May 28, 2014, 2 pages	
	8	C Function Call Conventions and the Stack, retrieved from <URL: http://www.csee.umbc.edu/~chang/cs313.s02/stack.shtml> on Jan 11, 2014, 4 pages	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
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	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	10	Program compilation and execution flow, retrieved from <URL: http://cs.stackexchange.com/questions/6187/program-compilation-and-execution-flow> on Jan 9, 2014, 2 pages	
	11	Control unit, retrieved from <URL: http://wikipedia.com> on May 25, 2014, 2 pages	
	12	Create dynamic applications with javax.tools, retrieved from <URL: http://www.ibm.com/developerworks/library/j-jcomp/> on May 13, 2014, 11 pages	
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	14	Decision tree learning, retrieved from <URL: http://wikipedia.com> on Jun 24, 2014, 5 pages	
	15	Dynamic Bytecode Instrumentation, retrieved from <URL: http://www.drdobbs.com/tools/dynamic-bytecode-instrumentation/184406433> on Apr 26, 2014, 6 pages	
	16	Dynamic programming language, retrieved from <URL: http://wikipedia.com> on May 7, 2014, 4 pages	
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	18	eval(), retrieved from <URL: https://developer.mozilla.org/en-US/docs/Web/JavaScript/Reference/Global_Objects/eval> on May 7, 2014, 13 pages	
	19	Function, retrieved from <URL: https://developer.mozilla.org/en-US/docs/Web/JavaScript/Reference/Global_Objects/Function> on May 7, 2014, 11 pages	
	20	FunctionEnter2 Function, retrieved from <URL: https://msdn.microsoft.com/en-us/library/aa964981(d=default,l=en-us,v=vs.110).aspx> on Mar 19, 2014, 2 pages	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	21	FunctionLeave2 Function, retrieved from <URL: https://msdn.microsoft.com/en-us/library/aa964942(d=default,l=en-us,v=vs.110).aspx> on Mar 19, 2014, 1 pages	
	22	Heuristic, retrieved from <URL: http://wikipedia.com> on May 22, 2014, 5 pages	
	23	How to: Configure Trace Switches, retrieved from <URL: https://msdn.microsoft.com/en-us/library/vstudio/t06xyy08(d=default,l=en-us,v=vs.100).aspx> on Mar 19, 2014, 3 pages	
	24	Indirect branch, retrieved from <URL: http://wikipedia.com> on May 5, 2014, 1 pages	
	25	Instruction cycle, retrieved from <URL: http://wikipedia.com> on Apr 30, 2014, 2 pages	
	26	Instruction set, retrieved from <URL: http://wikipedia.com> on Apr 29, 2014, 6 pages	
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	28	Introduction to Instrumentation and Tracing, retrieved from <URL: http://msdn.microsoft.com/en-us/library/aa983649(d=default,l=en-us,v=vs.71).aspx> on Jan 10, 2014, 2 pages	
	29	Java bytecode, retrieved from <URL: http://wikipedia.com> on May 27, 2014, 4 pages	
	30	Package java.util.logging, retrieved from <URL: http://docs.oracle.com/javase/1.5.0/docs/api/java/util/logging/package-summary.html> on Mar 19, 2014, 2 pages	
	31	javac - Java programming language compiler, retrieved from <URL: http://docs.oracle.com/javase/7/docs/technotes/tools/windows/javac.html> on May 13, 2014, 12 pages	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	32	Interface JavaCompiler, retrieved from <URL: http://wikipedia.com> on May 13, 2014, 4 pages	
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	34	Just-in-time compilation, retrieved from <URL: http://wikipedia.com> on May 19, 2014, 5 pages	
	35	JVM Tool Interface, retrieved from <URL: http://docs.oracle.com/javase/7/docs/platform/jvmti/jvmti.html> on Apr 26, 2014, 127 pages	
	36	Machine code, retrieved from <URL: http://wikipedia.com> on Apr 30, 2014, 3 pages	
	37	Metaprogramming, retrieved from <URL: http://wikipedia.com> on May 8, 2014, 3 pages	
	38	Microsoft.VisualBasic.Logging Namespace, retrieved from <URL: http://msdn.microsoft.com/en-us/library/ms128122(d=default,l=en-us,v=vs.110).aspx> on Jan 13, 2014, 1 pages	
	39	Pin (computer program), retrieved from <URL: http://wikipedia.com> on May 6, 2014, 3 pages	
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	41	Profiling (computer programming), retrieved from <URL: http://wikipedia.com> on Jan 10, 2014, 4 pages	
	42	Profiling Overview, retrieved from <URL: http://msdn.microsoft.com/en-us/library/bb384493(d=default,l=en-us,v=vs.110).aspx> on Jan 12, 2014, 6 pages	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	43	Program counter, retrieved from <URL: http://wikipedia.com> on Apr 24, 2014, 2 pages
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	46	Class Runtime, retrieved from <URL: http://docs.oracle.com/javase/7/docs/api/java/lang/Runtime.html> on Jan 10, 2014, 10 pages
	47	Self-modifying code, retrieved from <URL: http://wikipedia.com> on Jan 12, 2014, 6 pages
	48	Subjective logic, retrieved from <URL: http://wikipedia.com> on May 22, 2014, 5 pages
	49	System.CodeDom.Compiler Namespace, retrieved from <URL: http://msdn.microsoft.com/en-us/library/z6b99ydt(d=default,l=en-us,v=vs.110).aspx> on May 13, 2014, 2 pages
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Tesla Exhibit 1002.194

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

OR

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Jasmin Cosic/	Date (YYYY-MM-DD)	2018-01-07
Name/Print	Jasmin Cosic	Registration Number	

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether the Freedom of Information Act requires disclosure of these record s.

2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.

3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.

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5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.

6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).

7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.

8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspections or an issued patent.

9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

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Examiner Initial*	Cite No	Patent Number	Kind Code[1]	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
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Examiner Initial*	Cite No	Publication Number	Kind Code[1]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
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Examiner Initial*	Cite No	Foreign Document Number[3]	Country Code[2] i	Kind Code[4]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear	T[5]
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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T[5]

Tesla Exhibit 1002.197

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	1	Tracing (software), retrieved from <URL: http://wikipedia.com> on Jan 10, 2014, 3 pages
	2	Tree (data structure), retrieved from <URL: http://wikipedia.com> on Jun 24, 2014, 6 pages
	3	PTRACE(2), retrieved from <URL: http://unixhelp.ed.ac.uk/CGI/man-cgi?ptrace> on Mar 19, 2014, 5 pages
	4	Wevtutil, retrieved from <URL: http://technet.microsoft.com/en-us/library/cc732848(d=default,l=en-us,v=ws.11).aspx> on Apr 28, 2014, 5 pages
	5	Intel Processor Trace, retrieved from <URL: https://software.intel.com/en-us/blogs/2013/09/18/processor-tracing> on Apr 28, 2014, 3 pages
	6	YOUNGHOON JUNG, JAVA DYNAMICS Reflection and a lot more, Oct 10, 2012, 55 pages, Columbia University
	7	AMITABH SRIVASTAVA, ALAN EUSTACE, ATOM A System for Building Customized Program Analysis Tools, May 3, 2004, 12 pages
	8	MATHEW SMITHSON, KAPIL ANAND, APARNA KOTHA, KHALED ELWAZEER, NATHAN GILES, RAJEEV BARUA, Binary Rewriting without Relocation Information, Nov 10, 2010, 11 pages, University of Maryland
	9	MAREK OLSZEWSKI, KEIR MIERTE, ADAM CZAJKOWSKI, ANGELA DEMLE BROWN, JIT Instrumentation - A Novel Approach To Dynamically Instrument Operating Systems, Feb 12, 2007, 14 pages, University of Toronto

If you wish to add additional non-patent literature document citation information please click the Add button Add

EXAMINER SIGNATURE

Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Tesla Exhibit 1002.198

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

[1] See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. [2] Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). [3] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [4] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. [5] Applicant is to place a check mark here if English language translation is attached.

Tesla Exhibit 1002.199

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

OR

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Jasmin Cosic/	Date (YYYY-MM-DD)	2018-01-07
Name/Print	Jasmin Cosic	Registration Number	

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

Tesla Exhibit 1002.200

Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether the Freedom of Information Act requires disclosure of these record s.

2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.

3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.

4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).

5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.

6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).

7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.

8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspections or an issued patent.

9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

Tesla Exhibit 1002.201

Doc code: IDS
Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (01-10)
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

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Examiner Initial*	Cite No	Patent Number	Kind Code[1]	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
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Examiner Initial*	Cite No	Publication Number	Kind Code[1]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	20030026588		2003-02-06	Elder, James H. ; et al.	
	2	20040249774		2004-12-09	Caid, William R. ; et al.	
	3	20040267521		2004-12-30	Cutler, Ross ; et al.	
	4	20050245303		2005-11-03	Graepel, Kurt Hartwig ; et al.	
	5	20060047612		2006-03-02	Stanley; Kenneth O. ; et al.	

Tesla Exhibit 1002.202

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	6	20070058856		2007-03-15	Boregowda; Lokesh R. ; et al.	
	7	20070106633		2007-05-10	Reiner; Bruce	
	8	20080144893		2008-06-19	GUO; Chun Biao ; et al.	
	9	20080288259		2008-11-20	Chambers; Robert L. ; et al.	
	10	20090110061		2009-04-30	LEE; Kang Yong ; et al.	
	11	20090131152		2009-05-21	Busse; Martin A.	
	12	20090141969		2009-06-04	Yu; Kai ; et al.	
	13	20090222388		2009-09-30	Hua; Wei ; et al.	
	14	20090324010		2009-12-31	Hou; Billy	
	15	20100033780		2010-02-11	Gitter; Kurt	
	16	20100063949		2010-03-11	EATON; JOHN ERIC ; et al.	

Tesla Exhibit 1002.203

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	17	20100241595		2010-09-23	Felsher; David Paul	
	18	20100278420		2010-11-04	Shet; Vinay Damodar ; et al.	
	19	20110030031		2011-02-03	Lussier; Paul ; et al.	
	20	20130156345		2013-06-20	Shmunk; Dmitry Valerievich	
	21	20130159021		2013-06-20	Felsher; David Paul	
	22	20130278631		2013-10-24	Border; John N. ; et al.	
	23	20140052717		2014-02-20	FRANKS; Kasian ; et al.	
	24	20140161250		2014-06-12	Leeds; Richard ; et al.	
	25	20140177946		2014-06-26	LIM; Kil-Taek ; et al.	
	26	20140207580		2014-06-24	Minnis; William Charles ; et al.	
	27	20150039304		2015-02-05	Wein; Ron	

Tesla Exhibit 1002.204

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	28	20150055821		2015-02-26	Fotland; David Allen	

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Examiner Initial*	Cite No	Foreign Document Number[3]	Country Code[2] i	Kind Code[4]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear	T[5]
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	1	ABOUT | OpenCV, retrieved from <URL: http://opencv.org/about.html> on Dec 13, 2014, 1 pages	
	2	Animetrics, Inc. | 3D Facial Recognition, retrieved from <URL: http://animetrics.com/> on Dec 13, 2014, 2 pages	
	3	Bag-of-words model, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 2 pages	
	4	Beier–Neely morphing algorithm, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 1 pages	
	5	Computer vision, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 7 pages	

Tesla Exhibit 1002.205

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	6	Convolutional neural network, retrieved from <URL: http://wikipedia.com> on Nov 11, 2015, 5 pages	
	7	Decimation (signal processing), retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 3 pages	
	8	Digital image processing, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 3 pages	
	9	Dissolve (filmmaking), retrieved from <URL: http://wikipedia.com> on Nov 11, 2015, 2 pages	
	10	Facial recognition system, retrieved from <URL: http://wikipedia.com> on Nov 11, 2015, 6 pages	
	11	Feature detection (computer vision), retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 3 pages	
	12	Feature extraction, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 2 pages	
	13	Gesture recognition, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 5 pages	
	14	GrabCut, retrieved from <URL: http://wikipedia.com> on Nov 1, 2015, 1 pages	
	15	Image processing, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 2 pages	
	16	Image segmentation, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 12 pages	

Tesla Exhibit 1002.206

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	17	Image warping, retrieved from <URL: http://wikipedia.com> on Nov 1, 2015, 2 pages	
	18	Inbetweening, retrieved from <URL: http://wikipedia.com> on Nov 11, 2015, 2 pages	
	19	Interpolation, retrieved from <URL: http://wikipedia.com> on Nov 1, 2015, 5 pages	
	20	Language model, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 4 pages	
	21	List of speech recognition software, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 4 pages	
	22	Livewire Segmentation Technique, retrieved from <URL: http://wikipedia.com> on Nov 1, 2015, 2 pages	
	23	Morphing, retrieved from <URL: http://wikipedia.com> on Nov 11, 2015, 2 pages	
	24	Motion estimation, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 2 pages	
	25	Muse: Face Morph || Mesh Warping, retrieved from <URL: http://alexwolfe.blogspot.com/2011/10/face-morph-mesh-warping.html> on Nov 19, 2015, 2 pages	
	26	n-gram, retrieved from <URL: http://wikipedia.com> on Nov 1, 2015, 5 pages	
	27	Optical flow, retrieved from <URL: http://wikipedia.com> on Nov 1, 2015, 4 pages	

Tesla Exhibit 1002.207

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	28	Outline of object recognition, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 7 pages	
	29	Phonotactics, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 3 pages	
	30	Recurrent neural network, retrieved from <URL: http://wikipedia.com> on Nov 11, 2015, 5 pages	
	31	Sample rate conversion, retrieved from <URL: http://wikipedia.com> on Nov 1, 2015, 2 pages	
	32	Simple interactive object extraction, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 2 pages	
	33	Speech recognition, retrieved from <URL: http://wikipedia.com> on Nov 11, 2015, 12 pages	
	34	Speech segmentation, retrieved from <URL: http://wikipedia.com> on Nov 1, 2015, 4 pages	
	35	Welcome to recognize-speech.com, retrieved from <URL: http://recognize-speech.com/> on Oct 18, 2015, 1 pages	
	36	Introduction Speech, retrieved from <URL: http://recognize-speech.com/speech> on Oct 18, 2015, 1 pages	
	37	Preprocessing, retrieved from <URL: http://recognize-speech.com/preprocessing> on Oct 18, 2015, 4 pages	
	38	Feature Extraction, retrieved from <URL: http://recognize-speech.com/feature-extraction> on Oct 18, 2015, 3 pages	

Tesla Exhibit 1002.208

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	39	Acoustic model, retrieved from <URL: http://recognize-speech.com/acoustic-model> on Oct 18, 2015, 2 pages	
	40	Video content analysis, retrieved from <URL: http://wikipedia.com> on Nov 19, 2015, 2 pages	
	41	Video tracking, retrieved from <URL: http://wikipedia.com> on Nov 1, 2015, 2 pages	
	42	Andrej Karpathy, Thomas Leung, George Toderici, Rahul Sukthankar, Sanketh Shetty, Li Fei-Fei, Large-scale Video Classification with Convolutional Neural Networks, Apr 14, 2014, 8 pages, Stanford University	
	43	Karen Simonyan, Andrew Zisserman, Two-Stream Convolutional Networks for Action Recognition in Videos, Nov 13, 2014, 11 pages, University of Oxford	

If you wish to add additional non-patent literature document citation information please click the Add button Add

EXAMINER SIGNATURE

Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1] See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. [2] Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). [3] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [4] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. [5] Applicant is to place a check mark here if English language translation is attached.

Tesla Exhibit 1002.209

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

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OR

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See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Jasmin Cosic/	Date (YYYY-MM-DD)	2018-01-07
Name/Print	Jasmin Cosic	Registration Number	

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Tesla Exhibit 1002.210

Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

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7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.

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9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

Tesla Exhibit 1002.211

Doc code: IDS
Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (01-10)
Approved for use through 07/31/2012. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

U.S.PATENTS Remove

Examiner Initial*	Cite No	Patent Number	Kind Code[1]	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	7113946		2006-09-26	Cosic	
	2	7117225		2006-10-03	Cosic	
	3	8335805		2012-12-18	Cosic	
	4	8417740		2013-04-09	Cosic	
	5	8572035		2013-10-29	Cosic	
	6	8655900		2014-02-18	Cosic	
	7	9047324		2015-06-02	Cosic	
	8	9282309		2016-03-08	Cosic	

Tesla Exhibit 1002.212

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	9	9298749		2016-03-29	Cosic	
	10	9367806		2016-06-14	Cosic	
	11	9443192		2016-09-13	Cosic	
	12	9582762		2017-02-28	Cosic	
	13	9595294		2017-03-14	Cosic	
	14	9697869		2017-07-04	Cosic	

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Examiner Initial*	Cite No	Publication Number	Kind Code[1]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	20030065662		2003-04-03	Cosic	
	2	20040194017		2004-09-30	Cosic	
	3	20050149517		2005-07-07	Cosic	

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	4	20050149542		2005-07-07	Cosic	
	5	20050289105		2005-12-29	Cosic	
	6	20100023541		2010-01-28	Cosic	
	7	20100082536		2010-04-01	Cosic	
	8	20130218932		2013-08-22	Cosic	
	9	20130226974		2013-08-29	Cosic	
	10	20160140999		2016-05-19	Cosic	
	11	20160142650		2016-05-19	Cosic	
	12	20160246819		2016-08-25	Cosic	
	13	20160246850		2016-08-25	Cosic	
	14	20160246868		2016-08-25	Cosic	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
	Application Number	15818765
	Filing Date	2017-11-21
	First Named Inventor	Jasmin Cosic
	Art Unit	
	Examiner Name	
	Attorney Docket Number	

	15	20160292185		2016-10-06	Cosic	

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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T[5]
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EXAMINER SIGNATURE

Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1] See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. [2] Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). [3] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [4] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. [5] Applicant is to place a check mark here if English language translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	15818765
Filing Date	2017-11-21
First Named Inventor	Jasmin Cosic
Art Unit	
Examiner Name	
Attorney Docket Number	

CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

OR

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Jasmin Cosic/	Date (YYYY-MM-DD)	2018-01-07
Name/Print	Jasmin Cosic	Registration Number	

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

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2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.

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6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).

7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.

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9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

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Electronic Acknowledgement Receipt

EFS ID:	31427476
Application Number:	15818765
International Application Number:	
Confirmation Number:	6002
Title of Invention:	DEVICES, SYSTEMS, AND METHODS FOR USE IN AUTOMATION
First Named Inventor/Applicant Name:	Jasmin Cosic
Customer Number:	116094
Filer:	Jasmin Cosic
Filer Authorized By:	
Attorney Docket Number:	
Receipt Date:	07-JAN-2018
Filing Date:	21-NOV-2017
Time Stamp:	20:41:49
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Form (SB08)	IDS_Part1.pdf	615771 c12b1811f233f924cf7e9c18ab0d48c18b131524	no	13

Warnings:

Information:					
2	Non Patent Literature	NPL_Part1.pdf	19783223 81a3858c2f8d004a3e30bbae7ec3bfb2087ac1b9	no	380
Warnings:					
The page size in the PDF is too large. The pages should be 8.5 x 11 or A4. If this PDF is submitted, the pages will be resized upon entry into the Image File Wrapper and may affect subsequent processing					
Information:					
3	Information Disclosure Statement (IDS) Form (SB08)	IDS_Part2.pdf	613081 c223ee957ac2210a8328efb0d81ade2b078ec8d7	no	5
Warnings:					
Information:					
A U.S. Patent Number Citation or a U.S. Publication Number Citation is required in the Information Disclosure Statement (IDS) form for autoloading of data into USPTO systems. You may remove the form to add the required data in order to correct the Informational Message if you are citing U.S. References. If you chose not to include U.S. References, the image of the form will be processed and be made available within the Image File Wrapper (IFW) system. However, no data will be extracted from this form. Any additional data such as Foreign Patent Documents or Non Patent Literature will be manually reviewed and keyed into USPTO systems.					
4	Non Patent Literature	NPL_Part2.pdf	5883714 fac28c7df69b33d27768405ac8c3f0bb9481d1e4	no	114
Warnings:					
The page size in the PDF is too large. The pages should be 8.5 x 11 or A4. If this PDF is submitted, the pages will be resized upon entry into the Image File Wrapper and may affect subsequent processing					
Information:					
5	Information Disclosure Statement (IDS) Form (SB08)	IDS_Part3.pdf	614631 a6f3181852b3429178b1f3f3f38586bedfa8e240	no	10
Warnings:					
Information:					
6	Non Patent Literature	NPL_Part3.pdf	16903333 327e784f4ab311324ac99b3c6dd8b06ae0b170c7	no	160
Warnings:					
Information:					
7	Information Disclosure Statement (IDS) Form (SB08)	IDS_Part4.pdf	612928 447659024eb4a2de93f96a0ab44520fcf2c3789f	no	6
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Total Files Size (in bytes):	45026681

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor:	Jasmin Cosic	Confirmation No.: 6002	
Title:	"DEVICES, SYSTEMS, AND METHODS FOR USE IN AUTOMATION"		
Serial No.:	15/818,765	Filed: November 21, 2017	
Examiner:		Group Art Unit:	

Via EFS-Web January 6, 2018

Mail Stop Amendment
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Commissioner:

In response to Notice to File Corrected Application Papers dated December 21, 2017, the applicant submits this amendment of the specification for the above-referenced application. Attached please find:

1. A clean version of a substitute specification, and
2. A marked-up version of a substitute specification showing changes made.

The substitute specification includes no new matter. The only correction made is to correct obvious typographical error in a letter related to Figs. 5 on page 51 of the specification.

The undersigned can be reached at (317) 772-1312 to discuss any aspect of this application.

I hereby certify that this correspondence is being submitted electronically via EFS Web to the United States Patent and Trademark Office.

By /Jasmin Cosic/
Jasmin Cosic

Date submitted: January 6, 2018

Respectfully submitted,

/Jasmin Cosic/

Jasmin Cosic

Tesla Exhibit 1002.222

Electronic Acknowledgement Receipt

EFS ID:	31426851
Application Number:	15818765
International Application Number:	
Confirmation Number:	6002
Title of Invention:	DEVICES, SYSTEMS, AND METHODS FOR USE IN AUTOMATION
First Named Inventor/Applicant Name:	Jasmin Cosic
Customer Number:	116094
Filer:	Jasmin Cosic
Filer Authorized By:	
Attorney Docket Number:	
Receipt Date:	06-JAN-2018
Filing Date:	21-NOV-2017
Time Stamp:	17:57:36
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Specification	SUBSTITUTE_SPECIFICATION_CLEAN.pdf	1826123 b4a871b9a00804f9f786b1c7def57818af07aecc	no	144

Warnings:

Information:					
2	Specification	SUBSTITUTE_SPECIFICATION_MARKEDUP.pdf	1845962 0111624a0cbda2e1cbafde2d6d92eead18840477	no	144
Warnings:					
Information:					
3	Applicant Response to Pre-Exam Formalities Notice	Answer.pdf	30067 aa2152621bd49db36820f4f39ce0b6cb2e2f5445	no	2
Warnings:					
Information:					
		Total Files Size (in bytes):	3702152		

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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

DEVICES, SYSTEMS, AND METHODS FOR USE IN AUTOMATION

FIELD

The disclosure generally relates to automated devices and/or systems.

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BACKGROUND

Devices or systems commonly operate by receiving a user's operating directions. Hence, devices or systems are limited to relying on the user to direct them.

SUMMARY OF THE INVENTION

In some aspects, the disclosure relates to a system. The system may be implemented at least in part on one or more computing devices. In some embodiments, the system comprises a processor circuit configured to execute instruction sets for operating a device. The system may further include a memory unit configured to store data. The system may further include a picture capturing apparatus configured to capture digital pictures. The system may further include an artificial intelligence unit. In some embodiments, the artificial intelligence unit may be configured to: receive a first digital picture from the picture capturing apparatus. The artificial intelligence unit may be further configured to: receive one or more instruction sets for operating the device from the processor circuit. The artificial intelligence unit may be further configured to: learn the first digital picture correlated with the one or more instruction sets for operating the device. The artificial intelligence unit may be further configured to: receive a new digital picture from the picture capturing apparatus. The artificial intelligence unit may be further configured to: anticipate the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture. The artificial intelligence unit may be further configured to: cause the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture, the executing performed in response to the anticipating of the artificial intelligence unit, wherein the device performs one or more operations defined by the one or more instruction sets for operating the device correlated with the first digital picture, the one or more operations performed in response to the executing by the processor circuit.

In certain embodiments, at least one of the processor circuit, the memory unit, the picture capturing apparatus, or the artificial intelligence unit are part of, operating on, or coupled to the device. In further embodiments, the device includes one or more devices. In further embodiments, the device includes a smartphone, a fixture, a control device, a computing enabled device, or a computer.

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In some embodiments, the processor circuit includes one or more processor circuits. In further embodiments, the processor circuit includes a logic circuit. The logic circuit may include a microcontroller. The one or more instruction sets may include one or more inputs into or one or more outputs from the logic circuit.

In certain embodiments, the processor circuit includes a logic circuit, the instruction sets for operating the device include inputs into the logic circuit, and executing instruction sets for operating the device includes performing logic operations on the inputs into the logic circuit and producing outputs for operating the device. The logic circuit may include a microcontroller. In further embodiments, the processor circuit includes a logic circuit, the instruction sets for operating the device include outputs from the logic circuit for operating the device, and executing instruction sets for operating the device includes performing logic operations on inputs into the logic circuit and producing the outputs from the logic circuit for operating the device.

In some embodiments, the memory unit includes one or more memory units. In further embodiments, the memory unit resides on a remote computing device, the remote computing device coupled to the processor circuit via a network. The remote computing device may include a server.

In some embodiments, the picture capturing apparatus includes one or more picture capturing apparatuses. In further embodiments, the picture capturing apparatus includes a motion picture camera or a still picture camera. In further embodiments, the picture capturing apparatus resides on a remote device, the remote device coupled to the processor circuit via a network.

In certain embodiments, the artificial intelligence unit is coupled to the picture capturing apparatus. In further embodiments, the artificial intelligence unit is coupled to the memory unit. In further embodiments, the artificial intelligence unit is part of, operating on, or coupled to the processor circuit. In further embodiments, the system further comprises: a second processor circuit, wherein the artificial intelligence unit is part of, operating on, or coupled to the second processor circuit. In further embodiments, the artificial intelligence unit is part of, operating on, or coupled to a remote computing device, the remote computing device coupled to the processor circuit via a network. In further embodiments, the artificial intelligence unit includes a circuit, a computing apparatus, or a computing system attachable to the processor circuit. In further embodiments, the artificial intelligence unit includes a circuit, a computing apparatus, or a computing system attachable to the device. In further embodiments, the artificial intelligence unit is attachable to an application for operating the device, the application running on the processor circuit. In further embodiments, the artificial intelligence unit includes a circuit, a computing apparatus, or a computing system built into the processor circuit. In further embodiments, the artificial intelligence unit includes a circuit, a computing apparatus, or a computing system built into the device. In further embodiments, the artificial intelligence unit is built into an application for operating the device, the application running on the processor circuit. In further embodiments, the artificial intelligence unit is provided as a feature of the processor circuit. In further embodiments, the artificial intelligence unit is provided as a feature of an application running on the processor circuit. In further embodiments, the artificial intelligence unit is provided as a feature of the device. In further embodiments, the artificial intelligence unit is further configured to: take control from, share control with, or release control to the processor circuit. In further embodiments, the artificial intelligence unit is further configured to: take control from, share control with, or release control to an application or an object of the application, the application running on the processor circuit. In further embodiments, the artificial intelligence unit is further configured to: take control from, share control with, or release control to a user or a system.

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In some embodiments, the first digital picture includes a stream of digital pictures. In further embodiments, the new digital picture includes a stream of digital pictures. In further embodiments, the first and the new digital pictures portray the device's surrounding. In further embodiments, the first and the new digital pictures portray a remote device's surrounding. In further embodiments, the first or the new digital picture includes a JPEG picture, a GIF picture, a TIFF picture, a PNG picture, a PDF picture, or a digitally encoded picture. The stream of digital pictures may include a MPEG motion picture, an AVI motion picture, a FLV motion picture, a MOV motion picture, a RM motion picture, a SWF motion picture, a WMV motion picture, a DivX motion picture, or a digitally encoded motion picture. In further embodiments, the first digital picture includes a comparative digital picture whose at least one portion can be used for comparisons with at least one portion of digital pictures subsequent to the first digital picture, the digital pictures subsequent to the first digital picture comprising the new digital picture. In further embodiments, the first digital picture includes a comparative digital picture that can be used for comparisons with the new digital picture. In further embodiments, the new digital picture includes an anticipatory digital picture whose correlated one or more instruction sets can be used for anticipation of one or more instruction sets to be executed by the processor circuit.

In certain embodiments, the one or more instruction sets for operating the device include one or more instruction sets that temporally correspond to the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed at a time of the capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed prior to the capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed within a threshold period of time prior to the capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed subsequent to the capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed within a threshold period of time subsequent to the capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed within a threshold period of time prior to the capturing the first digital picture or a threshold period of time subsequent to the capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed from a start of capturing a preceding digital picture to a start of capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed from a start of capturing the first digital picture to a start of capturing a subsequent digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed from a completion of capturing a preceding digital picture to a completion of capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture include one or more instruction sets executed from a completion of capturing the first digital picture to a completion of capturing a subsequent digital picture.

In some embodiments, the one or more instruction sets for operating the device are executed by the processor circuit. In further embodiments, the one or more instruction sets for operating the device are part of an application for operating the device, the application running on the processor circuit. In further embodiments, the one

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or more instruction sets for operating the device include one or more inputs into or one or more outputs from the processor circuit. In further embodiments, the one or more instruction sets for operating the device include values or states of one or more registers or elements of the processor circuit. In further embodiments, an instruction set includes at least one of: a command, a keyword, a symbol, an instruction, an operator, a variable, a value, an object, a data structure, a function, a parameter, a state, a signal, an input, an output, a character, a digit, or a reference thereto. In further embodiments, the one or more instruction sets include a source code, a bytecode, an intermediate code, a compiled code, an interpreted code, a translated code, a runtime code, an assembly code, a structured query language (SQL) code, or a machine code. In further embodiments, the one or more instruction sets include one or more code segments, lines of code, statements, instructions, functions, routines, subroutines, or basic blocks. In further embodiments, the processor circuit includes a logic circuit. The one or more instruction sets for operating the device include one or more inputs into a logic circuit. The one or more instruction sets for operating the device include one or more outputs from a logic circuit. In further embodiments, the one or more instruction sets for operating the device include one or more instruction sets for operating an application or an object of the application, the application running on the processor circuit.

In some embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes obtaining the one or more instruction sets from the processor circuit. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes receiving the one or more instruction sets as they are executed by the processor circuit. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes receiving the one or more instruction sets for operating the device from a register or an element of the processor circuit. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes receiving the one or more instruction sets for operating the device from an element that is part of, operating on, or coupled to the processor circuit. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes receiving the one or more instruction sets for operating the device from at least one of: the memory unit, the device, a virtual machine, a runtime engine, a hard drive, a storage device, a peripheral device, a network connected device, or a user. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes receiving the one or more instruction sets from a plurality of processor circuits, applications, memory units, devices, virtual machines, runtime engines, hard drives, storage devices, peripheral devices, network connected devices, or users.

In certain embodiments, the processor circuit includes a logic circuit, and wherein the receiving the one or more instruction sets for operating the device from the processor circuit includes receiving the one or more instruction sets for operating the device from the logic circuit. The logic circuit may include a microcontroller. The receiving the one or more instruction sets for operating the device from the logic circuit may include receiving the one or more instruction sets for operating the device from an element of the logic circuit. The receiving the one or more instruction sets for operating the device from the logic circuit may include receiving one or more inputs into the logic circuit. The receiving the one or more instruction sets for operating the device from the logic circuit may include receiving one or more outputs from the logic circuit.

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In some embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes receiving the one or more instruction sets for operating the device from an application for operating the device, the application running on the processor circuit. In further embodiments, the system further comprises: an application including instruction sets for operating the device, the application running on the processor circuit, wherein the receiving the one or more instruction sets for operating the device from the processor circuit includes receiving the one or more instruction sets for operating the device from the application.

In certain embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes receiving the one or more instruction sets at a source code write time, a compile time, an interpretation time, a translation time, a linking time, a loading time, or a runtime. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes at least one of: tracing, profiling, or instrumentation of a source code, a bytecode, an intermediate code, a compiled code, an interpreted code, a translated code, a runtime code, an assembly code, a structured query language (SQL) code, or a machine code. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes at least one of: tracing, profiling, or instrumentation of an element that is part of, operating on, or coupled to the processor circuit. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes at least one of: tracing, profiling, or instrumentation of a register of the processor circuit, the memory unit, a storage, or a repository where the one or more instruction sets for operating the device are stored. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes at least one of: tracing, profiling, or instrumentation of the processor circuit, the device, a virtual machine, a runtime engine, an operating system, an execution stack, a program counter, or a processing element. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes at least one of: tracing, profiling, or instrumentation of the processor circuit or tracing, profiling, or instrumentation of a component of the processor circuit. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes at least one of: tracing, profiling, or instrumentation of an application or an object of the application, the application running on the processor circuit. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes at least one of: tracing, profiling, or instrumentation at a source code write time, a compile time, an interpretation time, a translation time, a linking time, a loading time, or a runtime. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes at least one of: tracing, profiling, or instrumentation of one or more of code segments, lines of code, statements, instructions, functions, routines, subroutines, or basic blocks. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes at least one of: tracing, profiling, or instrumentation of a user input. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes at least one of: a manual, an automatic, a dynamic, or a just in time (JIT) tracing, profiling, or instrumentation. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes utilizing at least one of: a .NET tool, a .NET application programming interface (API), a Java tool, a Java API, a logging tool, or an independent tool for obtaining instruction sets. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes utilizing an assembly language. In further embodiments, the receiving the

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one or more instruction sets for operating the device from the processor circuit includes utilizing a branch or a jump. In further embodiments, the receiving the one or more instruction sets for operating the device from the processor circuit includes a branch tracing or a simulation tracing.

In some embodiments, the system further comprises: an interface configured to receive instruction sets, wherein the one or more instruction sets for operating the device are received by the interface. The interface may include an acquisition interface.

In certain embodiments, the first digital picture correlated with the one or more instruction sets for operating the device includes a unit of knowledge of how the device operated in a visual surrounding. In further embodiments, the first digital picture correlated with the one or more instruction sets for operating the device is included in a neuron, a node, a vertex, or an element of a data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. Some of the neurons, nodes, vertices, or elements may be interconnected.

In some embodiments, the first digital picture correlated with the one or more instruction sets for operating the device is structured into a knowledge cell. In further embodiments, the knowledge cell includes a unit of knowledge of how the device operated in a visual surrounding. In further embodiments, the knowledge cell is included in a neuron, a node, a vertex, or an element of a data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. Some of the neurons, nodes, vertices, or elements may be interconnected.

In certain embodiments, the learning the first digital picture correlated with the one or more instruction sets for operating the device includes correlating the first digital picture with the one or more instruction sets for operating the device. The correlating the first digital picture with the one or more instruction sets for operating the device may include generating a knowledge cell, the knowledge cell comprising the first digital picture correlated with the one or more instruction sets for operating the device. The correlating the first digital picture with the one or more instruction sets for operating the device may include structuring a unit of knowledge of how the device operated in a visual surrounding.

In some embodiments, the learning the first digital picture correlated with the one or more instruction sets for operating the device includes learning a user's knowledge, style, or methodology of operating the device in a visual surrounding. In further embodiments, the learning the first digital picture correlated with the one or more instruction sets for operating the device includes spontaneous learning the first digital picture correlated with the one or more instruction sets for operating the device.

In some embodiments, the learning the first digital picture correlated with the one or more instruction sets for operating the device includes storing, into the memory unit, the first digital picture correlated with the one or more instruction sets for operating the device, the first digital picture correlated with the one or more instruction sets for operating the device being part of a stored plurality of digital pictures correlated with one or more instruction sets for operating the device. In further embodiments, the plurality of digital pictures correlated with one or more instruction sets for operating the device include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, a knowledge structure, or a data structure. In further embodiments, the plurality of digital pictures correlated with one or more instruction sets for operating the device are organized into a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, a

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knowledge structure, or a data structure. In further embodiments, each of the plurality of digital pictures correlated with one or more instruction sets for operating the device is included in a neuron, a node, a vertex, or an element of a data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. Some of the neurons, nodes, vertices, or elements may be interconnected. In further embodiments, the plurality of digital pictures correlated with one or more instruction sets for operating the device include a user's knowledge, style, or methodology of operating the device in visual surroundings. In further embodiments, the plurality of digital pictures correlated with one or more instruction sets for operating the device are stored on a remote computing device. In further embodiments, the plurality of digital pictures correlated with one or more instruction sets for operating the device include an artificial intelligence system for knowledge structuring, storing, or representation. The artificial intelligence system for knowledge structuring, storing, or representation may include at least one of: a deep learning system, a supervised learning system, an unsupervised learning system, a neural network, a search-based system, an optimization-based system, a logic-based system, a fuzzy logic-based system, a tree-based system, a graph-based system, a hierarchical system, a symbolic system, a sub-symbolic system, an evolutionary system, a genetic system, a multi-agent system, a deterministic system, a probabilistic system, or a statistical system.

In certain embodiments, the anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture includes comparing at least one portion of the new digital picture with at least one portion of the first digital picture. The at least one portion of the new digital picture may include at least one region, at least one feature, or at least one pixel of the new digital picture. The at least one portion of the first digital picture may include at least one region, at least one feature, or at least one pixel of the first digital picture. The comparing the at least one portion of the new digital picture with the at least one portion of the first digital picture may include comparing at least one region of the new digital picture with at least one region of the first digital picture. The comparing the at least one portion of the new digital picture with the at least one portion of the first digital picture may include comparing at least one feature of the new digital picture with at least one feature of the first digital picture. The at least one portion of the new digital picture with the at least one portion of the first digital picture may include comparing at least one pixel of the new digital picture with at least one pixel of the first digital picture. The comparing the at least one portion of the new digital picture with the at least one portion of the first digital picture may include at least one of: performing a color adjustment, performing a size adjustment, performing a content manipulation, utilizing a transparency, or utilizing a mask on the new or the first digital picture. The comparing the at least one portion of the new digital picture with the at least one portion of the first digital picture may include recognizing at least one person or object in the new digital picture and at least one person or object in the first digital picture, and comparing the at least one person or object from the new digital picture with the at least one person or object from the first digital picture.

In some embodiments, he anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture includes determining that there is at least a partial match between the new digital picture and the first digital picture. In further embodiments, the determining that there is at least a partial match between the new digital picture and the first digital picture includes determining that there is at least a partial match between one or more portions of the new digital picture and one or more portions of the first digital picture. In further embodiments, the determining

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that there is at least a partial match between the new digital picture and the first digital picture includes determining that a similarity between at least one portion of the new digital picture and at least one portion of the first digital picture exceeds a similarity threshold. In further embodiments, the determining that there is at least a partial match between the new digital picture and the first digital picture includes determining a substantial similarity between at least one portion of the new digital picture and at least one portion of the first digital picture. The at least one portion of the new digital picture may include at least one region, at least one feature, or at least one pixel of the new digital picture. The at least one portion of the first digital picture may include at least one region, at least one feature, or at least one pixel of the first digital picture. The substantial similarity may be achieved when a similarity between the at least one portion of the new digital picture and the at least one portion of the first digital picture exceeds a similarity threshold. The substantial similarity may be achieved when a number or a percentage of matching or partially matching regions from the new digital picture and from the first digital picture exceeds a threshold number or threshold percentage. The substantial similarity may be achieved when a number or a percentage of matching or partially matching features from the new digital picture and from the first digital picture exceeds a threshold number or threshold percentage. The substantial similarity may be achieved when a number or a percentage of matching or partially matching pixels from the new digital picture and from the first digital picture exceeds a threshold number or threshold percentage. The substantial similarity may be achieved when one or more same or similar objects are recognized in the new digital picture and the first digital picture. In further embodiments, the determining that there is at least a partial match between the new digital picture and the first digital picture includes determining that a number or a percentage of matching regions from the new digital picture and from the first digital picture exceeds a threshold number or threshold percentage. The matching regions from the new digital picture and from the first digital picture may be determined factoring in at least one of: a location of a region, an importance of a region, a threshold for a similarity in a region, or a threshold for a difference in a region. In further embodiments, the determining that there is at least a partial match between the new digital picture and the first digital picture includes determining that a number or a percentage of matching features from the new digital picture and from the first digital picture exceeds a threshold number or threshold percentage. The matching features from the new digital picture and from the first digital picture may be determined factoring in at least one of: a type of a feature, an importance of a feature, a location of a feature, a threshold for a similarity in a feature, or a threshold for a difference in a feature. In further embodiments, the determining that there is at least a partial match between the new digital picture and the first digital picture includes determining that a number or a percentage of matching pixels from the new digital picture and from the first digital picture exceeds a threshold number or threshold percentage. The matching pixels from the new digital picture and from the first digital picture may be determined factoring in at least one of: a location of a pixel, a threshold for a similarity in a pixel, or a threshold for a difference in a pixel. In further embodiments, the determining that there is at least a partial match between the new digital picture and the first digital picture includes recognizing a same person or object in the new and the first digital pictures.

In some embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture instead of or prior to an instruction set that would have been executed next. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes

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modifying one or more instruction sets of the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes modifying a register or an element of the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes inserting the one or more instruction sets for operating the device correlated with the first digital picture into a register or an element of the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes redirecting the processor circuit to the one or more instruction sets for operating the device correlated with the first digital picture. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes redirecting the processor circuit to one or more alternate instruction sets, the alternate instruction sets comprising the one or more instruction sets for operating the device correlated with the first digital picture. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes transmitting, to the processor circuit for execution, the one or more instruction sets for operating the device correlated with the first digital picture. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes issuing an interrupt to the processor circuit and executing the one or more instruction sets for operating the device correlated with the first digital picture following the interrupt. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes modifying an element that is part of, operating on, or coupled to the processor circuit.

In certain embodiments, the processor circuit includes a logic circuit, and wherein the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes causing the logic circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture. The logic circuit may include a microcontroller. The causing the logic circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture may include modifying an element of the logic circuit. The causing the logic circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture may include inserting the one or more instruction sets for operating the device correlated with the first digital picture into an element of the logic circuit. The causing the logic circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture may include redirecting the logic circuit to the one or more instruction sets for operating the device correlated with the first digital picture. The causing the logic circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture may include replacing inputs into the logic circuit with the one or more instruction sets for operating the device correlated with the first digital picture. The causing the logic circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture may include replacing outputs from the logic circuit with the one or more instruction sets for operating the device correlated with the first digital picture.

In certain embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes causing an application for operating the device to execute the one or more instruction sets for operating the device correlated with the first digital picture, the application running on the processor circuit.

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In further embodiments, the system further comprises: an application including instruction sets for operating the device, the application running on the processor circuit, wherein the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes modifying the application.

In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes redirecting an application to the one or more instruction sets for operating the device correlated with the first digital picture, the application running on the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes redirecting an application to one or more alternate instruction sets, the application running on the processor circuit, the alternate instruction sets comprising the one or more instruction sets for operating the device correlated with the first digital picture. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes modifying one or more instruction sets of an application, the application running on the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes modifying a source code, a bytecode, an intermediate code, a compiled code, an interpreted code, a translated code, a runtime code, an assembly code, or a machine code. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes modifying at least one of: the memory unit, a register of the processor circuit, a storage, or a repository where instruction sets are stored or used. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes modifying one or more instruction sets for operating an application or an object of the application, the application running on the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes modifying at least one of: an element of the processor circuit, an element of the device, a virtual machine, a runtime engine, an operating system, an execution stack, a program counter, or a user input. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes modifying one or more instruction sets at a source code write time, a compile time, an interpretation time, a translation time, a linking time, a loading time, or a runtime. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes modifying one or more code segments, lines of code, statements, instructions, functions, routines, subroutines, or basic blocks. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes a manual, an automatic, a dynamic, or a just in time (JIT) instrumentation of an application, the application running on the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes utilizing one or more of a .NET tool, a .NET application programming interface (API), a Java tool, a Java API, an operating system tool, or an independent tool for modifying instruction sets. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes utilizing at least one of: a dynamic, an interpreted, or a scripting

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programming language. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes utilizing at least one of: a dynamic code, a dynamic class loading, or a reflection. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes utilizing an assembly language. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes utilizing at least one of: a metaprogramming, a self-modifying code, or an instruction set modification tool. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes utilizing at least one of: just in time (JIT) compiling, JIT interpretation, JIT translation, dynamic recompiling, or binary rewriting. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes utilizing at least one of: a dynamic expression creation, a dynamic expression execution, a dynamic function creation, or a dynamic function execution. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes adding or inserting additional code into a code of an application, the application running on the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes at least one of: modifying, removing, rewriting, or overwriting a code of an application, the application running on the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture includes at least one of: branching, redirecting, extending, or hot swapping a code of an application, the application running on the processor circuit. The branching or redirecting the code may include inserting at least one of: a branch, a jump, or a means for redirecting an execution. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes implementing a user's knowledge, style, or methodology of operating the device in a visual surrounding.

In some embodiments, the system further comprises: an interface configured to cause execution of instruction sets, wherein the executing the one or more instruction sets for operating the device correlated with the first digital picture is caused by the interface. The interface may include a modification interface.

In certain embodiments, the one or more operations defined by the one or more instruction sets for operating the device correlated with the first digital picture include at least one of: an operation with or by a smartphone, an operation with or by a fixture, an operation with or by a control device, or an operation with or by a computer or computing enabled device.

In some embodiments, the performing the one or more operations defined by the one or more instruction sets for operating the device correlated with the first digital picture includes implementing a user's knowledge, style, or methodology of operating the device in a visual surrounding.

In certain embodiments, the system further comprises: an application running on the processor circuit.

In some embodiments, the instruction sets for operating the device are part of an application for operating the device, the application running on the processor circuit.

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In certain embodiments, the system further comprises: an application for operating the device, the application running on the processor circuit. The application for operating the device may include the instruction sets for operating the device.

In some embodiments, the artificial intelligence unit is further configured to: receive at least one extra information. In further embodiments, the at least one extra information include one or more of: a time information, a location information, a computed information, an observed information, a sensory information, or a contextual information. In further embodiments, the at least one extra information include one or more of: an information on a digital picture, an information on an object in the digital picture, an information on the device's visual surrounding, an information on an instruction set, an information on an application, an information on an object of the application, an information on the processor circuit, an information on the device, or an information on an user. In further embodiments, the artificial intelligence unit is further configured to: learn the first digital picture correlated with the at least one extra information. The learning the first digital picture correlated with at least one extra information may include correlating the first digital picture with the at least one extra information. The learning the first digital picture correlated with at least one extra information may include storing the first digital picture correlated with the at least one extra information into the memory unit. In further embodiments, the anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture includes anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between an extra information correlated with the new digital picture and an extra information correlated with the first digital picture. The anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between an extra information correlated with the new digital picture and an extra information correlated with the first digital picture may include comparing an extra information correlated with the new digital picture and an extra information correlated with the first digital picture. The anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between an extra information correlated with the new digital picture and an extra information correlated with the first digital picture may include determining that a similarity between an extra information correlated with the new digital picture and an extra information correlated with the first digital picture exceeds a similarity threshold.

In some embodiments, the system further comprises: a user interface, wherein the artificial intelligence unit is further configured to: present, via the user interface, a user with an option to execute the one or more instruction sets for operating the device correlated with the first digital picture.

In certain embodiments, the system further comprises: a user interface, wherein the artificial intelligence unit is further configured to: receive, via the user interface, a user's selection to execute the one or more instruction sets for operating the device correlated with the first digital picture.

In some embodiments, the artificial intelligence unit is further configured to: rate the executed one or more instruction sets for operating the device correlated with the first digital picture. The rating the executed one or more instruction sets for operating the device correlated with the first digital picture may include displaying, on a display, the executed one or more instruction sets for operating the device correlated with the first digital picture along with one or more rating values as options to be selected by a user. The rating the executed one or more instruction sets for operating the device correlated with the first digital picture may include rating the executed one or more

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instruction sets for operating the device correlated with the first digital picture without a user input. The rating the executed one or more instruction sets for operating the device correlated with the first digital picture may include associating one or more rating values with the executed one or more instruction sets for operating the device correlated with the first digital picture and storing the one or more rating values into the memory unit.

In certain embodiments, the system further comprises: a user interface, wherein the artificial intelligence unit is further configured to: present, via the user interface, a user with an option to cancel the execution of the executed one or more instruction sets for operating the device correlated with the first digital picture. In further embodiments, the canceling the execution of the executed one or more instruction sets for operating the device correlated with the first digital picture includes restoring the processor circuit or the device to a prior state. The restoring the processor circuit or the device to a prior state may include saving the state of the processor circuit or the device prior to executing the one or more instruction sets for operating the device correlated with the first digital picture.

In some embodiments, the system further comprises: an input device configured to receive a user's operating directions, the user's operating directions for instructing the processor circuit on how to operate the device.

In certain embodiments, the autonomous device operating includes a partially or a fully autonomous device operating. The partially autonomous device operating may include executing the one or more instruction sets for operating the device correlated with the first digital picture responsive to a user confirmation. The fully autonomous device operating may include executing the one or more instruction sets for operating the device correlated with the first digital picture without a user confirmation.

In some embodiments, the artificial intelligence unit is further configured to: receive a second digital picture from the picture capturing apparatus; receive additional one or more instruction sets for operating the device from the processor circuit; and learn the second digital picture correlated with the additional one or more instruction sets for operating the device. In further embodiments, the second digital picture includes a second stream of digital pictures. In further embodiments, the learning the first digital picture correlated with the one or more instruction sets for operating the device and the learning the second digital picture correlated with the additional one or more instruction sets for operating the device include creating a connection between the first digital picture correlated with the one or more instruction sets for operating the device and the second digital picture correlated with the additional one or more instruction sets for operating the device. The connection may include or is associated with at least one of: an occurrence count, a weight, a parameter, or a data. In further embodiments, the learning the first digital picture correlated with the one or more instruction sets for operating the device and the learning the second digital picture correlated with the additional one or more instruction sets for operating the device include updating a connection between the first digital picture correlated with the one or more instruction sets for operating the device and the second digital picture correlated with the additional one or more instruction sets for operating the device. The updating the connection between the first digital picture correlated with the one or more instruction sets for operating the device and the second digital picture correlated with the additional one or more instruction sets for operating the device may include updating at least one of: an occurrence count, a weight, a parameter, or a data included in or associated with the connection. In further embodiments, the learning the first digital picture correlated with the one or more instruction sets for operating the device includes storing the first digital picture correlated with the one or more instruction sets for operating the device into a first node of a data structure, and wherein the learning the second

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digital picture correlated with the additional one or more instruction sets for operating the device includes storing the second digital picture correlated with the additional one or more instruction sets for operating the device into a second node of the data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. The learning the first digital picture correlated with the one or more instruction sets for operating the device and the learning the second digital picture correlated with the additional one or more instruction sets for operating the device may include creating a connection between the first node and the second node. The learning the first digital picture correlated with the one or more instruction sets for operating the device and the learning the second digital picture correlated with the additional one or more instruction sets for operating the device may include updating a connection between the first node and the second node. In further embodiments, the first digital picture correlated with the one or more instruction sets for operating the device is stored into a first node of a neural network and the second digital picture correlated with the additional one or more instruction sets for operating the device is stored into a second node of the neural network. The first node and the second node may be connected by a connection. The first node may be part of a first layer of the neural network and the second node may be part of a second layer of the neural network. In further embodiments, the first digital picture correlated with the one or more instruction sets for operating the device is stored into a first node of a graph and the second digital picture correlated with the additional one or more instruction sets for operating the device is stored into a second node of the graph. The first node and the second node may be connected by a connection. In further embodiments, the first digital picture correlated with the one or more instruction sets for operating the device is stored into a first node of a sequence and the second digital picture correlated with the additional one or more instruction sets for operating the device is stored into a second node of the sequence.

In some aspects, the disclosure relates to a non-transitory computer storage medium having a computer program stored thereon, the program including instructions that when executed by one or more processor circuits cause the one or more processor circuits to perform operations comprising: receiving a first digital picture from a picture capturing apparatus. The operations may further include receiving one or more instruction sets for operating a device. The operations may further include learning the first digital picture correlated with the one or more instruction sets for operating the device. The operations may further include receiving a new digital picture from the picture capturing apparatus. The operations may further include anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture. The operations may further include causing an execution of the one or more instruction sets for operating the device correlated with the first digital picture, the causing performed in response to the anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture, wherein the device performs one or more operations defined by the one or more instruction sets for operating the device correlated with the first digital picture, the one or more operations performed in response to the executing.

In some aspects, the disclosure relates to a method comprising: (a) receiving a first digital picture from a picture capturing apparatus by one or more processor circuits. The method may further include (b) receiving one or more instruction sets for operating a device by the one or more processor circuits. The method may further include (c) learning the first digital picture correlated with the one or more instruction sets for operating the device, the

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learning of (c) performed by the one or more processor circuits. The method may further include (d) receiving a new digital picture from the picture capturing apparatus by the one or more processor circuits. The method may further include (e) anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture, the anticipating of (e) performed by the one or more processor circuits. The method may further include (f) executing the one or more instruction sets for operating the device correlated with the first digital picture, the executing of (f) performed in response to the anticipating of (e). The method may further include (g) performing, by the device, one or more operations defined by the one or more instruction sets for operating the device correlated with the first digital picture, the one or more operations performed in response to the executing of (f).

The operations or steps of the non-transitory computer storage medium and/or the method may be performed by any of the elements of the above described systems as applicable. The non-transitory computer storage medium and/or the method may include any of the operations, steps, and embodiments of the above described systems as applicable as well as the following embodiments.

In certain embodiments, the device includes one or more devices. In further embodiments, the device includes a smartphone, a fixture, a control device, a computing enabled device, or a computer. In further embodiments, the picture capturing apparatus includes one or more picture capturing apparatuses. In further embodiments, the picture capturing apparatus includes a motion picture camera or a still picture camera. In further embodiments, the picture capturing apparatus resides on a remote device, the remote device coupled to the one or more processor circuits via a network.

In some embodiments, the one or more instruction sets for operating the device include one or more instruction sets that temporally correspond to the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed at a time of the capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed prior to the capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed within a threshold period of time prior to the capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed subsequent to the capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed within a threshold period of time subsequent to the capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed within a threshold period of time prior to the capturing the first digital picture or a threshold period of time subsequent to the capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed from a start of capturing a preceding digital picture to a start of capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed from a start of capturing the first digital picture to a start of capturing a subsequent digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets executed from a completion of capturing a preceding digital picture to a completion of capturing the first digital picture. The one or more instruction sets that temporally correspond to the first digital picture may include one or more instruction sets

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executed from a completion of capturing the first digital picture to a completion of capturing a subsequent digital picture.

In certain embodiments, the one or more instruction sets for operating the device are executed by a processor circuit. In further embodiments, the one or more instruction sets for operating the device are part of an application for operating the device. In further embodiments, the one or more instruction sets for operating the device include one or more inputs into or one or more outputs from a processor circuit. In further embodiments, the one or more instruction sets for operating the device include values or states of one or more registers or elements of a processor circuit. In further embodiments, an instruction set includes at least one of: a command, a keyword, a symbol, an instruction, an operator, a variable, a value, an object, a data structure, a function, a parameter, a state, a signal, an input, an output, a character, a digit, or a reference thereto. In further embodiments, the one or more instruction sets include a source code, a bytecode, an intermediate code, a compiled code, an interpreted code, a translated code, a runtime code, an assembly code, a structured query language (SQL) code, or a machine code. In further embodiments, the one or more instruction sets include one or more code segments, lines of code, statements, instructions, functions, routines, subroutines, or basic blocks. In further embodiments, the one or more instruction sets for operating the device include one or more inputs into a logic circuit. In further embodiments, the one or more instruction sets for operating the device include one or more outputs from a logic circuit. In further embodiments, the one or more instruction sets for operating the device include one or more instruction sets for operating an application or an object of the application.

In some embodiments, the receiving the one or more instruction sets for operating the device includes obtaining the one or more instruction sets. In further embodiments, the receiving the one or more instruction sets for operating the device includes receiving the one or more instruction sets as they are executed. In further embodiments, the receiving the one or more instruction sets for operating the device includes receiving the one or more instruction sets for operating the device from a register or an element of a processor circuit. In further embodiments, the receiving the one or more instruction sets for operating the device includes receiving the one or more instruction sets for operating the device from an element that is part of, operating on, or coupled to a processor circuit. In further embodiments, the receiving the one or more instruction sets for operating the device includes receiving the one or more instruction sets for operating the device from at least one of: a memory unit, the device, a virtual machine, a runtime engine, a hard drive, a storage device, a peripheral device, a network connected device, or a user. In further embodiments, the receiving the one or more instruction sets for operating the device includes receiving the one or more instruction sets from a plurality of processor circuits, applications, memory units, devices, virtual machines, runtime engines, hard drives, storage devices, peripheral devices, network connected devices, or users.

In certain embodiments, the receiving the one or more instruction sets for operating the device includes receiving the one or more instruction sets for operating the device from a logic circuit. The logic circuit may include a microcontroller. The receiving the one or more instruction sets for operating the device from the logic circuit may include receiving the one or more instruction sets for operating the device from an element of the logic circuit. The receiving the one or more instruction sets for operating the device from the logic circuit may include receiving one or more inputs into the logic circuit. The receiving the one or more instruction sets for operating the device from the logic circuit may include receiving one or more outputs from the logic circuit.

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In some embodiments, the receiving the one or more instruction sets for operating the device includes receiving the one or more instruction sets for operating the device from an application for operating the device. In further embodiments, the receiving the one or more instruction sets for operating the device includes receiving the one or more instruction sets for operating the device from an application, the application including instruction sets for operating the device. In further embodiments, the receiving the one or more instruction sets for operating the device includes receiving the one or more instruction sets at a source code write time, a compile time, an interpretation time, a translation time, a linking time, a loading time, or a runtime. In further embodiments, the receiving the one or more instruction sets for operating the device includes at least one of: tracing, profiling, or instrumentation of a source code, a bytecode, an intermediate code, a compiled code, an interpreted code, a translated code, a runtime code, an assembly code, a structured query language (SQL) code, or a machine code. In further embodiments, the receiving the one or more instruction sets for operating the device includes at least one of: tracing, profiling, or instrumentation of an element that is part of, operating on, or coupled to a processor circuit. In further embodiments, the receiving the one or more instruction sets for operating the device includes at least one of: tracing, profiling, or instrumentation of a register of a processor circuit, a memory unit, a storage, or a repository where the one or more instruction sets for operating the device are stored. In further embodiments, the receiving the one or more instruction sets for operating the device includes at least one of: tracing, profiling, or instrumentation of a processor circuit, the device, a virtual machine, a runtime engine, an operating system, an execution stack, a program counter, or a processing element. In further embodiments, the receiving the one or more instruction sets for operating the device includes at least one of: tracing, profiling, or instrumentation of a processor circuit or tracing, profiling, or instrumentation of a component of the processor circuit. In further embodiments, the receiving the one or more instruction sets for operating the device includes at least one of: tracing, profiling, or instrumentation of an application or an object of the application. In further embodiments, the receiving the one or more instruction sets for operating the device includes at least one of: tracing, profiling, or instrumentation at a source code write time, a compile time, an interpretation time, a translation time, a linking time, a loading time, or a runtime. In further embodiments, the receiving the one or more instruction sets for operating the device includes at least one of: tracing, profiling, or instrumentation of one or more of code segments, lines of code, statements, instructions, functions, routines, subroutines, or basic blocks. In further embodiments, the receiving the one or more instruction sets for operating the device includes at least one of: tracing, profiling, or instrumentation of a user input. In further embodiments, the receiving the one or more instruction sets for operating the device includes at least one of: a manual, an automatic, a dynamic, or a just in time (JIT) tracing, profiling, or instrumentation. In further embodiments, the receiving the one or more instruction sets for operating the device includes utilizing at least one of: a .NET tool, a .NET application programming interface (API), a Java tool, a Java API, a logging tool, or an independent tool for obtaining instruction sets. In further embodiments, the receiving the one or more instruction sets for operating the device includes utilizing an assembly language. In further embodiments, the receiving the one or more instruction sets for operating the device includes utilizing a branch or a jump. In further embodiments, the receiving the one or more instruction sets for operating the device includes a branch tracing or a simulation tracing. In further embodiments, the receiving the one or more instruction sets for operating the device includes receiving the one or more instruction sets for operating the device by an interface. The interface may include an acquisition interface.

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In certain embodiments, the first digital picture correlated with the one or more instruction sets for operating the device includes a unit of knowledge of how the device operated in a visual surrounding. In further embodiments, the first digital picture correlated with the one or more instruction sets for operating the device is included in a neuron, a node, a vertex, or an element of a data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. Some of the neurons, nodes, vertices, or elements may be interconnected. In further embodiments, the first digital picture correlated with the one or more instruction sets for operating the device is structured into a knowledge cell. In further embodiments, the knowledge cell includes a unit of knowledge of how the device operated in a visual surrounding. In further embodiments, the knowledge cell is included in a neuron, a node, a vertex, or an element of a data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. Some of the neurons, nodes, vertices, or elements may be interconnected.

In certain embodiments, the learning the first digital picture correlated with the one or more instruction sets for operating the device includes correlating the first digital picture with the one or more instruction sets for operating the device. The correlating the first digital picture with the one or more instruction sets for operating the device may include generating a knowledge cell, the knowledge cell comprising the first digital picture correlated with the one or more instruction sets for operating the device. The correlating the first digital picture with the one or more instruction sets for operating the device may include structuring a unit of knowledge of how the device operated in a visual surrounding. In further embodiments, the learning the first digital picture correlated with the one or more instruction sets for operating the device includes learning a user's knowledge, style, or methodology of operating the device in a visual surrounding. In further embodiments, the learning the first digital picture correlated with the one or more instruction sets for operating the device includes spontaneous learning the first digital picture correlated with the one or more instruction sets for operating the device.

In some embodiments, the learning the first digital picture correlated with the one or more instruction sets for operating the device includes storing, into a memory unit, the first digital picture correlated with the one or more instruction sets for operating the device, the first digital picture correlated with the one or more instruction sets for operating the device being part of a stored plurality of digital pictures correlated with one or more instruction sets for operating the device. In further embodiments, the memory unit includes one or more memory units. In further embodiments, the memory unit resides on a remote computing device, the remote computing device coupled to the one or more processor circuits via a network. The remote computing device may include a server. In further embodiments, the plurality of digital pictures correlated with one or more instruction sets for operating the device include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, a knowledge structure, or a data structure. In further embodiments, the plurality of digital pictures correlated with one or more instruction sets for operating the device are organized into a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, a knowledge structure, or a data structure. In further embodiments, each of the plurality of digital pictures correlated with one or more instruction sets for operating the device is included in a neuron, a node, a vertex, or an element of a data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. Some of the neurons, nodes, vertices, or elements may be

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interconnected. In further embodiments, the plurality of digital pictures correlated with one or more instruction sets for operating the device include a user's knowledge, style, or methodology of operating the device in visual surroundings. In further embodiments, the plurality of digital pictures correlated with one or more instruction sets for operating the device are stored on a remote computing device. In further embodiments, the plurality of digital pictures correlated with one or more instruction sets for operating the device include an artificial intelligence system for knowledge structuring, storing, or representation. The artificial intelligence system for knowledge structuring, storing, or representation may include at least one of: a deep learning system, a supervised learning system, an unsupervised learning system, a neural network, a search-based system, an optimization-based system, a logic-based system, a fuzzy logic-based system, a tree-based system, a graph-based system, a hierarchical system, a symbolic system, a sub-symbolic system, an evolutionary system, a genetic system, a multi-agent system, a deterministic system, a probabilistic system, or a statistical system.

In some embodiments, the anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture includes comparing at least one portion of the new digital picture with at least one portion of the first digital picture. The at least one portion of the new digital picture may include at least one region, at least one feature, or at least one pixel of the new digital picture. The at least one portion of the first digital picture may include at least one region, at least one feature, or at least one pixel of the first digital picture. The comparing the at least one portion of the new digital picture with the at least one portion of the first digital picture may include comparing at least one region of the new digital picture with at least one region of the first digital picture. The comparing the at least one portion of the new digital picture with the at least one portion of the first digital picture may include comparing at least one feature of the new digital picture with at least one feature of the first digital picture. The comparing the at least one portion of the new digital picture with the at least one portion of the first digital picture may include comparing at least one pixel of the new digital picture with at least one pixel of the first digital picture. The comparing the at least one portion of the new digital picture with the at least one portion of the first digital picture may include at least one of: performing a color adjustment, performing a size adjustment, performing a content manipulation, utilizing a transparency, or utilizing a mask on the new or the first digital picture. The comparing the at least one portion of the new digital picture with the at least one portion of the first digital picture may include recognizing at least one person or object in the new digital picture and at least one person or object in the first digital picture, and comparing the at least one person or object from the new digital picture with the at least one person or object from the first digital picture.

In certain embodiments, the anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture includes determining that there is at least a partial match between the new digital picture and the first digital picture. In further embodiments, the determining that there is at least a partial match between the new digital picture and the first digital picture includes determining that there is at least a partial match between one or more portions of the new digital picture and one or more portions of the first digital picture. In further embodiments, the determining that there is at least a partial match between the new digital picture and the first digital picture includes determining that a similarity between at least one portion of the new digital picture and at least one portion of the first digital picture exceeds a similarity threshold. In further embodiments, the determining that there is at least a partial match

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between the new digital picture and the first digital picture includes determining a substantial similarity between at least one portion of the new digital picture and at least one portion of the first digital picture. The at least one portion of the new digital picture may include at least one region, at least one feature, or at least one pixel of the new digital picture. The at least one portion of the first digital picture may include at least one region, at least one feature, or at least one pixel of the first digital picture. The substantial similarity may be achieved when a similarity between the at least one portion of the new digital picture and the at least one portion of the first digital picture exceeds a similarity threshold. The substantial similarity may be achieved when a number or a percentage of matching or partially matching regions from the new digital picture and from the first digital picture exceeds a threshold number or threshold percentage. The substantial similarity may be achieved when a number or a percentage of matching or partially matching features from the new digital picture and from the first digital picture exceeds a threshold number or threshold percentage. The substantial similarity may be achieved when a number or a percentage of matching or partially matching pixels from the new digital picture and from the first digital picture exceeds a threshold number or threshold percentage. The substantial similarity may be achieved when one or more same or similar objects are recognized in the new digital picture and the first digital picture. In further embodiments, the determining that there is at least a partial match between the new digital picture and the first digital picture includes determining that a number or a percentage of matching regions from the new digital picture and from the first digital picture exceeds a threshold number or threshold percentage. The matching regions from the new digital picture and from the first digital picture may be determined factoring in at least one of: a location of a region, an importance of a region, a threshold for a similarity in a region, or a threshold for a difference in a region. In further embodiments, the determining that there is at least a partial match between the new digital picture and the first digital picture includes determining that a number or a percentage of matching features from the new digital picture and from the first digital picture exceeds a threshold number or threshold percentage. The matching features from the new digital picture and from the first digital picture may be determined factoring in at least one of: a type of a feature, an importance of a feature, a location of a feature, a threshold for a similarity in a feature, or a threshold for a difference in a feature. In further embodiments, the determining that there is at least a partial match between the new digital picture and the first digital picture includes determining that a number or a percentage of matching pixels from the new digital picture and from the first digital picture exceeds a threshold number or threshold percentage. The matching pixels from the new digital picture and from the first digital picture may be determined factoring in at least one of: a location of a pixel, a threshold for a similarity in a pixel, or a threshold for a difference in a pixel. In further embodiments, the determining that there is at least a partial match between the new digital picture and the first digital picture includes recognizing a same person or object in the new and the first digital pictures.

In some embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes executing the one or more instruction sets for operating the device correlated with the first digital picture instead of or prior to an instruction set that would have been executed next. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes modifying one or more instruction sets. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes modifying a register or an element of a processor circuit. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes inserting the one or more instruction sets for operating the

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device correlated with the first digital picture into a register or an element of a processor circuit. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes redirecting a processor circuit to the one or more instruction sets for operating the device correlated with the first digital picture. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes redirecting a processor circuit to one or more alternate instruction sets, the alternate instruction sets comprising the one or more instruction sets for operating the device correlated with the first digital picture. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes transmitting, to a processor circuit for execution, the one or more instruction sets for operating the device correlated with the first digital picture. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes issuing an interrupt to a processor circuit and executing the one or more instruction sets for operating the device correlated with the first digital picture following the interrupt. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes modifying an element that is part of, operating on, or coupled to a processor circuit.

In certain embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes executing, by a logic circuit, the one or more instruction sets for operating the device correlated with the first digital picture. The logic circuit may include a microcontroller. The executing, by the logic circuit, the one or more instruction sets for operating the device correlated with the first digital picture may include modifying an element of the logic circuit. The executing, by the logic circuit, the one or more instruction sets for operating the device correlated with the first digital picture may include inserting the one or more instruction sets for operating the device correlated with the first digital picture into an element of the logic circuit. The executing, by the logic circuit, the one or more instruction sets for operating the device correlated with the first digital picture may include redirecting the logic circuit to the one or more instruction sets for operating the device correlated with the first digital picture. The executing, by the logic circuit, the one or more instruction sets for operating the device correlated with the first digital picture may include replacing inputs into the logic circuit with the one or more instruction sets for operating the device correlated with the first digital picture. The executing, by the logic circuit, the one or more instruction sets for operating the device correlated with the first digital picture may include replacing outputs from the logic circuit with the one or more instruction sets for operating the device correlated with the first digital picture.

In some embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes executing, by an application for operating the device, the one or more instruction sets for operating the device correlated with the first digital picture. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes modifying an application, the application including instruction sets for operating the device. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes redirecting an application to the one or more instruction sets for operating the device correlated with the first digital picture. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes redirecting an application to one or more alternate instruction sets, the alternate instruction sets comprising the one or more instruction sets for operating the device correlated with the first digital picture. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first

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digital picture includes modifying one or more instruction sets of an application. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes modifying a source code, a bytecode, an intermediate code, a compiled code, an interpreted code, a translated code, a runtime code, an assembly code, or a machine code. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes modifying at least one of: a memory unit, a register of a processor circuit, a storage, or a repository where instruction sets are stored or used. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes modifying one or more instruction sets for operating an application or an object of the application. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes modifying at least one of: an element of a processor circuit, an element of the device, a virtual machine, a runtime engine, an operating system, an execution stack, a program counter, or a user input. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes modifying one or more instruction sets at a source code write time, a compile time, an interpretation time, a translation time, a linking time, a loading time, or a runtime. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes modifying one or more code segments, lines of code, statements, instructions, functions, routines, subroutines, or basic blocks. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes a manual, an automatic, a dynamic, or a just in time (JIT) instrumentation of an application. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes utilizing one or more of a .NET tool, a .NET application programming interface (API), a Java tool, a Java API, an operating system tool, or an independent tool for modifying instruction sets. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes utilizing at least one of: a dynamic, an interpreted, or a scripting programming language. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes utilizing at least one of: a dynamic code, a dynamic class loading, or a reflection. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes utilizing an assembly language. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes utilizing at least one of: a metaprogramming, a self-modifying code, or an instruction set modification tool. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes utilizing at least one of: just in time (JIT) compiling, JIT interpretation, JIT translation, dynamic recompiling, or binary rewriting. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes utilizing at least one of: a dynamic expression creation, a dynamic expression execution, a dynamic function creation, or a dynamic function execution. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes adding or inserting additional code into a code of an application. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes at least one of: modifying, removing, rewriting, or overwriting a code of an application. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes at

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least one of: branching, redirecting, extending, or hot swapping a code of an application. The branching or redirecting the code may include inserting at least one of: a branch, a jump, or a means for redirecting an execution. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes implementing a user's knowledge, style, or methodology of operating the device in a visual surrounding. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first digital picture includes executing the one or more instruction sets for operating the device correlated with the first digital picture via an interface. The interface may include a modification interface.

In certain embodiments, the one or more operations defined by the one or more instruction sets for operating the device correlated with the first digital picture include at least one of: an operation with or by a smartphone, an operation with or by a fixture, an operation with or by a control device, or an operation with or by a computer or computing enabled device. In further embodiments, the performing the one or more operations defined by the one or more instruction sets for operating the device correlated with the first digital picture includes implementing a user's knowledge, style, or methodology of operating the device in a visual surrounding.

In some embodiments, the instruction sets for operating the device are part of an application for operating the device.

In certain embodiments, the operations of the non-transitory computer storage medium and/or the method further comprise: receiving at least one extra information. In further embodiments, the at least one extra information include one or more of: a time information, a location information, a computed information, an observed information, a sensory information, or a contextual information. In further embodiments, the at least one extra information include one or more of: an information on a digital picture, an information on an object in the digital picture, an information on the device's visual surrounding, an information on an instruction set, an information on an application, an information on an object of the application, an information on a processor circuit, an information on the device, or an information on an user. In further embodiments, the operations of the non-transitory computer storage medium and/or the method further comprise: learning the first digital picture correlated with the at least one extra information. The learning the first digital picture correlated with at least one extra information may include correlating the first digital picture with the at least one extra information. The learning the first digital picture correlated with at least one extra information may include storing the first digital picture correlated with the at least one extra information into a memory unit. In further embodiments, the anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture includes anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between an extra information correlated with the new digital picture and an extra information correlated with the first digital picture. The anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between an extra information correlated with the new digital picture and an extra information correlated with the first digital picture may include comparing an extra information correlated with the new digital picture and an extra information correlated with the first digital picture. The anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between an extra information correlated with the new digital picture and an extra information correlated with the first digital picture may include determining that a similarity

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between an extra information correlated with the new digital picture and an extra information correlated with the first digital picture exceeds a similarity threshold.

In some embodiments, the operations of the non-transitory computer storage medium and/or the method further comprise: presenting, via a user interface, a user with an option to execute the one or more instruction sets for operating the device correlated with the first digital picture.

In certain embodiments, the operations of the non-transitory computer storage medium and/or the method further comprise: receiving, via a user interface, a user's selection to execute the one or more instruction sets for operating the device correlated with the first digital picture.

In some embodiments, the operations of the non-transitory computer storage medium and/or the method further comprise: rating the executed one or more instruction sets for operating the device correlated with the first digital picture. The rating the executed one or more instruction sets for operating the device correlated with the first digital picture may include displaying, on a display, the executed one or more instruction sets for operating the device correlated with the first digital picture along with one or more rating values as options to be selected by a user. The rating the executed one or more instruction sets for operating the device correlated with the first digital picture may include rating the executed one or more instruction sets for operating the device correlated with the first digital picture without a user input. The rating the executed one or more instruction sets for operating the device correlated with the first digital picture may include associating one or more rating values with the executed one or more instruction sets for operating the device correlated with the first digital picture and storing the one or more rating values into a memory unit.

In certain embodiments, the operations of the non-transitory computer storage medium and/or the method further comprise: presenting, via a user interface, a user with an option to cancel the execution of the executed one or more instruction sets for operating the device correlated with the first digital picture. In further embodiments, the canceling the execution of the executed one or more instruction sets for operating the device correlated with the first digital picture includes restoring a processor circuit or the device to a prior state. The restoring the processor circuit or the device to a prior state may include saving the state of the processor circuit or the device prior to executing the one or more instruction sets for operating the device correlated with the first digital picture.

In some embodiments, the operations of the non-transitory computer storage medium and/or the method further comprise: receiving, via an input device, a user's operating directions, the user's operating directions for instructing a processor circuit on how to operate the device.

In certain embodiments, the operations of the non-transitory computer storage medium and/or the method further comprise: receiving a second digital picture from the picture capturing apparatus; receiving additional one or more instruction sets for operating the device; and learning the second digital picture correlated with the additional one or more instruction sets for operating the device. In further embodiments, the second digital picture includes a second stream of digital pictures. In further embodiments, the learning the first digital picture correlated with the one or more instruction sets for operating the device and the learning the second digital picture correlated with the additional one or more instruction sets for operating the device include creating a connection between the first digital picture correlated with the one or more instruction sets for operating the device and the second digital picture correlated with the additional one or more instruction sets for operating the device. The connection may include or is associated with at least one of: an occurrence count, a weight, a parameter, or a data. In further embodiments, the

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learning the first digital picture correlated with the one or more instruction sets for operating the device and the learning the second digital picture correlated with the additional one or more instruction sets for operating the device include updating a connection between the first digital picture correlated with the one or more instruction sets for operating the device and the second digital picture correlated with the additional one or more instruction sets for operating the device. The updating the connection between the first digital picture correlated with the one or more instruction sets for operating the device and the second digital picture correlated with the additional one or more instruction sets for operating the device may include updating at least one of: an occurrence count, a weight, a parameter, or a data included in or associated with the connection. In further embodiments, the learning the first digital picture correlated with the one or more instruction sets for operating the device includes storing the first digital picture correlated with the one or more instruction sets for operating the device into a first node of a data structure, and wherein the learning the second digital picture correlated with the additional one or more instruction sets for operating the device may include storing the second digital picture correlated with the additional one or more instruction sets for operating the device into a second node of the data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. The learning the first digital picture correlated with the one or more instruction sets for operating the device and the learning the second digital picture correlated with the additional one or more instruction sets for operating the device may include creating a connection between the first node and the second node. The learning the first digital picture correlated with the one or more instruction sets for operating the device and the learning the second digital picture correlated with the additional one or more instruction sets for operating the device may include updating a connection between the first node and the second node. In further embodiments, the first digital picture correlated with the one or more instruction sets for operating the device is stored into a first node of a neural network and the second digital picture correlated with the additional one or more instruction sets for operating the device is stored into a second node of the neural network. The first node and the second node may be connected by a connection. The first node may be part of a first layer of the neural network and the second node may be part of a second layer of the neural network. In further embodiments, the first digital picture correlated with the one or more instruction sets for operating the device is stored into a first node of a graph and the second digital picture correlated with the additional one or more instruction sets for operating the device is stored into a second node of the graph. The first node and the second node may be connected by a connection. In further embodiments, the first digital picture correlated with the one or more instruction sets for operating the device is stored into a first node of a sequence and the second digital picture correlated with the additional one or more instruction sets for operating the device is stored into a second node of the sequence.

In some aspects, the disclosure relates to a system for learning a visual surrounding for autonomous device operating. The system may be implemented at least in part on one or more computing devices. In some embodiments, the system comprises a processor circuit configured to execute instruction sets for operating a device. The system may further include a memory unit configured to store data. The system may further include a picture capturing apparatus configured to capture digital pictures. The system may further include an artificial intelligence unit. In some embodiments, the artificial intelligence unit may be configured to: receive a first digital picture from the picture capturing apparatus. The artificial intelligence unit may be further configured to: receive one or more instruction sets for operating the device from the processor circuit. The artificial intelligence unit may be

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further configured to: learn the first digital picture correlated with the one or more instruction sets for operating the device.

In some aspects, the disclosure relates to a non-transitory computer storage medium having a computer program stored thereon, the program including instructions that when executed by one or more processor circuits cause the one or more processor circuits to perform operations comprising: receiving a first digital picture from a picture capturing apparatus. The operations may further include: receiving one or more instruction sets for operating a device. The operations may further include: learning the first digital picture correlated with the one or more instruction sets for operating the device.

In some aspects, the disclosure relates to a method comprising: (a) receiving a first digital picture from a picture capturing apparatus by one or more processor circuits. The method may further include: (b) receiving one or more instruction sets for operating a device by the one or more processor circuits. The method may further include: (c) learning the first digital picture correlated with the one or more instruction sets for operating the device, the learning of (c) performed by the one or more processor circuits.

The operations or steps of the non-transitory computer storage medium and/or the method may be performed by any of the elements of the above described systems as applicable. The non-transitory computer storage medium and/or the method may include any of the operations, steps, and embodiments of the above described systems as applicable.

In some aspects, the disclosure relates to a system for using a visual surrounding for autonomous device operating. The system may be implemented at least in part on one or more computing devices. In some embodiments, the system comprises a processor circuit configured to execute instruction sets for operating a device. The system may further include a memory unit configured to store data. The system may further include a picture capturing apparatus configured to capture digital pictures. The system may further include an artificial intelligence unit. In some embodiments, the artificial intelligence unit may be configured to: access the memory unit that stores a plurality of digital pictures correlated with one or more instruction sets for operating the device, the plurality including a first digital picture correlated with one or more instruction sets for operating the device. The artificial intelligence unit may be further configured to: receive a new digital picture from the picture capturing apparatus. The artificial intelligence unit may be further configured to: anticipate the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture. The artificial intelligence unit may be further configured to: cause the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture, the executing performed in response to the anticipating of the artificial intelligence unit, wherein the device performs one or more operations defined by the one or more instruction sets for operating the device correlated with the first digital picture, the one or more operations performed in response to the executing by the processor circuit.

In some aspects, the disclosure relates to a non-transitory computer storage medium having a computer program stored thereon, the program including instructions that when executed by one or more processor circuits cause the one or more processor circuits to perform operations comprising: accessing a memory unit that stores a plurality of digital pictures correlated with one or more instruction sets for operating a device, the plurality including a first digital picture correlated with one or more instruction sets for operating the device. The operations may further include: receiving a new digital picture from a picture capturing apparatus. The operations may further include:

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anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture. The operations may further include: causing an execution of the one or more instruction sets for operating the device correlated with the first digital picture, the causing performed in response to the anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture, wherein the device performs one or more operations defined by the one or more instruction sets for operating the device correlated with the first digital picture, the one or more operations performed in response to the executing.

In some aspects, the disclosure relates to a method comprising: (a) accessing a memory unit that stores a plurality of digital pictures correlated with one or more instruction sets for operating a device, the plurality including a first digital picture correlated with one or more instruction sets for operating the device, the accessing of (a) performed by the one or more processor circuits. The method may further include: (b) receiving a new digital picture from a picture capturing apparatus by the one or more processor circuits. The method may further include: (c) anticipating the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture, the anticipating of (c) performed by the one or more processor circuits. The method may further include: (d) executing the one or more instruction sets for operating the device correlated with the first digital picture, the executing of (d) performed in response to the anticipating of (c). The method may further include: (e) performing, by the device, one or more operations defined by the one or more instruction sets for operating the device correlated with the first digital picture, the one or more operations performed in response to the executing of (d).

The operations or steps of the non-transitory computer storage medium and/or the method may be performed by any of the elements of the above described systems as applicable. The non-transitory computer storage medium and/or the method may include any of the operations, steps, and embodiments of the above described systems as applicable.

In some aspects, the disclosure relates to a system for learning and using a visual surrounding for autonomous device operating. The system may be implemented at least in part on one or more computing devices. In some embodiments, the system comprises a processor circuit configured to execute instruction sets for operating a device. The system may further include a memory unit configured to store data. The system may further include a picture capturing apparatus configured to capture digital pictures. The system may further include an artificial intelligence. In some embodiments, the artificial intelligence unit may be configured to: receive a first stream of digital pictures from the picture capturing apparatus. The artificial intelligence unit may be further configured to: receive one or more instruction sets for operating the device from the processor circuit. The artificial intelligence unit may be further configured to: learn the first stream of digital pictures correlated with the one or more instruction sets for operating the device. The artificial intelligence unit may be further configured to: receive a new stream of digital pictures from the picture capturing apparatus. The artificial intelligence unit may be further configured to: anticipate the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between the new stream of digital pictures and the first stream of digital pictures. The artificial intelligence unit may be further configured to: cause the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures, the executing performed in

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response to the anticipating of the artificial intelligence unit, wherein the device performs one or more operations defined by the one or more instruction sets for operating the device correlated with the first stream of digital pictures, the one or more operations performed in response to the executing by the processor circuit.

In certain embodiments, the first stream of digital pictures includes one or more digital pictures. In further embodiments, the new stream of digital pictures includes one or more digital pictures. In further embodiments, the first and the new streams of digital pictures portray the device's surrounding. In further embodiments, the first and the new streams of digital pictures portray a remote device's surrounding. In further embodiments, the first or the new stream of digital pictures includes a digital motion picture. The digital motion picture may include a MPEG motion picture, an AVI motion picture, a FLV motion picture, a MOV motion picture, a RM motion picture, a SWF motion picture, a WMV motion picture, a DivX motion picture, or a digitally encoded motion picture. In further embodiments, the first stream of digital pictures includes a comparative stream of digital pictures whose at least one portion can be used for comparisons with at least one portion of streams of digital pictures subsequent to the first stream of digital pictures, the streams of digital pictures subsequent to the first stream of digital pictures comprising the new stream of digital pictures. In further embodiments, the first stream of digital pictures includes a comparative stream of digital pictures that can be used for comparisons with the new stream of digital pictures. In further embodiments, the new stream of digital pictures includes an anticipatory stream of digital pictures whose correlated one or more instruction sets can be used for anticipation of one or more instruction sets to be executed by the processor circuit.

In some embodiments, the first stream of digital pictures correlated with the one or more instruction sets for operating the device includes a unit of knowledge of how the device operated in a visual surrounding. In further embodiments, the first stream of digital pictures correlated with the one or more instruction sets for operating the device is included in a neuron, a node, a vertex, or an element of a data structure. In further embodiments, the data structure includes a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. Some of the neurons, nodes, vertices, or elements may be interconnected. In further embodiments, the first stream of digital pictures correlated with the one or more instruction sets for operating the device is structured into a knowledge cell. In further embodiments, the knowledge cell includes a unit of knowledge of how the device operated in a visual surrounding. In further embodiments, the knowledge cell is included in a neuron, a node, a vertex, or an element of a data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. Some of the neurons, nodes, vertices, or elements may be interconnected.

In certain embodiments, the learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device includes correlating the first stream of digital pictures with the one or more instruction sets for operating the device. The correlating the first stream of digital pictures with the one or more instruction sets for operating the device may include generating a knowledge cell, the knowledge cell comprising the first stream of digital pictures correlated with the one or more instruction sets for operating the device. The correlating the first stream of digital pictures with the one or more instruction sets for operating the device may include structuring a unit of knowledge of how the device operated in a visual surrounding. In further embodiments, the learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device includes learning a user's knowledge, style, or methodology of operating the device in a visual surrounding.

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In further embodiments, the learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device includes spontaneous learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device.

In some embodiments, the learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device includes storing, into the memory unit, the first stream of digital pictures correlated with the one or more instruction sets for operating the device, the first stream of digital pictures correlated with the one or more instruction sets for operating the device being part of a stored plurality of streams of digital pictures correlated with one or more instruction sets for operating the device. In further embodiments, the plurality of streams of digital pictures correlated with one or more instruction sets for operating the device include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, a knowledge structure, or a data structure. In further embodiments, the plurality of streams of digital pictures correlated with one or more instruction sets for operating the device are organized into a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, a knowledge structure, or a data structure. In further embodiments, each of the plurality of streams of digital pictures correlated with one or more instruction sets for operating the device is included in a neuron, a node, a vertex, or an element of a data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. Some of the neurons, nodes, vertices, or elements may be interconnected. In further embodiments, the plurality of streams of digital pictures correlated with one or more instruction sets for operating the device include a user's knowledge, style, or methodology of operating the device in visual surroundings. In further embodiments, the plurality of streams of digital pictures correlated with one or more instruction sets for operating the device are stored on a remote computing device. In further embodiments, the plurality of streams of digital pictures correlated with one or more instruction sets for operating the device include an artificial intelligence system for knowledge structuring, storing, or representation. The artificial intelligence system for knowledge structuring, storing, or representation may include at least one of: a deep learning system, a supervised learning system, an unsupervised learning system, a neural network, a search-based system, an optimization-based system, a logic-based system, a fuzzy logic-based system, a tree-based system, a graph-based system, a hierarchical system, a symbolic system, a sub-symbolic system, an evolutionary system, a genetic system, a multi-agent system, a deterministic system, a probabilistic system, or a statistical system.

In some embodiments, the anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes comparing at least one portion of the new stream of digital pictures with at least one portion of the first stream of digital pictures. The at least one portion of the new stream of digital pictures may include at least one digital picture, at least one region, at least one feature, or at least one pixel of the new stream of digital pictures. The at least one portion of the first stream of digital pictures may include at least one digital picture, at least one region, at least one feature, or at least one pixel of the first stream of digital pictures. The comparing the at least one portion of the new stream of digital pictures with the at least one portion of the first stream of digital pictures may include comparing at least one digital picture of the new stream of digital pictures with at least one digital picture of the first stream of digital pictures. The at least one portion of the new stream of digital pictures with the at least one portion of the first stream of digital pictures may include comparing at least one region

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of at least one digital picture of the new stream of digital pictures with at least one region of at least one digital picture of the first stream of digital pictures. The comparing the at least one portion of the new stream of digital pictures with the at least one portion of the first stream of digital pictures may include comparing at least one feature of at least one digital picture of the new stream of digital pictures with at least one feature of at least one digital picture of the first stream of digital pictures. The comparing the at least one portion of the new stream of digital pictures with the at least one portion of the first stream of digital pictures may include comparing at least one pixel of at least one digital picture of the new stream of digital pictures with at least one pixel of at least one digital picture of the first stream of digital pictures. The comparing the at least one portion of the new stream of digital pictures with the at least one portion of the first stream of digital pictures may include at least one of: performing a color adjustment, performing a size adjustment, performing a content manipulation, performing temporal alignment, performing dynamic time warping, utilizing a transparency, or utilizing a mask on the new or the first stream of digital pictures. The comparing the at least one portion of the new stream of digital pictures with the at least one portion of the first stream of digital pictures may include recognizing at least one person or object in the new stream of digital pictures and at least one person or object in the first stream of digital pictures, and comparing the at least one person or object from the new stream of digital pictures with the at least one person or object from the first stream of digital pictures.

In certain embodiments, the anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining that there is at least a partial match between one or more portions of the new stream of digital pictures and one or more portions of the first stream of digital pictures. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining that a similarity between at least one portion of the new stream of digital pictures and at least one portion of the first stream of digital pictures exceeds a similarity threshold. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining a substantial similarity between at least one portion of the new stream of digital pictures and at least one portion of the first stream of digital pictures. The at least one portion of the new stream of digital pictures may include at least one digital picture, at least one region, at least one feature, or at least one pixel of the new stream of digital pictures. The at least one portion of the first stream of digital pictures may include at least one digital picture, at least one region, at least one feature, or at least one pixel of the first stream of digital pictures. The substantial similarity may be achieved when a similarity between the at least one portion of the new stream of digital pictures and the at least one portion of the first stream of digital pictures exceeds a similarity threshold. The substantial similarity may be achieved when a number or a percentage of matching or partially matching digital pictures from the new stream of digital pictures and from the first stream of digital pictures exceeds a threshold number or threshold percentage. The substantial similarity may be achieved when a number or a percentage of matching or partially matching regions of at least one digital picture from the new stream of digital pictures and from the first stream of digital pictures exceeds a threshold number or threshold percentage. The substantial similarity

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may be achieved when a number or a percentage of matching or partially matching features of at least one digital picture from the new stream of digital pictures and from the first stream of digital pictures exceeds a threshold number or threshold percentage. The substantial similarity may be achieved when a number or a percentage of matching or partially matching pixels of at least one digital picture from the new stream of digital pictures and from the first stream of digital pictures exceeds a threshold number or threshold percentage. The substantial similarity may be achieved when one or more same or similar objects are recognized in the new stream of digital pictures and the first stream of digital pictures. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining that a number or a percentage of matching digital pictures from the new stream of digital pictures and from the first stream of digital pictures exceeds a threshold number or threshold percentage. The matching digital pictures from the new stream of digital pictures and from the first stream of digital pictures may be determined factoring in at least one of: an order of a digital picture in a stream of digital pictures, an importance of a digital picture, a threshold for a similarity in a digital picture, or a threshold for a difference in a digital picture. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining that a number or a percentage of matching regions from at least one digital picture of the new stream of digital pictures and from at least one digital picture of the first stream of digital pictures exceeds a threshold number or threshold percentage. The matching regions from at least one digital picture of the new stream of digital pictures and from at least one digital picture of the first stream of digital pictures may be determined factoring in at least one of: a location of a region, an importance of a region, a threshold for a similarity in a region, or a threshold for a difference in a region. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining that a number or a percentage of matching features from at least one digital picture of the new stream of digital pictures and from at least one digital picture of the first stream of digital pictures exceeds a threshold number or threshold percentage. The matching features from at least one digital picture of the new stream of digital pictures and from at least one digital picture of the first stream of digital pictures may be determined factoring in at least one of: a type of a feature, an importance of a feature, a location of a feature, a threshold for a similarity in a feature, or a threshold for a difference in a feature. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining that a number or a percentage of matching pixels from at least one digital picture of the new stream of digital pictures and from at least one digital picture of the first stream of digital pictures exceeds a threshold number or threshold percentage. The matching pixels from at least one digital picture of the new stream of digital pictures and from at least one digital picture of the first stream of digital pictures may be determined factoring in at least one of: a location of a pixel, a threshold for a similarity in a pixel, or a threshold for a difference in a pixel. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes recognizing a same person or object in the new and the first streams of digital pictures.

In certain embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures instead of or prior to an instruction set that would have been executed next. In further embodiments, the causing the

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processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying one or more instruction sets of the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying a register or an element of the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes inserting the one or more instruction sets for operating the device correlated with the first stream of digital pictures into a register or an element of the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes redirecting the processor circuit to the one or more instruction sets for operating the device correlated with the first stream of digital pictures. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes redirecting the processor circuit to one or more alternate instruction sets, the alternate instruction sets comprising the one or more instruction sets for operating the device correlated with the first stream of digital pictures. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes transmitting, to the processor circuit for execution, the one or more instruction sets for operating the device correlated with the first stream of digital pictures. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes issuing an interrupt to the processor circuit and executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures following the interrupt. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying an element that is part of, operating on, or coupled to the processor circuit.

In some embodiments, the processor circuit includes a logic circuit, and wherein the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes causing the logic circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures. The logic circuit may include a microcontroller. The causing the logic circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures may include modifying an element of the logic circuit. The causing the logic circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures may include inserting the one or more instruction sets for operating the device correlated with the first stream of digital pictures into an element of the logic circuit. The causing the logic circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures may include redirecting the logic circuit to the one or more instruction sets for operating the device correlated with the first stream of digital pictures. The causing the logic circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures may include replacing inputs into the logic circuit with the one or more instruction sets for operating the device correlated with the first stream of digital pictures. The causing the logic circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures may include replacing outputs from the logic circuit with the one or more instruction sets for operating the device correlated with the first stream of digital pictures.

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In certain embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes causing an application for operating the device to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures, the application running on the processor circuit.

In some embodiments, the system further comprises: an application including instruction sets for operating the device, the application running on the processor circuit, wherein the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying the application.

In certain embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes redirecting an application to the one or more instruction sets for operating the device correlated with the first stream of digital pictures, the application running on the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes redirecting an application to one or more alternate instruction sets, the application running on the processor circuit, the alternate instruction sets comprising the one or more instruction sets for operating the device correlated with the first stream of digital pictures. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying one or more instruction sets of an application, the application running on the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying a source code, a bytecode, an intermediate code, a compiled code, an interpreted code, a translated code, a runtime code, an assembly code, or a machine code. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying at least one of: the memory unit, a register of the processor circuit, a storage, or a repository where instruction sets are stored or used. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying one or more instruction sets for operating an application or an object of the application, the application running on the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying at least one of: an element of the processor circuit, an element of the device, a virtual machine, a runtime engine, an operating system, an execution stack, a program counter, or a user input. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying one or more instruction sets at a source code write time, a compile time, an interpretation time, a translation time, a linking time, a loading time, or a runtime. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying one or more code segments, lines of code, statements, instructions, functions, routines, subroutines, or basic blocks. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes a manual, an automatic, a dynamic, or a just in time (JIT) instrumentation of an application, the application running on the processor circuit. In further embodiments, the causing the processor

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circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes utilizing one or more of a .NET tool, a .NET application programming interface (API), a Java tool, a Java API, an operating system tool, or an independent tool for modifying instruction sets. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes utilizing at least one of: a dynamic, an interpreted, or a scripting programming language. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes utilizing at least one of: a dynamic code, a dynamic class loading, or a reflection. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes utilizing an assembly language. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes utilizing at least one of: a metaprogramming, a self-modifying code, or an instruction set modification tool. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes utilizing at least one of: just in time (JIT) compiling, JIT interpretation, JIT translation, dynamic recompiling, or binary rewriting. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes utilizing at least one of: a dynamic expression creation, a dynamic expression execution, a dynamic function creation, or a dynamic function execution. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes adding or inserting additional code into a code of an application, the application running on the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes at least one of: modifying, removing, rewriting, or overwriting a code of an application, the application running on the processor circuit. In further embodiments, the causing the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes at least one of: branching, redirecting, extending, or hot swapping a code of an application, the application running on the processor circuit. The branching or redirecting the code may include inserting at least one of: a branch, a jump, or a means for redirecting an execution.

In some embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes implementing a user's knowledge, style, or methodology of operating the device in a visual surrounding.

In certain embodiments, the system further comprises: an interface configured to cause execution of instruction sets, wherein the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures is caused by the interface. The interface may include a modification interface.

In some embodiments, the artificial intelligence unit is further configured to: receive at least one extra information. In further embodiments, the at least one extra information include one or more of: an information on a stream of digital pictures, an information on an object in the stream of digital pictures, an information on the device's visual surrounding, an information on an instruction set, an information on an application, an information on an object of the application, an information on the processor circuit, an information on the device, or an information on an user. In further embodiments, the artificial intelligence unit is further configured to: learn the first stream of digital

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pictures correlated with the at least one extra information. The learning the first stream of digital pictures correlated with at least one extra information may include correlating the first stream of digital pictures with the at least one extra information. The learning the first stream of digital pictures correlated with at least one extra information may include storing the first stream of digital pictures correlated with the at least one extra information into the memory unit. In further embodiments, the anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between an extra information correlated with the new stream of digital pictures and an extra information correlated with the first stream of digital pictures. The anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between an extra information correlated with the new stream of digital pictures and an extra information correlated with the first stream of digital pictures may include comparing an extra information correlated with the new stream of digital pictures and an extra information correlated with the first stream of digital pictures. The anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between an extra information correlated with the new stream of digital pictures and an extra information correlated with the first stream of digital pictures may include determining that a similarity between an extra information correlated with the new stream of digital pictures and an extra information correlated with the first stream of digital pictures exceeds a similarity threshold.

In certain embodiments, the artificial intelligence unit is further configured to: receive a second stream of digital pictures from the picture capturing apparatus; receive additional one or more instruction sets for operating the device from the processor circuit; and learn the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device. In further embodiments, the learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device and the learning the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device include creating a connection between the first stream of digital pictures correlated with the one or more instruction sets for operating the device and the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device. The connection includes or is associated with at least one of: an occurrence count, a weight, a parameter, or a data. In further embodiments, the learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device and the learning the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device include updating a connection between the first stream of digital pictures correlated with the one or more instruction sets for operating the device and the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device. The updating the connection between the first stream of digital pictures correlated with the one or more instruction sets for operating the device and the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device may include updating at least one of: an occurrence count, a weight, a parameter, or a data included in or associated with the connection. In further embodiments, the learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device includes storing the first stream of digital pictures correlated with the one or more instruction sets for operating the device into a first node of a data structure, and wherein the learning the second stream of digital pictures correlated with the additional

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one or more instruction sets for operating the device includes storing the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device into a second node of the data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. The learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device and the learning the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device may include creating a connection between the first node and the second node. The learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device and the learning the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device may include updating a connection between the first node and the second node. In further embodiments, the first stream of digital pictures correlated with the one or more instruction sets for operating the device is stored into a first node of a neural network and the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device is stored into a second node of the neural network. The first node and the second node may be connected by a connection. The first node may be part of a first layer of the neural network and the second node may be part of a second layer of the neural network. In further embodiments, the first stream of digital pictures correlated with the one or more instruction sets for operating the device is stored into a first node of a graph and the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device is stored into a second node of the graph. The first node and the second node may be connected by a connection. In further embodiments, the first stream of digital pictures correlated with the one or more instruction sets for operating the device is stored into a first node of a sequence and the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device is stored into a second node of the sequence.

In some aspects, the disclosure relates to a non-transitory computer storage medium having a computer program stored thereon, the program including instructions that when executed by one or more processor circuits cause the one or more processor circuits to perform operations comprising: receiving a first stream of digital pictures from a picture capturing apparatus. The operations may further include: receiving one or more instruction sets for operating a device. The operations may further include: learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device. The operations may further include: receiving a new stream of digital pictures from the picture capturing apparatus. The operations may further include: anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between the new stream of digital pictures and the first stream of digital pictures. The operations may further include: causing an execution of the one or more instruction sets for operating the device correlated with the first stream of digital pictures, the causing performed in response to the anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between the new stream of digital pictures and the first stream of digital pictures, wherein the device performs one or more operations defined by the one or more instruction sets for operating the device correlated with the first stream of digital pictures, the one or more operations performed in response to the executing.

In some aspects, the disclosure relates to a method comprising: (a) receiving a first stream of digital pictures from a picture capturing apparatus by one or more processor circuits. The method may further include: (b) receiving one or more instruction sets for operating a device by the one or more processor circuits. The method may

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further include: (c) learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device, the learning of (c) performed by the one or more processor circuits. The method may further include: (d) receiving a new stream of digital pictures from the picture capturing apparatus by the one or more processor circuits. The method may further include: (e) anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between the new stream of digital pictures and the first stream of digital pictures, the anticipating of (e) performed by the one or more processor circuits. The method may further include: (f) executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures, the executing of (f) performed in response to the anticipating of (e). The method may further include: (g) performing, by the device, one or more operations defined by the one or more instruction sets for operating the device correlated with the first stream of digital pictures, the one or more operations performed in response to the executing of (f).

The operations or steps of the non-transitory computer storage medium and/or the method may be performed by any of the elements of the above described systems as applicable. The non-transitory computer storage medium and/or the method may include any of the operations, steps, and embodiments of the above described systems as applicable as well as the following embodiments.

In some embodiments, the first stream of digital pictures correlated with the one or more instruction sets for operating the device includes a unit of knowledge of how the device operated in a visual surrounding. In further embodiments, the first stream of digital pictures correlated with the one or more instruction sets for operating the device is included in a neuron, a node, a vertex, or an element of a data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. Some of the neurons, nodes, vertices, or elements may be interconnected. In further embodiments, the first stream of digital pictures correlated with the one or more instruction sets for operating the device is structured into a knowledge cell. In further embodiments, the knowledge cell includes a unit of knowledge of how the device operated in a visual surrounding. In further embodiments, the knowledge cell is included in a neuron, a node, a vertex, or an element of a data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. Some of the neurons, nodes, vertices, or elements may be interconnected.

In certain embodiments, the learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device includes correlating the first stream of digital pictures with the one or more instruction sets for operating the device. The correlating the first stream of digital pictures with the one or more instruction sets for operating the device may include generating a knowledge cell, the knowledge cell comprising the first stream of digital pictures correlated with the one or more instruction sets for operating the device. The correlating the first stream of digital pictures with the one or more instruction sets for operating the device may include structuring a unit of knowledge of how the device operated in a visual surrounding. In further embodiments, the learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device includes learning a user's knowledge, style, or methodology of operating the device in a visual surrounding. In further embodiments, the learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device includes spontaneous learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device.

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In some embodiments, the learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device includes storing, into a memory unit, the first stream of digital pictures correlated with the one or more instruction sets for operating the device, the first stream of digital pictures correlated with the one or more instruction sets for operating the device being part of a stored plurality of streams of digital pictures correlated with one or more instruction sets for operating the device. In further embodiments, the plurality of streams of digital pictures correlated with one or more instruction sets for operating the device include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, a knowledge structure, or a data structure. In further embodiments, the plurality of streams of digital pictures correlated with one or more instruction sets for operating the device are organized into a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, a knowledge structure, or a data structure. In further embodiments, each of the plurality of streams of digital pictures correlated with one or more instruction sets for operating the device is included in a neuron, a node, a vertex, or an element of a data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. Some of the neurons, nodes, vertices, or elements may be interconnected. In further embodiments, the plurality of streams of digital pictures correlated with one or more instruction sets for operating the device include a user's knowledge, style, or methodology of operating the device in visual surroundings. In further embodiments, the plurality of streams of digital pictures correlated with one or more instruction sets for operating the device are stored on a remote computing device. In further embodiments, the plurality of streams of digital pictures correlated with one or more instruction sets for operating the device include an artificial intelligence system for knowledge structuring, storing, or representation. The artificial intelligence system for knowledge structuring, storing, or representation may include at least one of: a deep learning system, a supervised learning system, an unsupervised learning system, a neural network, a search-based system, an optimization-based system, a logic-based system, a fuzzy logic-based system, a tree-based system, a graph-based system, a hierarchical system, a symbolic system, a sub-symbolic system, an evolutionary system, a genetic system, a multi-agent system, a deterministic system, a probabilistic system, or a statistical system.

In further embodiments, the anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes comparing at least one portion of the new stream of digital pictures with at least one portion of the first stream of digital pictures. The at least one portion of the new stream of digital pictures may include at least one digital picture, at least one region, at least one feature, or at least one pixel of the new stream of digital pictures. The at least one portion of the first stream of digital pictures may include at least one digital picture, at least one region, at least one feature, or at least one pixel of the first stream of digital pictures. The comparing the at least one portion of the new stream of digital pictures with the at least one portion of the first stream of digital pictures may include comparing at least one digital picture of the new stream of digital pictures with at least one digital picture of the first stream of digital pictures. The comparing the at least one portion of the new stream of digital pictures with the at least one portion of the first stream of digital pictures may include comparing at least one region of at least one digital picture of the new stream of digital pictures with at least one region of at least one digital picture of the first stream of digital pictures. The comparing the at least one portion of the new stream of digital pictures with the at least one portion of the first stream of digital pictures may include comparing at least one

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feature of at least one digital picture of the new stream of digital pictures with at least one feature of at least one digital picture of the first stream of digital pictures. The comparing the at least one portion of the new stream of digital pictures with the at least one portion of the first stream of digital pictures may include comparing at least one pixel of at least one digital picture of the new stream of digital pictures with at least one pixel of at least one digital picture of the first stream of digital pictures. The comparing the at least one portion of the new stream of digital pictures with the at least one portion of the first stream of digital pictures may include at least one of: performing a color adjustment, performing a size adjustment, performing a content manipulation, performing temporal alignment, performing dynamic time warping, utilizing a transparency, or utilizing a mask on the new or the first stream of digital pictures. The comparing the at least one portion of the new stream of digital pictures with the at least one portion of the first stream of digital pictures may include recognizing at least one person or object in the new stream of digital pictures and at least one person or object in the first stream of digital pictures, and comparing the at least one person or object from the new stream of digital pictures with the at least one person or object from the first stream of digital pictures.

In some embodiments, the anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining that there is at least a partial match between one or more portions of the new stream of digital pictures and one or more portions of the first stream of digital pictures. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining that a similarity between at least one portion of the new stream of digital pictures and at least one portion of the first stream of digital pictures exceeds a similarity threshold. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining a substantial similarity between at least one portion of the new stream of digital pictures and at least one portion of the first stream of digital pictures. The at least one portion of the new stream of digital pictures may include at least one digital picture, at least one region, at least one feature, or at least one pixel of the new stream of digital pictures. The at least one portion of the first stream of digital pictures may include at least one digital picture, at least one region, at least one feature, or at least one pixel of the first stream of digital pictures. The substantial similarity may be achieved when a similarity between the at least one portion of the new stream of digital pictures and the at least one portion of the first stream of digital pictures exceeds a similarity threshold. The substantial similarity may be achieved when a number or a percentage of matching or partially matching digital pictures from the new stream of digital pictures and from the first stream of digital pictures exceeds a threshold number or threshold percentage. The substantial similarity may be achieved when a number or a percentage of matching or partially matching regions of at least one digital picture from the new stream of digital pictures and from the first stream of digital pictures exceeds a threshold number or threshold percentage. The substantial similarity may be achieved when a number or a percentage of matching or partially matching features of at least one digital picture from the new stream of digital pictures and from the first stream of digital pictures exceeds a threshold number or threshold percentage. The substantial similarity may be achieved when a number or a percentage of

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matching or partially matching pixels of at least one digital picture from the new stream of digital pictures and from the first stream of digital pictures exceeds a threshold number or threshold percentage. The substantial similarity may be achieved when one or more same or similar objects are recognized in the new stream of digital pictures and the first stream of digital pictures. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining that a number or a percentage of matching digital pictures from the new stream of digital pictures and from the first stream of digital pictures exceeds a threshold number or threshold percentage. The matching digital pictures from the new stream of digital pictures and from the first stream of digital pictures may be determined factoring in at least one of: an order of a digital picture in a stream of digital pictures, an importance of a digital picture, a threshold for a similarity in a digital picture, or a threshold for a difference in a digital picture. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining that a number or a percentage of matching regions from at least one digital picture of the new stream of digital pictures and from at least one digital picture of the first stream of digital pictures exceeds a threshold number or threshold percentage. The matching regions from at least one digital picture of the new stream of digital pictures and from at least one digital picture of the first stream of digital pictures may be determined factoring in at least one of: a location of a region, an importance of a region, a threshold for a similarity in a region, or a threshold for a difference in a region. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining that a number or a percentage of matching features from at least one digital picture of the new stream of digital pictures and from at least one digital picture of the first stream of digital pictures exceeds a threshold number or threshold percentage. The matching features from at least one digital picture of the new stream of digital pictures and from at least one digital picture of the first stream of digital pictures may be determined factoring in at least one of: a type of a feature, an importance of a feature, a location of a feature, a threshold for a similarity in a feature, or a threshold for a difference in a feature. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes determining that a number or a percentage of matching pixels from at least one digital picture of the new stream of digital pictures and from at least one digital picture of the first stream of digital pictures exceeds a threshold number or threshold percentage. The matching pixels from at least one digital picture of the new stream of digital pictures and from at least one digital picture of the first stream of digital pictures may be determined factoring in at least one of: a location of a pixel, a threshold for a similarity in a pixel, or a threshold for a difference in a pixel. In further embodiments, the determining that there is at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes recognizing a same person or object in the new and the first streams of digital pictures.

In certain embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures instead of or prior to an instruction set that would have been executed next. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying one or more instruction sets. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying a register or an element of a processor circuit. In further embodiments, the

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executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes inserting the one or more instruction sets for operating the device correlated with the first stream of digital pictures into a register or an element of a processor circuit. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes redirecting a processor circuit to the one or more instruction sets for operating the device correlated with the first stream of digital pictures. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes redirecting a processor circuit to one or more alternate instruction sets, the alternate instruction sets comprising the one or more instruction sets for operating the device correlated with the first stream of digital pictures. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes transmitting, to a processor circuit for execution, the one or more instruction sets for operating the device correlated with the first stream of digital pictures. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes issuing an interrupt to a processor circuit and executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures following the interrupt. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying an element that is part of, operating on, or coupled to a processor circuit.

In some embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes executing, by a logic circuit, the one or more instruction sets for operating the device correlated with the first stream of digital pictures. The logic circuit may include a microcontroller. The executing, by the logic circuit, the one or more instruction sets for operating the device correlated with the first stream of digital pictures may include modifying an element of the logic circuit. The executing, by the logic circuit, the one or more instruction sets for operating the device correlated with the first stream of digital pictures may include inserting the one or more instruction sets for operating the device correlated with the first stream of digital pictures into an element of the logic circuit. The executing, by the logic circuit, the one or more instruction sets for operating the device correlated with the first stream of digital pictures may include redirecting the logic circuit to the one or more instruction sets for operating the device correlated with the first stream of digital pictures. The executing, by the logic circuit, the one or more instruction sets for operating the device correlated with the first stream of digital pictures may include replacing inputs into the logic circuit with the one or more instruction sets for operating the device correlated with the first stream of digital pictures. The executing, by the logic circuit, the one or more instruction sets for operating the device correlated with the first stream of digital pictures my include replacing outputs from the logic circuit with the one or more instruction sets for operating the device correlated with the first stream of digital pictures.

In certain embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes executing, by an application for operating the device, the one or more instruction sets for operating the device correlated with the first stream of digital pictures. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying an application, the application including instruction sets for operating the device. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first

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stream of digital pictures includes redirecting an application to the one or more instruction sets for operating the device correlated with the first stream of digital pictures. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes redirecting an application to one or more alternate instruction sets, the alternate instruction sets comprising the one or more instruction sets for operating the device correlated with the first stream of digital pictures. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying one or more instruction sets of an application. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying a source code, a bytecode, an intermediate code, a compiled code, an interpreted code, a translated code, a runtime code, an assembly code, or a machine code. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying at least one of: a memory unit, a register of a processor circuit, a storage, or a repository where instruction sets are stored or used. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying one or more instruction sets for operating an application or an object of the application. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying at least one of: an element of a processor circuit, an element of the device, a virtual machine, a runtime engine, an operating system, an execution stack, a program counter, or a user input. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying one or more instruction sets at a source code write time, a compile time, an interpretation time, a translation time, a linking time, a loading time, or a runtime. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes modifying one or more code segments, lines of code, statements, instructions, functions, routines, subroutines, or basic blocks. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes a manual, an automatic, a dynamic, or a just in time (JIT) instrumentation of an application. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes utilizing one or more of a .NET tool, a .NET application programming interface (API), a Java tool, a Java API, an operating system tool, or an independent tool for modifying instruction sets. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes utilizing at least one of: a dynamic, an interpreted, or a scripting programming language. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes utilizing at least one of: a dynamic code, a dynamic class loading, or a reflection. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes utilizing an assembly language. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes utilizing at least one of: a metaprogramming, a self-modifying code, or an instruction set modification tool. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes utilizing at least one of: just in time (JIT) compiling, JIT interpretation, JIT translation, dynamic recompiling, or binary rewriting. In further embodiments, the executing the one or more instruction sets for

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operating the device correlated with the first stream of digital pictures includes utilizing at least one of: a dynamic expression creation, a dynamic expression execution, a dynamic function creation, or a dynamic function execution. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes adding or inserting additional code into a code of an application. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes at least one of: modifying, removing, rewriting, or overwriting a code of an application. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes at least one of: branching, redirecting, extending, or hot swapping a code of an application. The branching or redirecting the code may include inserting at least one of: a branch, a jump, or a means for redirecting an execution. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes implementing a user's knowledge, style, or methodology of operating the device in a visual surrounding. In further embodiments, the executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures includes executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures via an interface. The interface may include a modification interface.

In certain embodiments, the operations of the non-transitory computer storage medium and/or the method further comprise: receiving at least one extra information. In further embodiments, the at least one extra information include one or more of: an information on a stream of digital pictures, an information on an object in the stream of digital pictures, an information on the device's visual surrounding, an information on an instruction set, an information on an application, an information on an object of the application, an information on a processor circuit, an information on the device, or an information on an user. In further embodiments, the operations of the non-transitory computer storage medium and/or the method further comprise: learning the first stream of digital pictures correlated with the at least one extra information. The learning the first stream of digital pictures correlated with at least one extra information may include correlating the first stream of digital pictures with the at least one extra information. The learning the first stream of digital pictures correlated with at least one extra information may include storing the first stream of digital pictures correlated with the at least one extra information into a memory unit. In further embodiments, the anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between the new stream of digital pictures and the first stream of digital pictures includes anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between an extra information correlated with the new stream of digital pictures and an extra information correlated with the first stream of digital pictures. The anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between an extra information correlated with the new stream of digital pictures and an extra information correlated with the first stream of digital pictures may include comparing an extra information correlated with the new stream of digital pictures and an extra information correlated with the first stream of digital pictures. The anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between an extra information correlated with the new stream of digital pictures and an extra information correlated with the first stream of digital pictures may include

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determining that a similarity between an extra information correlated with the new stream of digital pictures and an extra information correlated with the first stream of digital pictures exceeds a similarity threshold.

In some embodiments, the operations of the non-transitory computer storage medium and/or the method further comprise: receiving a second stream of digital pictures from the picture capturing apparatus; receiving additional one or more instruction sets for operating the device; and learning the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device. In further embodiments, the learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device and the learning the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device include creating a connection between the first stream of digital pictures correlated with the one or more instruction sets for operating the device and the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device. The connection may include or be associated with at least one of: an occurrence count, a weight, a parameter, or a data. In further embodiments, the learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device and the learning the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device include updating a connection between the first stream of digital pictures correlated with the one or more instruction sets for operating the device and the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device. The updating the connection between the first stream of digital pictures correlated with the one or more instruction sets for operating the device and the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device may include updating at least one of: an occurrence count, a weight, a parameter, or a data included in or associated with the connection. In further embodiments, the learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device includes storing the first stream of digital pictures correlated with the one or more instruction sets for operating the device into a first node of a data structure, and wherein the learning the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device includes storing the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device into a second node of the data structure. The data structure may include a neural network, a graph, a collection of sequences, a sequence, a collection of knowledge cells, a knowledgebase, or a knowledge structure. The learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device and the learning the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device may include creating a connection between the first node and the second node. The learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device and the learning the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device may include updating a connection between the first node and the second node. In further embodiments, the first stream of digital pictures correlated with the one or more instruction sets for operating the device is stored into a first node of a neural network and the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device is stored into a second node of the neural network. The first node and the second node may be connected by a connection. The first node may be part of a first layer of the neural network and the second node may be part of a second layer of the neural network. In further embodiments, the first stream of digital pictures correlated with the one or more instruction sets for operating the

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device is stored into a first node of a graph and the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device is stored into a second node of the graph. The first node and the second node may be connected by a connection. In further embodiments, the first stream of digital pictures correlated with the one or more instruction sets for operating the device is stored into a first node of a sequence and the second stream of digital pictures correlated with the additional one or more instruction sets for operating the device is stored into a second node of the sequence.

In some aspects, the disclosure relates to a system for learning a visual surrounding for autonomous device operating. The system may be implemented at least in part on one or more computing devices. In some embodiments, the system comprises a processor circuit configured to execute instruction sets for operating a device. The system may further include a memory unit configured to store data. The system may further include a picture capturing apparatus configured to capture digital pictures. The system may further include an artificial intelligence unit. In some embodiments, the artificial intelligence unit may be configured to: receive a first stream of digital pictures from the picture capturing apparatus. The artificial intelligence unit may be further configured to: receive one or more instruction sets for operating the device from the processor circuit. The artificial intelligence unit may be further configured to: learn the first stream of digital pictures correlated with the one or more instruction sets for operating the device.

In some aspects, the disclosure relates to a non-transitory computer storage medium having a computer program stored thereon, the program including instructions that when executed by one or more processor circuits cause the one or more processor circuits to perform operations comprising: receiving a first stream of digital pictures from a picture capturing apparatus. The operations may further include: receiving one or more instruction sets for operating a device. The operations may further include: learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device.

In some aspects, the disclosure relates to a method comprising: (a) receiving a first stream of digital pictures from a picture capturing apparatus by one or more processor circuits. The method may further include: (b) receiving one or more instruction sets for operating a device by the one or more processor circuits. The method may further include: (c) learning the first stream of digital pictures correlated with the one or more instruction sets for operating the device, the learning of (c) performed by the one or more processor circuits.

The operations or steps of the non-transitory computer storage medium and/or the method may be performed by any of the elements of the above described systems as applicable. The non-transitory computer storage medium and/or the method may include any of the operations, steps, and embodiments of the above described systems as applicable.

In some aspects, the disclosure relates to a system for using a visual surrounding for autonomous device operating. The system may be implemented at least in part on one or more computing devices. In some embodiments, the system comprises a processor circuit configured to execute instruction sets for operating a device. The system may further include a memory unit configured to store data. The system may further include a picture capturing apparatus configured to capture digital pictures. The system may further include an artificial intelligence unit. In some embodiments, the artificial intelligence unit may be configured to: access the memory unit that stores a plurality of streams of digital pictures correlated with one or more instruction sets for operating the device, the plurality including a first stream of digital pictures correlated with one or more instruction sets for

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operating the device. The artificial intelligence unit may be further configured to: receive a new stream of digital pictures from the picture capturing apparatus. The artificial intelligence unit may be further configured to: anticipate the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between the new stream of digital pictures and the first stream of digital pictures. The artificial intelligence unit may be further configured to: cause the processor circuit to execute the one or more instruction sets for operating the device correlated with the first stream of digital pictures, the executing performed in response to the anticipating of the artificial intelligence unit, wherein the device performs one or more operations defined by the one or more instruction sets for operating the device correlated with the first stream of digital pictures, the one or more operations performed in response to the executing by the processor circuit.

In some aspects, the disclosure relates to a non-transitory computer storage medium having a computer program stored thereon, the program including instructions that when executed by one or more processor circuits cause the one or more processor circuits to perform operations comprising: accessing a memory unit that stores a plurality of streams of digital pictures correlated with one or more instruction sets for operating a device, the plurality including a first stream of digital pictures correlated with one or more instruction sets for operating the device. The operations may further include: receiving a new stream of digital pictures from a picture capturing apparatus. The operations may further include: anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between the new stream of digital pictures and the first stream of digital pictures. The operations may further include: causing an execution of the one or more instruction sets for operating the device correlated with the first stream of digital pictures, the causing performed in response to the anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between the new stream of digital pictures and the first stream of digital pictures, wherein the device performs one or more operations defined by the one or more instruction sets for operating the device correlated with the first stream of digital pictures, the one or more operations performed in response to the executing.

In some aspects, the disclosure relates to a method comprising: (a) accessing a memory unit that stores a plurality of streams of digital pictures correlated with one or more instruction sets for operating a device, the plurality including a first stream of digital pictures correlated with one or more instruction sets for operating the device, the accessing of (a) performed by the one or more processor circuits. The method may further include: (b) receiving a new stream of digital pictures from a picture capturing apparatus by the one or more processor circuits. The method may further include: (c) anticipating the one or more instruction sets for operating the device correlated with the first stream of digital pictures based on at least a partial match between the new stream of digital pictures and the first stream of digital pictures, the anticipating of (c) performed by the one or more processor circuits. The method may further include: (d) executing the one or more instruction sets for operating the device correlated with the first stream of digital pictures, the executing of (d) performed in response to the anticipating of (c). The method may further include: (e) performing, by the device, one or more operations defined by the one or more instruction sets for operating the device correlated with the first stream of digital pictures, the one or more operations performed in response to the executing of (d).

The operations or steps of the non-transitory computer storage medium and/or the method may be performed by any of the elements of the above described systems as applicable. The non-transitory computer

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storage medium and/or the method may include any of the operations, steps, and embodiments of the above described systems as applicable.

In some aspects, the disclosure relates to a system for learning and using a visual surrounding for autonomous device operating. The system may be implemented at least in part on one or more computing devices. In some embodiments, the system comprises a logic circuit configured to receive inputs and produce outputs, the outputs for operating a device. The system may further include a memory unit configured to store data. The system may further include a picture capturing apparatus configured to capture digital pictures. The system may further include an artificial intelligence unit. In some embodiments, the artificial intelligence unit may be configured to: receive a first digital picture from the picture capturing apparatus. The artificial intelligence unit may be further configured to: receive at least one input, wherein the at least one input is also received by the logic circuit. The artificial intelligence unit may be further configured to: learn the first digital picture correlated with the at least one input. The artificial intelligence unit may be further configured to: receive a new digital picture from the picture capturing apparatus. The artificial intelligence unit may be further configured to: anticipate the at least one input correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture. The artificial intelligence unit may be further configured to: cause the logic circuit to receive the at least one input correlated with the first digital picture, the causing performed in response to the anticipating of the artificial intelligence unit, wherein the device performs at least one operation defined by at least one output for operating the device produced by the logic circuit.

In certain embodiments, the logic circuit configured to receive inputs and produce outputs includes a logic circuit configured to produce outputs based at least in part on logic operations performed on the inputs. In further embodiments, the learning the first digital picture correlated with the at least one input includes correlating the first digital picture with the at least one input. In further embodiments, the learning the first digital picture correlated with the at least one input includes storing, into the memory unit, the first digital picture correlated with the at least one input, the first digital picture correlated with the at least input being part of a stored plurality of digital pictures correlated with at least one input. In further embodiments, the anticipating the at least one input correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture includes comparing at least one portion of the new digital picture with at least one portion of the first digital picture. In further embodiments, the anticipating the at least one input correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture includes determining that there is at least a partial match between the new digital picture and the first digital picture. In further embodiments, the causing the logic circuit to receive the at least one input correlated with the first digital picture includes transmitting, to the logic circuit, the at least one input correlated with the first digital picture. In further embodiments, the causing the logic circuit to receive the at least one input correlated with the first digital picture includes replacing at least one input into the logic circuit with the at least one input correlated with the first digital picture.

In some aspects, the disclosure relates to a non-transitory computer storage medium having a computer program stored thereon, the program including instructions that when executed by one or more processor circuits cause the one or more processor circuits to perform operations comprising: receiving a first digital picture from a picture capturing apparatus. The operations may further include: receiving at least one input, wherein the at least one input is also received by a logic circuit, and wherein the logic circuit is configured to receive inputs and produce

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outputs, the outputs for operating a device. The operations may further include: learning the first digital picture correlated with the at least one input. The operations may further include: receiving a new digital picture from the picture capturing apparatus. The operations may further include: anticipating the at least one input correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture. The operations may further include: causing the logic circuit to receive the at least one input correlated with the first digital picture, the causing performed in response to the anticipating the at least one input correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture, wherein the device performs at least one operation defined by at least one output for operating the device produced by the logic circuit.

In some aspects, the disclosure relates to a method comprising: (a) receiving a first digital picture from a picture capturing apparatus by one or more processor circuits. The method may further include: (b) receiving at least one input by the one or more processor circuits, wherein the at least one input are also received by a logic circuit, and wherein the logic circuit is configured to receive inputs and produce outputs, the outputs for operating a device. The method may further include: (c) learning the first digital picture correlated with the at least one input, the learning of (c) performed by the one or more processor circuits. The method may further include: (d) receiving a new digital picture from the picture capturing apparatus by the one or more processor circuits. The method may further include: (e) anticipating the at least one input correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture, the anticipating of (e) performed by the one or more processor circuits. The method may further include: (f) receiving, by the logic circuit, the at least one input correlated with the first digital picture, the receiving of (f) performed in response to the anticipating of (e). The method may further include: (g) performing, by the device, at least one operation defined by at least one output for operating the device produced by the logic circuit.

The operations or steps of the non-transitory computer storage medium and/or the method may be performed by any of the elements of the above described systems as applicable. The non-transitory computer storage medium and/or the method may include any of the operations, steps, and embodiments of the above described systems as applicable.

In some aspects, the disclosure relates to a system for learning and using a visual surrounding for autonomous device operating. The system may be implemented at least in part on one or more computing devices. In some embodiments, the system comprises a logic circuit configured to receive inputs and produce outputs, the outputs for operating a device. The system may further include a memory unit configured to store data. The system may further include a picture capturing apparatus configured to capture digital pictures. The system may further include an artificial intelligence unit. In some embodiments, the artificial intelligence unit may be configured to: receive a first digital picture from the picture capturing apparatus. The artificial intelligence unit may be further configured to: receive at least one output, the at least one output transmitted from the logic circuit. The artificial intelligence unit may be further configured to: learn the first digital picture correlated with the at least one output. The artificial intelligence unit may be further configured to: receive a new digital picture from the picture capturing apparatus. The artificial intelligence unit may be further configured to: anticipate the at least one output correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital

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picture. The artificial intelligence unit may be further configured to: cause the device to perform at least one operation defined by the at least one output correlated with the first digital picture.

In certain embodiments, the logic circuit configured to receive inputs and produce outputs includes a logic circuit configured to produce outputs based at least in part on logic operations performed on the inputs. In further embodiments, the learning the first digital picture correlated with the at least one output includes correlating the first digital picture with the at least one output. In further embodiments, the learning the first digital picture correlated with the at least one output includes storing, into the memory unit, the first digital picture correlated with the at least one output, the first digital picture correlated with the at least output being part of a stored plurality of digital pictures correlated with at least one output. In further embodiments, the anticipating the at least one output correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture includes comparing at least one portion of the new digital picture with at least one portion of the first digital picture. In further embodiments, the anticipating the at least one output correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture includes determining that there is at least a partial match between the new digital picture and the first digital picture. In further embodiments, the causing the device to perform at least one operation defined by the at least one output correlated with the first digital picture includes replacing at least one output from the logic circuit with the at least one output correlated with the first digital picture.

In some aspects, the disclosure relates to a non-transitory computer storage medium having a computer program stored thereon, the program including instructions that when executed by one or more processor circuits cause the one or more processor circuits to perform operations comprising: receiving a first digital picture from the picture capturing apparatus. The operations may further include: receiving at least one output, the at least one output transmitted from a logic circuit, wherein the logic circuit is configured to receive inputs and produce outputs, the outputs for operating a device. The operations may further include: learning the first digital picture correlated with the at least one output. The operations may further include: receiving a new digital picture from the picture capturing apparatus. The operations may further include: anticipating the at least one output correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture. The operations may further include: causing the device to perform at least one operation defined by the at least one output correlated with the first digital picture.

In some aspects, the disclosure relates to a method comprising: (a) receiving a first digital picture from the picture capturing apparatus by one or more processor circuits. The method may further include: (b) receiving at least one output by the one or more processor circuits, the at least one output transmitted from a logic circuit, wherein the logic circuit is configured to receive inputs and produce outputs, the outputs for operating a device. The method may further include: (c) learning the first digital picture correlated with the at least one output, the learning of (c) performed by the one or more processor circuits. The method may further include: (d) receiving a new digital picture from the picture capturing apparatus by the one or more processor circuits. The method may further include: (e) anticipating the at least one output correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture, the anticipating of (e) performed by the one or more processor circuits. The method may further include: (f) performing, by the device, at least one operation defined by the at least one output correlated with the first digital picture.

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The operations or steps of the non-transitory computer storage medium and/or the method may be performed by any of the elements of the above described systems as applicable. The non-transitory computer storage medium and/or the method may include any of the operations, steps, and embodiments of the above described systems as applicable.

In some aspects, the disclosure relates to a system for learning and using a visual surrounding for autonomous device operating. The system may be implemented at least in part on one or more computing devices. In some embodiments, the system comprises an actuator configured to receive inputs and perform motions. The system may further include a memory unit configured to store data. The system may further include a picture capturing apparatus configured to capture digital pictures. The system may further include an artificial intelligence unit. In some embodiments, the artificial intelligence unit may be configured to: receive a first digital picture from the picture capturing apparatus. The artificial intelligence unit may be further configured to: receive at least one input, wherein the at least one input is also received by the actuator. The artificial intelligence unit may be further configured to: learn the first digital picture correlated with the at least one input. The artificial intelligence unit may be further configured to: receive a new digital picture from the picture capturing apparatus. The artificial intelligence unit may be further configured to. The artificial intelligence unit may be further configured to: anticipate the at least one input correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture. The artificial intelligence unit may be further configured to: cause the actuator to receive the at least one input correlated with the first digital picture, the causing performed in response to the anticipating of the artificial intelligence unit, wherein the actuator performs at least one motion defined by the at least one input correlated with the first digital picture.

In some aspects, the disclosure relates to a non-transitory computer storage medium having a computer program stored thereon, the program including instructions that when executed by one or more processor circuits cause the one or more processor circuits to perform operations comprising: receiving a first digital picture from a picture capturing apparatus. The operations may further include: receiving at least one input, wherein the at least one input is also received by an actuator, and wherein the actuator is configured to receive inputs and perform motions. The operations may further include: learning the first digital picture correlated with the at least one input. The operations may further include: receiving a new digital picture from the picture capturing apparatus. The operations may further include: anticipating the at least one input correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture. The operations may further include: causing the actuator to receive the at least one input correlated with the first digital picture, the causing performed in response to the anticipating the at least one input correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture, wherein the actuator performs at least one motion defined by the at least one input correlated with the first digital picture.

In some aspects, the disclosure relates to a method comprising: (a) receiving a first digital picture from a picture capturing apparatus by one or more processor circuits. The method may further include: (b) receiving at least one input by the one or more processor circuits, wherein the at least one input are also received by an actuator, and wherein the actuator is configured to receive inputs and perform motions. The method may further include: (c) learning the first digital picture correlated with the at least one input, the learning of (c) performed by the one or more processor circuits. The method may further include: (d) receiving a new digital picture from the picture capturing

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apparatus by the one or more processor circuits. The method may further include: (e) anticipating the at least one input correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture, the anticipating of (e) performed by the one or more processor circuits. The method may further include: (f) receiving, by the actuator, the at least one input correlated with the first digital picture, the receiving of (f) performed in response to the anticipating of (e). The method may further include: (g) performing, by the actuator, at least one motion defined by the at least one input correlated with the first digital picture.

The operations or steps of the non-transitory computer storage medium and/or the method may be performed by any of the elements of the above described systems as applicable. The non-transitory computer storage medium and/or the method may include any of the operations, steps, and embodiments of the above described systems as applicable.

Other features and advantages of the disclosure will become apparent from the following description, including the claims and drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 illustrates a block diagram of Computing Device 70 that can provide processing capabilities used in some of the disclosed embodiments.

Fig. 2 illustrates an embodiment of Device 98 comprising Unit for Learning and/or Using Visual Surrounding for Autonomous Device Operation (VSADO Unit 100).

Fig. 3 illustrates some embodiments of obtaining instruction sets, data, and/or other information through tracing, profiling, or sampling of Processor 11 registers, memory, or other computing system components.

Figs. 4A-4B illustrate some embodiments of obtaining instruction sets, data, and/or other information through tracing, profiling, or sampling of Logic Circuit 250.

Figs. 5A-5E illustrate some embodiments of Instruction Sets 526.

Figs. 6A-6B illustrate some embodiments of Extra Information 527.

Fig. 7 illustrates an embodiment where VSADO Unit 100 is part of or operating on Processor 11.

Fig. 8 illustrates an embodiment where VSADO Unit 100 resides on Server 96 accessible over Network 95.

Fig. 9 illustrates an embodiment where Picture Capturing Apparatus 90 is part of Remote Device 97 accessible over Network 95.

Fig. 10 illustrates an embodiment of VSADO Unit 100 comprising Picture Recognizer 350.

Fig. 11 illustrates an embodiment of Artificial Intelligence Unit 110.

Fig. 12 illustrates an embodiment of Knowledge Structuring Unit 520 correlating individual Digital Pictures 525 with any Instruction Sets 526 and/or Extra Info 527.

Fig. 13 illustrates another embodiment of Knowledge Structuring Unit 520 correlating individual Digital Pictures 525 with any Instruction Sets 526 and/or Extra Info 527.

Fig. 14 illustrates an embodiment of Knowledge Structuring Unit 520 correlating streams of Digital Pictures 525 with any Instruction Sets 526 and/or Extra Info 527.

Fig. 15 illustrates another embodiment of Knowledge Structuring Unit 520 correlating streams of Digital Pictures 525 with any Instruction Sets 526 and/or Extra Info 527.

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Fig. 16 illustrates various artificial intelligence methods, systems, and/or models that can be utilized in VSADO Unit 100 embodiments.

Figs. 17A-17C illustrate embodiments of interconnected Knowledge Cells 800 and updating weights of Connections 853.

Fig. 18 illustrates an embodiment of learning Knowledge Cells 800 comprising one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 using Collection of Knowledge Cells 530d.

Fig. 19 illustrates an embodiment of learning Knowledge Cells 800 comprising one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 using Neural Network 530a.

Fig. 20 illustrates an embodiment of learning Knowledge Cells 800 comprising one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 using Neural Network 530a comprising shortcut Connections 853.

Fig. 21 illustrates an embodiment of learning Knowledge Cells 800 comprising one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 using Graph 530b.

Fig. 22 illustrates an embodiment of learning Knowledge Cells 800 comprising one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 using Collection of Sequences 530c.

Fig. 23 illustrates an embodiment of determining anticipatory Instruction Sets 526 from a single Knowledge Cell 800.

Fig. 24 illustrates an embodiment of determining anticipatory Instruction Sets 526 by traversing a single Knowledge Cell 800.

Fig. 25 illustrates an embodiment of determining anticipatory Instruction Sets 526 using collective similarity comparisons.

Fig. 26 illustrates an embodiment of determining anticipatory Instruction Sets 526 using Neural Network 530a.

Fig. 27 illustrates an embodiment of determining anticipatory Instruction Sets 526 using Graph 530b.

Fig. 28 illustrates an embodiment of determining anticipatory Instruction Sets 526 using Collection of Sequences 530c.

Fig. 29 illustrates some embodiments of modifying execution and/or functionality of Processor 11 through modification of Processor 11 registers, memory, or other computing system components.

Figs. 30A-30B illustrate some embodiments of modifying execution and/or functionality of Logic Circuit 250 through modification of inputs and/or outputs of Logic Circuit 250.

Fig. 31 illustrates a flow chart diagram of an embodiment of method 6100 for learning and/or using visual surrounding for autonomous device operation.

Fig. 32 illustrates a flow chart diagram of an embodiment of method 6200 for learning and/or using visual surrounding for autonomous device operation.

Fig. 33 illustrates a flow chart diagram of an embodiment of method 6300 for learning and/or using visual surrounding for autonomous device operation.

Fig. 34 illustrates a flow chart diagram of an embodiment of method 6400 for learning and/or using visual surrounding for autonomous device operation.

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Fig. 35 illustrates a flow chart diagram of an embodiment of method 6500 for learning and/or using visual surrounding for autonomous device operation.

Fig. 36 illustrates a flow chart diagram of an embodiment of method 6600 for learning and/or using visual surrounding for autonomous device operation.

Fig. 37 illustrates an exemplary embodiment of Computing-enabled Machine 98a.

Fig. 38 illustrates an exemplary embodiment of Computing-enabled Machine 98a comprising or coupled to a plurality of Picture Capturing Apparatuses 90.

Fig. 39 illustrates an exemplary embodiment of Fixture 98b.

Fig. 40 illustrates an exemplary embodiment of Control Device 98c.

Fig. 41 illustrates an exemplary embodiment of Smartphone 98d.

Like reference numerals in different figures indicate like elements. Horizontal or vertical "…" or other such indicia may be used to indicate additional instances of the same type of element. n, m, x, or other such letters or indicia represent integers or other sequential numbers that follow the sequence where they are indicated. It should be noted that n, m, x, or other such letters or indicia may represent different numbers in different elements even where the elements are depicted in the same figure. In general, n, m, x, or other such letters or indicia may follow the sequence and/or context where they are indicated. Any of these or other such letters or indicia may be used interchangeably depending on the context and space available. The drawings are not necessarily to scale, with emphasis instead being placed upon illustrating the embodiments, principles, and concepts of the disclosure. A line or arrow between any of the disclosed elements comprises an interface that enables the coupling, connection, and/or interaction between the elements.

DETAILED DESCRIPTION

The disclosed artificially intelligent devices, systems, and methods for learning and/or using visual surrounding for autonomous device operation comprise apparatuses, systems, methods, features, functionalities, and/or applications that enable learning one or more digital pictures of a device's surrounding along with correlated instruction sets for operating the device, storing this knowledge in a knowledgebase (i.e. neural network, graph, sequences, etc.), and autonomously operating a device. The disclosed artificially intelligent devices, systems, and methods for learning and/or using visual surrounding for autonomous device operation, any of their elements, any of their embodiments, or a combination thereof can generally be referred to as VSADO, VSADO Unit, or as other similar name or reference.

Referring now to Fig. 1, an embodiment is illustrated of Computing Device 70 (also referred to simply as computing device or other similar name or reference, etc.) that can provide processing capabilities used in some embodiments of the forthcoming disclosure. Later described devices and systems, in combination with processing capabilities of Computing Device 70, enable learning and/or using a device's visual surrounding for autonomous device operation and/or other functionalities described herein. Various embodiments of the disclosed devices, systems, and/or methods include hardware, functions, logic, programs, and/or a combination thereof that can be provided or implemented on any type or form of computing, computing enabled, or other device such as a mobile device, a computer, a computing enabled telephone, a server, a cloud device, a gaming device, a television device, a digital camera, a GPS receiver, a media player, an embedded device, a supercomputer, a wearable device, an

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implantable device, or any other type or form of computing, computing enabled, or other device capable of performing the operations described herein.

In some designs, Computing Device 70 comprises hardware, processing techniques or capabilities, programs, or a combination thereof. Computing Device 70 includes one or more central processing units, which may also be referred to as processors 11. Processor 11 includes one or more memory ports 10 and/or one or more input-output ports, also referred to as I/O ports 15, such as I/O ports 15A and 15B. Processor 11 may be special or general purpose. Computing Device 70 may further include memory 12, which can be connected to the remainder of the components of Computing Device 70 via bus 5. Memory 12 can be connected to processor 11 via memory port 10. Computing Device 70 may also include display device 21 such as a monitor, projector, glasses, and/or other display device. Computing Device 70 may also include Human-machine Interface 23 such as a keyboard, a pointing device, a mouse, a touchscreen, a joystick, and/or other input device that can be connected with the remainder of the Computing Device 70 components via I/O control 22. In some implementations, Human-machine Interface 23 can be connected with bus 5 or directly connected with specific components of Computing Device 70. Computing Device 70 may include additional elements, such as one or more input/output devices 13. Processor 11 may include or be interfaced with cache memory 14. Storage 27 may include memory, which provides an operating system, also referred to as OS 17, additional application programs 18 operating on OS 17, and/or data space 19 in which additional data or information can be stored. Alternative memory device 16 can be connected to the remaining components of Computing Device 70 via bus 5. Network interface 25 can also be connected with bus 5 and be used to communicate with external computing devices via a network. Some or all described elements of Computing Device 70 can be directly or operatively connected or coupled with each other using any other connection means known in art. Other additional elements may be included as needed, or some of the disclosed ones may be excluded, or a combination thereof may be utilized in alternate implementations of Computing Device 70.

Processor 11 includes any logic circuitry that can respond to or process instructions fetched from memory 12 or other element. Processor 11 may also include any combination of hardware and/or processing techniques or capabilities for implementing or executing logic functions or programs. Processor 11 may include a single core or a multi core processor. Processor 11 includes the functionality for loading operating system 17 and operating any application programs 18 thereon. In some embodiments, Processor 11 can be provided in a microprocessing or a processing unit, such as, for example, Snapdragon processor produced by Qualcomm Inc., processor by Intel Corporation of Mountain View, California, processor manufactured by Motorola Corporation of Schaumburg, Ill.; processor manufactured by Transmeta Corporation of Santa Clara, Calif.; the RS/6000 processor, processor manufactured by International Business Machines of White Plains, N.Y.; processor manufactured by Advanced Micro Devices of Sunnyvale, California, or any computing unit for performing similar functions. In other embodiments, processor 11 can be provided in a graphics processing unit (GPU), visual processing unit (VPU), or other highly parallel processing unit or circuit such as, for example, nVidia GeForce line of GPUs, AMD Radeon line of GPUs, and/or others. Such GPUs or other highly parallel processing units may provide superior performance in processing operations on neural networks and/or other data structures. In further embodiments, processor 11 can be provided in a micro controller such as, for example, Texas instruments, Atmel, Microchip Technology, ARM, Silicon Labs, Intel, and/or other lines of micro controllers, and/or others. In further embodiments, processor 11 includes any

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circuit (i.e. logic circuit, etc.) or device for performing logic operations. Computing Device 70 can be based on one or more of the aforementioned or other processors capable of operating as described herein.

Memory 12 includes one or more memory chips capable of storing data and allowing any storage location to be accessed by processor 11 and/or other element. Examples of Memory 12 include static random access memory (SRAM), Flash memory, Burst SRAM or SynchBurst SRAM (BSRAM), Dynamic random access memory (DRAM), Fast Page Mode DRAM (FPM DRAM), Enhanced DRAM (EDRAM), Extended Data Output RAM (EDO RAM), Extended Data Output DRAM (EDO DRAM), Burst Extended Data Output DRAM (BEDO DRAM), Enhanced DRAM (EDRAM), synchronous DRAM (SDRAM), JEDEC SRAM, PC100 SDRAM, Double Data Rate SDRAM (DDR SDRAM), Enhanced SDRAM (ESDRAM), SyncLink DRAM (SLDRAM), Direct Rambus DRAM (DRDRAM), Ferroelectric RAM (FRAM), and/or others. Memory 12 can be based on any of the above described memory chips, or any other available memory chips capable of operating as described herein. In some embodiments, processor 11 can communicate with memory 12 via a system bus 5. In other embodiments, processor 11 can communicate directly with memory 12 via a memory port 10.

Processor 11 can communicate directly with cache memory 14 via a connection means such as a secondary bus which may also sometimes be referred to as a backside bus. In some embodiments, processor 11 can communicate with cache memory 14 using the system bus 5. Cache memory 14 may typically have a faster response time than main memory 12 and can include a type of memory which is considered faster than main memory 12, such as for example SRAM, BSRAM, or EDRAM. Cache memory includes any structure such as multilevel caches, for example. In some embodiments, processor 11 can communicate with one or more I/O devices 13 via a system bus 5. Various busses can be used to connect processor 11 to any of the I/O devices 13, such as a VESA VL bus, an ISA bus, an EISA bus, a MicroChannel Architecture (MCA) bus, a PCI bus, a PCI-X bus, a PCI-Express bus, a NuBus, and/or others. In some embodiments, processor 11 can communicate directly with I/O device 13 via HyperTransport, Rapid I/O, or InfiniBand. In further embodiments, local busses and direct communication can be mixed. For example, processor 11 can communicate with an I/O device 13 using a local interconnect bus and communicate with another I/O device 13 directly. Similar configurations can be used for any other components described herein.

Computing Device 70 may further include alternative memory such as a SD memory slot, a USB memory stick, an optical drive such as a CD-ROM drive, a CD-R/RW drive, a DVD-ROM drive or a BlueRay disc, a hard-drive, and/or any other device comprising non-volatile memory suitable for storing data or installing application programs. Computing Device 70 may further include a storage device 27 comprising any type or form of non-volatile memory for storing an operating system (OS) such as any type or form of Windows OS, Mac OS, Unix OS, Linux OS, Android OS, iPhone OS, mobile version of Windows OS, an embedded OS, or any other OS that can operate on Computing Device 70. Computing Device 70 may also include application programs 18, and/or data space 19 for storing additional data or information. In some embodiments, alternative memory 16 can be used as or similar to storage device 27. Additionally, OS 17 and/or application programs 18 can be operable from a bootable medium, such as for example, a flash drive, a micro SD card, a bootable CD or DVD, and/or other bootable medium.

Application Program 18 (also referred to as program, computer program, application, script, code, or other similar name or refeerence) comprises instructions that can provide functionality when executed by processor 11. As such, Application Program 18 may be used to operate (i.e. perform operations on/with) or control a device or

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system. Application program 18 can be implemented in a high-level procedural or object-oriented programming language, or in a low-level machine or assembly language. Any language used can be compiled, interpreted, or otherwise translated into machine language. Application program 18 can be deployed in any form including as a stand-alone program or as a module, component, subroutine, or other unit suitable for use in a computing system. Application program 18 does not necessarily correspond to a file in a file system. A program can be stored in a portion of a file that may hold other programs or data, in a single file dedicated to the program, or in multiple files (i.e. files that store one or more modules, sub programs, or portions of code, etc.). Application Program 18 can be delivered in various forms such as, for example, executable file, library, script, plugin, addon, applet, interface, console application, web application, application service provider (ASP)-type application, operating system, and/or other forms. Application program 18 can be deployed to be executed on one computing device or on multiple computing devices (i.e. cloud, distributed, or parallel computing, etc.), or at one site or distributed across multiple sites interconnected by a communication network.

Network interface 25 can be utilized for interfacing Computing Device 70 with other devices via a network through a variety of connections including standard telephone lines, wired or wireless connections, LAN or WAN links (i.e. 802.11, T1, T3, 56 kb, X.25, etc.), broadband connections (i.e. ISDN, Frame Relay, ATM, etc.), or a combination thereof. Examples of networks include the Internet, an intranet, an extranet, a local area network (LAN), a wide area network (WAN), a personal area network (PAN), a home area network (HAN), a campus area network (CAN), a metropolitan area network (MAN), a global area network (GAN), a storage area network (SAN), virtual network, a virtual private network (VPN), Bluetooth network, a wireless network, a wireless LAN, a radio network, a HomePNA, a power line communication network, a G.hn network, an optical fiber network, an Ethernet network, an active networking network, a client-server network, a peer-to-peer network, a bus network, a star network, a ring network, a mesh network, a star-bus network, a tree network, a hierarchical topology network, and/or other networks. Network interface 25 may include a built-in network adapter, network interface card, PCMCIA network card, card bus network adapter, wireless network adapter, Bluetooth network adapter, WiFi network adapter, USB network adapter, modem, and/or any other device suitable for interfacing Computing Device 70 with any type of network capable of communication and/or operations described herein.

Still referring to Fig. 1, I/O devices 13 may be present in various shapes or forms in Computing Device 70. Examples of I/O device 13 capable of input include a joystick, a keyboard, a mouse, a trackpad, a trackpoint, a touchscreen, a trackball, a microphone, a drawing tablet, a glove, a tactile input device, a still or video camera, and/or other input device. Examples of I/O device 13 capable of output include a video display, a touchscreen, a projector, a glasses, a speaker, a tactile output device, and/or other output device. Examples of I/O device 13 capable of input and output include a disk drive, an optical storage device, a modem, a network card, and/or other input/output device. I/O device 13 can be interfaced with processor 11 via an I/O port 15, for example. I/O device 13 can also be controlled by I/O control 22 in some implementations. I/O control 22 may control one or more I/O devices such as Human-machine Interface 23 (i.e. keyboard, pointing device, touchscreen, joystick, mouse, optical pen, etc.). I/O control 22 enables any type or form of a device such as, for example, a video camera or microphone to be interfaced with other components of Computing Device 70. Furthermore, I/O device 13 may also provide storage such as or similar to storage 27, and/or alternative memory such as or similar to alternative memory 16 in some implementations.

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An output interface such as a graphical user interface, an acoustic output interface, a tactile output interface, any device driver (i.e. audio, video, or other driver), and/or other output interface or system can be utilized to process output from elements of Computing Device 70 for conveyance on an output device such as Display 21. In some aspects, Display 21 or other output device itself may include an output interface for processing output from elements of Computing Device 70. Further, an input interface such as a keyboard listener, a touchscreen listener, a mouse listener, any device driver (i.e. audio, video, keyboard, mouse, touchscreen, or other driver), a speech recognizer, a video interpreter, and/or other input interface or system can be utilized to process input from Human-machine Interface 23 or other input device for use by elements of Computing Device 70. In some aspects, Human-machine Interface 23 or other input device itself may include an input interface for processing input for use by elements of Computing Device 70.

Computing Device 70 may include or be connected to multiple display devices 21. Display devices 21 can each be of the same or different type or form. Computing Device 70 and/or its elements comprise any type or form of suitable hardware, programs, or a combination thereof to support, enable, or provide for the connection and use of multiple display devices 21. In one example, Computing Device 70 includes any type or form of video adapter, video card, driver, and/or library to interface, communicate, connect, or otherwise use display devices 21. In some aspects, a video adapter may include multiple connectors to interface to multiple display devices 21. In other aspects, Computing Device 70 includes multiple video adapters, with each video adapter connected to one or more display devices 21. In some embodiments, Computing Device's 70 operating system can be configured for using multiple displays 21. In other embodiments, one or more display devices 21 can be provided by one or more other computing devices such as remote computing devices connected to Computing Device 70 via a network.

In some embodiments, I/O device 13 can be a bridge between system bus 5 and an external communication bus, such as a USB bus, an Apple Desktop Bus, an RS-232 serial connection, a SCSI bus, a FireWire bus, a FireWire 800 bus, an Ethernet bus, an AppleTalk bus, a Gigabit Ethernet bus, an Asynchronous Transfer Mode bus, a HIPPI bus, a Super HIPPI bus, a SerialPlus bus, a SCI/LAMP bus, a FibreChannel bus, a Serial Attached small computer system interface bus, and/or other bus.

Computing Device 70 can operate under the control of operating system 17, which may support Computing Device's 70 basic functions, interface with and manage hardware resources, interface with and manage peripherals, provide common services for application programs, schedule tasks, and/or perform other functionalities. A modern operating system enables features and functionalities such as a high resolution display, graphical user interface (GUI), touchscreen, cellular network connectivity (i.e. mobile operating system, etc.), Bluetooth connectivity, WiFi connectivity, global positioning system (GPS) capabilities, mobile navigation, microphone, speaker, still picture camera, video camera, voice recorder, speech recognition, music player, video player, near field communication, personal digital assistant (PDA), and/or other features, functionalities, or applications. For example, Computing Device 70 can use any conventional operating system, any embedded operating system, any real-time operating system, any open source operating system, any video gaming operating system, any proprietary operating system, any online operating system, any operating system for mobile computing devices, or any other operating system capable of running on Computing Device 70 and performing operations described herein. Example of operating systems include Windows XP, Windows 7, Windows 8, etc. manufactured by Microsoft Corporation of Redmond, Wash.; Mac OS, iPhone OS, etc. manufactured by Apple Computer of Cupertino, Calif.; OS/2 manufactured by

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International Business Machines of Armonk, N.Y.; Linux, a freely-available operating system distributed by Caldera Corp. of Salt Lake City, Utah; or any type or form of a Unix operating system, among others. Any operating systems such as the ones for Android devices can similarly be utilized.

Computing Device 70 can be implemented as or be part of various different model architectures such as web services, distributed computing, grid computing, cloud computing, and/or other architectures. For example, in addition to the traditional desktop, server, or mobile operating system architectures, a cloud-based operating system can be utilized to provide the structure on which embodiments of the disclosure can be implemented. Other aspects of Computing Device 70 can also be implemented in the cloud without departing from the spirit and scope of the disclosure. For example, memory, storage, processing, and/or other elements can be hosted in the cloud. In some embodiments, Computing Device 70 can be implemented on multiple devices. For example, a portion of Computing Device 70 can be implemented on a mobile device and another portion can be implemented on wearable electronics.

Computing Device 70 can be or include any mobile device, a mobile phone, a smartphone (i.e. iPhone, Windows phone, Blackberry, Android phone, etc.), a tablet, a personal digital assistant (PDA), wearable electronics, implantable electronics, or another mobile device capable of implementing the functionalities described herein. In other embodiments, Computing Device 70 can be or include an embedded device, which can be any device or system with a dedicated function within another device or system. Embedded systems range from the simplest ones dedicated to one task with no user interface to complex ones with advanced user interface that may resemble modern desktop computer systems. Examples of devices comprising an embedded device include a mobile telephone, a personal digital assistant (PDA), a gaming device, a media player, a digital still or video camera, a pager, a television device, a set-top box, a personal navigation device, a global positioning system (GPS) receiver, a portable storage device (i.e. a USB flash drive, etc.), a digital watch, a DVD player, a printer, a microwave oven, a washing machine, a dishwasher, a gateway, a router, a hub, an automobile entertainment system, an automobile navigation system, a refrigerator, a washing machine, a factory automation device, an assembly line device, a factory floor monitoring device, a thermostat, an automobile, a factory controller, a telephone, a network bridge, and/or other devices. An embedded device can operate under the control of an operating system for embedded devices such as MicroC/OS-II, QNX, VxWorks, eCos, TinyOS, Windows Embedded, Embedded Linux, and/or other embedded device operating systems.

Various implementations of the disclosed devices, systems, and/or methods can be realized in digital electronic circuitry, integrated circuitry, logic gates, specially designed application specific integrated circuits (ASICs), field programmable gate arrays (FPGAs), computer hardware, firmware, programs, virtual machines, and/or combinations thereof including their structural, logical, and/or physical equivalents.

The disclosed devices, systems, and/or methods may include clients and servers. A client and server are generally remote from each other and typically interact via a network. The relationship of a client and server may arise by virtue of computer programs running on their respective computers and having a client-server relationship to each other.

The disclosed devices, systems, and/or methods can be implemented in a computing system that includes a back end component, a middleware component, a front end component, or any combination thereof. The

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components of the system can be interconnected by any form or medium of digital data communication such as, for example, a network.

Computing Device 70 may include or be interfaced with a computer program product comprising instructions or logic encoded on a computer-readable medium. Such instructions or logic, when executed, may configure or cause a processor to perform the operations and/or functionalities disclosed herein. For example, a computer program can be provided or encoded on a computer-readable medium such as an optical medium (i.e. DVD-ROM, etc.), flash drive, hard drive, any memory, firmware, or other medium. Computer program can be installed onto a computing device to cause the computing device to perform the operations and/or functionalities disclosed herein. Machine-readable medium, computer-readable medium, or other such terms may refer to any computer program product, apparatus, and/or device for providing instructions and/or data to a programmable processor. As such, machine-readable medium includes any medium that can send or receive machine instructions as a machine-readable signal. Examples of a machine-readable medium include a volatile and/or non-volatile medium, a removable and/or non-removable medium, a communication medium, a storage medium, and/or other medium. A communication medium, for example, can transmit computer readable instructions and/or data in a modulated data signal such as a carrier wave or other transport technique, and may include any other form of information delivery medium known in art. A non-transitory machine-readable medium comprises all machine-readable media except for a transitory, propagating signal.

In some embodiments, the disclosed artificially intelligent devices, systems, and/or methods for learning and/or using visual surrounding for autonomous device operation, or elements thereof, can be implemented entirely or in part in a device (i.e. microchip, circuitry, logic gates, electronic device, computing device, special or general purpose processor, etc.) or system that comprises (i.e. hard coded, internally stored, etc.) or is provided with (i.e. externally stored, etc.) instructions for implementing VSADO functionalities. As such, the disclosed artificially intelligent devices, systems, and/or methods for learning and/or using visual surrounding for autonomous device operation, or elements thereof, may include the processing, memory, storage, and/or other features, functionalities, and embodiments of Computing Device 70 or elements thereof. Such device or system can operate on its own (i.e. standalone device or system, etc.), be embedded in another device or system (i.e. a television device, an oven, a refrigerator, a vehicle, an industrial machine, a robot, a smartphone, and/or any other device or system capable of housing the elements needed for VSADO functionalities), work in combination with other devices or systems, or be available in any other configuration. In other embodiments, the disclosed artificially intelligent devices, systems, and/or methods for learning and/or using visual surrounding for autonomous device operation, or elements thereof, may include Alternative Memory 16 that provides instructions for implementing VSADO functionalities to one or more Processors 11. In further embodiments, the disclosed artificially intelligent devices, systems, and/or methods for learning and/or using visual surrounding for autonomous device operation, or elements thereof, can be implemented entirely or in part as a computer program and executed by one or more Processors 11. Such program can be implemented in one or more modules or units of a single or multiple computer programs. Such program may be able to attach to or interface with, inspect, and/or take control of another application program to implement VSADO functionalities. In further embodiments, the disclosed artificially intelligent devices, systems, and/or methods for learning and/or using visual surrounding for autonomous device operation, or elements thereof, can be implemented as a network, web, distributed, cloud, or other such application accessed on one or more remote computing devices

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(i.e. servers, cloud, etc.) via Network Interface 25, such remote computing devices including processing capabilities and instructions for implementing VSADO functionalities. In further embodiments, the disclosed artificially intelligent devices, systems, and/or methods for learning and/or using visual surrounding for autonomous device operation, or elements thereof, can be (1) attached to or interfaced with any computing device or application program, (2) included as a feature of an operating system, (3) built (i.e. hard coded, etc.) into any computing device or application program, and/or (4) available in any other configuration to provide its functionalities.

In yet other embodiments, the disclosed artificially intelligent devices, systems, and/or methods for learning and/or using visual surrounding for autonomous device operation, or elements thereof, can be implemented at least in part in a computer program such as Java application or program. Java provides a robust and flexible environment for application programs including flexible user interfaces, robust security, built-in network protocols, powerful application programming interfaces, database or DBMS connectivity and interfacing functionalities, file manipulation capabilities, support for networked applications, and/or other features or functionalities. Application programs based on Java can be portable across many devices, yet leverage each device's native capabilities. Java supports the feature sets of most smartphones and a broad range of connected devices while still fitting within their resource constraints. Various Java platforms include virtual machine features comprising a runtime environment for application programs. Java platforms provide a wide range of user-level functionalities that can be implemented in application programs such as displaying text and graphics, playing and recording audio content, displaying and recording visual content, communicating with another computing device, and/or other functionalities. It should be understood that the disclosed artificially intelligent devices, systems, and/or methods for learning and/or using visual surrounding for autonomous device operation, or elements thereof, are programming language, platform, and operating system independent. Examples of programming languages that can be used instead of or in addition to Java include C, C++, Cobol, Python, Java Script, Tcl, Visual Basic, Pascal, VB Script, Perl, PHP, Ruby, and/or other programming languages capable of implementing the functionalities described herein.

Where a reference to a specific file or file type is used herein, other files, file types, or formats can be substituted.

Where a reference to a data structure is used herein, it should be understood that any variety of data structures can be used such as, for example, array, list, linked list, doubly linked list, queue, tree, heap, graph, map, grid, matrix, multi-dimensional matrix, table, database, database management system (DBMS), file, neural network, and/or any other type or form of a data structure including a custom one. A data structure may include one or more fields or data fields that are part of or associated with the data structure. A field or data field may include a data, an object, a data structure, and/or any other element or a reference/pointer thereto. A data structure can be stored in one or more memories, files, or other repositories. A data structure and/or any elements thereof, when stored in a memory, file, or other repository, may be stored in a different arrangement than the arrangement of the data structure and/or any elements thereof. For example, a sequence of elements can be stored in an arrangement other than a sequence in a memory, file, or other repository.

Where a reference to a repository is used herein, it should be understood that a repository may be or include one or more files or file systems, one or more storage locations or structures, one or more storage systems, one or more data structures or objects, one or more memory locations or structures, and/or other storage, memory, or data arrangements.

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Where a reference to an interface is used herein, it should be understood that the interface comprises any hardware, device, system, program, method, and/or combination thereof that enable direct or operative coupling, connection, and/or interaction of the elements between which the interface is indicated. A line or arrow shown in the figures between any of the depicted elements comprises such interface. Examples of an interface include a direct connection, an operative connection, a wired connection (i.e. wire, cable, etc.), a wireless connection, a device, a network, a bus, a circuit, a firmware, a driver, a bridge, a program, a combination thereof, and/or others.

Where a reference to an element coupled or connected to another element is used herein, it should be understood that the element may be in communication or any other interactive relationship with the other element. Furthermore, an element coupled or connected to another element can be coupled or connected to any other element in alternate implementations. Terms coupled, connected, interfaced, or other such terms may be used interchangeably herein depending on context.

Where a reference to an element matching another element is used herein, it should be understood that the element may be equivalent or similar to the other element. Therefore, the term match or matching can refer to total equivalence or similarity depending on context.

Where a reference to a device is used herein, it should be understood that the device may include or be referred to as a system, and vice versa depending on context, since a device may include a system of elements and a system may be embodied in a device.

Where a mention of a function, method, routine, subroutine, or other such procedure is used herein, it should be understood that the function, method, routine, subroutine, or other such procedure comprises a call, reference, or pointer to the function, method, routine, subroutine, or other such procedure.

Where a mention of data, object, data structure, item, element, or thing is used herein, it should be understood that the data, object, data structure, item, element, or thing comprises a reference or pointer to the data, object, data structure, item, element, or thing.

The term collection of elements can refer to plurality of elements without implying that the collection is an element itself.

Referring to Fig. 2, an embodiment of Device 98 comprising Unit for Learning and/or Using Visual Surrounding for Autonomous Device Operation (VSADO Unit 100) is illustrated. Device 98 also comprises interconnected Processor 11, Human-machine Interface 23, Picture Capturing Apparatus 90, Memory 12, and Storage 27. Processor 11 includes or executes Application Program 18. VSADO Unit 100 comprises interconnected Artificial Intelligence Unit 110, Acquisition Interface 120, and Modification Interface 130. Other additional elements can be included as needed, or some of the disclosed ones can be excluded, or a combination thereof can be utilized in alternate embodiments.

In one example, the teaching presented by the disclosure can be implemented in a device or system for learning and/or using visual surrounding for autonomous device operation. The device or system may include a processor circuit (i.e. Processor 11, etc.) configured to execute instruction sets (i.e. Instruction Sets 526, etc.) for operating a device. The device or system may further include a memory unit (i.e. Memory 12, etc.) configured to store data. The device or system may further include a picture capturing apparatus (i.e. Picture Capturing Apparatus 90, etc.) configured to capture digital pictures (i.e. Digital Pictures 525, etc.). The device or system may further include an artificial intelligence unit (i.e. Artificial Intelligence Unit 110, etc.). The artificial intelligence unit may be

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configured to receive a first digital picture from the picture capturing apparatus. The artificial intelligence unit may also be configured to receive one or more instruction sets for operating the device from the processor circuit. The artificial intelligence unit may also be configured to learn the first digital picture correlated with the one or more instruction sets for operating the device. The artificial intelligence unit may also be configured to receive a new digital picture from the picture capturing apparatus. The artificial intelligence unit may also be configured to anticipate the one or more instruction sets for operating the device correlated with the first digital picture based on at least a partial match between the new digital picture and the first digital picture. The artificial intelligence unit may also be configured to cause the processor circuit to execute the one or more instruction sets for operating the device correlated with the first digital picture, the executing performed in response to the anticipating of the artificial intelligence unit, wherein the device performs one or more operations defined by the one or more instruction sets for operating the device correlated with the first digital picture, the one or more operations performed in response to the executing by the processor circuit. Any of the operations of the described elements can be performed repeatedly and/or in different orders in alternate embodiments. In some embodiments, a stream of digital pictures can be used instead of or in addition to any digital picture such as, for example, using a first stream of digital pictures instead of the first digital picture. In other embodiments, a logic circuit (i.e. Logic Circuit 250, etc.) may be used instead of the processor circuit. In such embodiments, the one or more instruction sets for operating the device may include or be substituted with one or more inputs into or one or more outputs from the logic circuit. In further embodiments, an actuator may be included instead of or in addition to the processor circuit. In such embodiments, the one or more instruction sets for operating the device may include or be substituted with one or more inputs into the actuator. Other additional elements can be included as needed, or some of the disclosed ones can be excluded, or a combination thereof can be utilized in alternate embodiments. The device or system for learning and/or using visual surrounding for autonomous device operation may include any actions or operations of any of the disclosed methods such as methods 6100, 6200, 6300, 6400, 6500, and/or 6600 (all later described).

Device 98 comprises any hardware, programs, or a combination thereof. Device 98 may include a system. Device 98 may include any features, functionalities, and embodiments of Computing Device 70, or elements thereof. Examples of Device 98 include a desktop or other computer, a smartphone or other mobile computer, a vehicle, an industrial machine, a toy, a robot, a microwave or other oven, and/or any other device or machine comprising processing capabilities. Such device or machine may be built for any function or purpose examples of which are described later.

User 50 (also referred to simply as user or other similar name or reference) comprises a human user or non-human user. A non-human User 50 includes any device, system, program, and/or other mechanism for operating or controlling Device 98 and/or elements thereof. In one example, User 50 may issue an operating direction to Application Program 18 responsive to which Application Program's 18 instructions or instruction sets may be executed by Processor 11 to perform a desired operation on Device 98. In another example, User 50 may issue an operating direction to Processor 11, Logic Circuit 250 (later described), and/or other processing element responsive to which Processor 11, Logic Circuit 250, and/or other processing element may implement logic to perform a desired operation on Device 98. User's 50 operating directions comprise any user inputted data (i.e. values, text, symbols, etc.), directions (i.e. move right, move up, move forward, copy an item, click on a link, etc.), instructions or instruction sets (i.e. manually inputted instructions or instruction sets, etc.), and/or other inputs or

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information. A non-human User 50 can utilize more suitable interfaces instead of, or in addition to, Human-machine Interface 23 and/or Display 21 for controlling Device 98 and/or elements thereof. Examples of such interfaces include an application programming interface (API), bridge (i.e. bridge between applications, devices, or systems, etc.), driver, socket, direct or operative connection, handle, function/routine/subroutine, and/or other interfaces.

In some embodiments, Processor 11, Logic Circuit 250, Application Program 18, and/or other processing element may control or affect an actuator (not shown). Actuator comprises the functionality for implementing movements, actions, behaviors, maneuvers, and/or other mechanical or physical operations. Device 98 may include one or more actuators to enable Device 98 to perform mechanical, physical, or other operations and/or to interact with its environment. For example, an actuator can be connected to or coupled to an element such as a wheel, arm, or other element to act upon the environment. Examples of an actuator include a motor, a linear motor, a servomotor, a hydraulic element, a pneumatic element, an electro-magnetic element, a spring element, and/or other actuators. Examples of types of actuators include a rotary actuator, a linear actuator, and/or other types of actuators. In other embodiments, Processor 11, Logic Circuit 250, Application Program 18, and/or other processing element may control or affect any other device or element instead of or in addition to an actuator.

Picture Capturing Apparatus 90 comprises the functionality for capturing one or more pictures, and/or other functionalities. As such, Picture Capturing Apparatus 90 can be used to capture pictures of Device's 98 surrounding. In some embodiments, Picture Capturing Apparatus 90 may be or comprises a motion picture camera that can capture streams of pictures (i.e. motion pictures, videos, etc.). In other embodiments, Picture Capturing Apparatus 90 may be or comprises a still picture camera that can capture still pictures (i.e. photographs, etc.). In further embodiments, Picture Capturing Apparatus 90 may be or comprises any other picture capturing apparatus. In general, Picture Capturing Apparatus 90 may capture any light (i.e. visible light, infrared light, ultraviolet light, x-ray light, etc.) across the electromagnetic spectrum onto a light-sensitive material. In one example, a digital Picture Capturing Apparatus 90 can utilize a charge coupled device (CCD), a CMOS sensor, and/or other electronic image sensor to capture digital pictures that can then be stored in a memory or storage, or transmitted to an element such as Artificial Intelligence Unit 110. In another example, analog Picture Capturing Apparatus 90 can utilize an analog-to-digital converter to produce digital pictures. In some embodiments, Picture Capturing Apparatus 90 can be built, embedded, or integrated in Device 98, VSADO Unit 100, and/or other disclosed element. In other embodiments, Picture Capturing Apparatus 90 can be an external Picture Capturing Apparatus 90 connected with Device 98, VSADO Unit 100, and/or other disclosed element. In further embodiments, Picture Capturing Apparatus 90 comprises Computing Device 70 or elements thereof. In general, Picture Capturing Apparatus 90 can be implemented in any suitable configuration to provide its functionalities. Picture Capturing Apparatus 90 may capture one or more Digital Pictures 525. Digital Picture 525 (also referred to simply as digital pictures, etc.) may include a collection of color encoded pixels or dots. Examples of file formats that can be utilized to store Digital Picture 525 include JPEG, GIF, TIFF, PNG, PDF, and/or other file formats. A stream of Digital Pictures 525 (i.e. motion picture, video, etc.) may include one or more Digital Pictures 525. Examples of file formats that can be utilized to store a stream of Digital Pictures 525 include MPEG, AVI, FLV, MOV, RM, SWF, WMV, DivX, and/or other file formats. In some aspects, Digital Picture 525 may include or be substituted with a stream of Digital Pictures 525, and vice versa. Therefore, the terms digital picture and stream of digital pictures may be used interchangeably herein depending on context. In some aspects, Device's 98 surrounding may include exterior of Device 98. In other

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aspects, Device's 98 surrounding may include interior of Device 98 in case of hollow Device 98, Device 98 comprising compartments or openings, and/or other variously shaped Device 98.

VSADO Unit 100 comprises any hardware, programs, or a combination thereof. VSADO Unit 100 comprises the functionality for learning the operation of Device 98 in various visual surroundings. VSADO Unit 100 comprises the functionality for structuring and/or storing this knowledge in a knowledgebase (i.e. neural network, graph, sequences, other repository, etc.). VSADO Unit 100 comprises the functionality for enabling autonomous operation of Device 98 in various visual surroundings. VSADO Unit 100 comprises the functionality for interfacing with or attaching to Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element. VSADO Unit 100 comprises the functionality for obtaining instruction sets, data, and/or other information used, implemented, and/or executed by Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element. VSADO Unit 100 comprises the functionality for modifying instruction sets, data, and/or other information used, implemented, and/or executed by Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element. VSADO Unit 100 comprises learning, anticipating, decision making, automation, and/or other functionalities disclosed herein. Statistical, artificial intelligence, machine learning, and/or other models or techniques are utilized to implement the disclosed devices, systems, and methods.

When the disclosed VSADO Unit 100 functionalities are applied on Application Program 18, Processor 11, Logic Circuit 250 (later described), and/or other processing element of Device 98, Device 98 may become autonomous. VSADO Unit 100 may take control from, share control with, and/or release control to Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element to implement autonomous operation of Device 98. VSADO Unit 100 may take control from, share control with, and/or release control to Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element automatically or after prompting User 50 to allow it. In some aspects, Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element of an autonomous Device 98 may include or be provided with anticipatory instructions or instruction sets that User 50 did not issue or cause to be executed. Such anticipatory instructions or instruction sets include instruction sets that User 50 may want or is likely to issue or cause to be executed. Anticipatory instructions or instruction sets can be generated by VSADO Unit 100 or elements thereof based on the visual surrounding of Device 98. As such, Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element of an autonomous Device 98 may include or be provided with some or all original instructions or instruction sets and/or any anticipatory instructions or instruction sets generated by VSADO Unit 100. Therefore, autonomous Device 98 operating may include executing some or all original instructions or instruction sets and/or any anticipatory instructions or instruction sets generated by VSADO Unit 100. In one example, VSADO Unit 100 can overwrite or rewrite the original instructions or instruction sets of Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element with VSADO Unit 100-generated instructions or instruction sets. In another example, VSADO Unit 100 can insert or embed VSADO Unit 100-generated instructions or instruction sets among the original instructions or instruction sets of Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element. In a further example, VSADO Unit 100 can branch, redirect, or jump to VSADO Unit 100-generated instructions or instruction sets from the original instructions or instruction sets of Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element.

In some embodiments, autonomous Device 98 operating comprises determining, by VSADO Unit 100, a

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next instruction or instruction set to be executed based on Device's 98 visual surrounding prior to the user issuing or causing to be executed the next instruction or instruction set. In yet other embodiments, autonomous application operating comprises determining, by VSADO Unit 100, a next instruction or instruction set to be executed based on Device's 98 visual surrounding prior to the system receiving the next instruction or instruction set.

In some embodiments, autonomous Device 98 operating includes a partially or fully autonomous operating. In an example involving partially autonomous Device 98 operating, a user confirms VSADO Unit 100-generated instructions or instruction sets prior to their execution. In an example involving fully autonomous application operating, VSADO Unit 100-generated instructions or instruction sets are executed without user or other system confirmation (i.e. automatically, etc.).

In some embodiments, a combination of VSADO Unit 100 and other systems and/or techniques can be utilized to implement Device's 98 operation. In one example, VSADO Unit 100 may be a primary or preferred system for implementing Device's 98 operation. While operating autonomously under the control of VSADO Unit 100, Device 98 may encounter a visual surrounding that has not been encountered or learned before. In such situations, User 50 and/or non-VSADO system may take control of Device's 98 operation. VSADO Unit 100 may take control again when Device 98 encounters a previously learned visual surrounding. Naturally, VSADO Unit 100 can learn Device's 98 operation in visual surroundings while User 50 and/or non-VSADO system is in control of Device 98, thereby reducing or eliminating the need for future involvement of User 50 and/or non-VSADO system. In another example, User 50 and/or non-VSADO system may be a primary or preferred system for control of Device's 98 operation. While operating under the control of User 50 and/or non-VSADO system, User 50 and/or non-VSADO system may release control to VSADO Unit 100 for any reason (i.e. User 50 gets tired or distracted, non-VSADO system gets stuck or cannot make a decision, etc.), at which point Device 98 can be controlled by VSADO Unit 100. In some designs, VSADO Unit 100 may take control in certain special visual surroundings where VSADO Unit 100 may offer superior performance even though User 50 and/or non-VSADO system may generally be preferred. Once Device 98 leaves such special visual surrounding, VSADO Unit 100 may release control to User 50 and/or a non-VSADO system. In general, VSADO Unit 100 can take control from, share control with, or release control to User 50, non-VSADO system, and/or other system or process at any time, under any circumstances, and remain in control for any period of time as needed.

In some embodiments, VSADO Unit 100 may control one or more sub-devices, sub-systems, or elements of Device 98 while User 50 and/or non-VSADO system may control other one or more sub-devices, sub-systems, or elements of Device 98.

It should be understood that a reference to autonomous operating of Device 98 may include autonomous operating of Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element depending on context.

Acquisition Interface 120 comprises the functionality for obtaining or receiving instruction sets, data, and/or other information. Acquisition Interface 120 comprises the functionality for obtaining or receiving instruction sets, data, and/or other information from Processor 11, Application Program 18, Logic Circuit 250 (later described), and/or other processing element. Acquisition Interface 120 comprises the functionality for obtaining or receiving instruction sets, data, and/or other information at runtime. In some aspects, an instruction set may include any computer command, instruction, signal, or input used in Application Program 18, Processor 11, Logic Circuit 250, and/or other

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processing element. Therefore, the terms instruction set, command, instruction, signal, input, or other such terms may be used interchangeably herein depending on context. Acquisition Interface 120 also comprises the functionality for attaching to or interfacing with Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element. In one example, Acquisition Interface 120 comprises the functionality to access and/or read runtime engine/environment, virtual machine, operating system, compiler, just-in-time (JIT) compiler, interpreter, translator, execution stack, file, object, data structure, and/or other computing system elements. In another example, Acquisition Interface 120 comprises the functionality to access and/or read memory, storage, bus, interfaces, and/or other computing system elements. In a further example, Acquisition Interface 120 comprises the functionality to access and/or read Processor 11 registers and/or other Processor 11 elements. In a further example, Acquisition Interface 120 comprises the functionality to access and/or read inputs and/or outputs of Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element. In a further example, Acquisition Interface 120 comprises the functionality to access and/or read functions, methods, procedures, routines, subroutines, and/or other elements of Application Program 18. In a further example, Acquisition Interface 120 comprises the functionality to access and/or read source code, bytecode, compiled, interpreted, or otherwise translated code, machine code, and/or other code. In a further example, Acquisition Interface 120 comprises the functionality to access and/or read values, variables, parameters, and/or other data or information. Acquisition Interface 120 also comprises the functionality for transmitting the obtained instruction sets, data, and/or other information to Artificial Intelligence Unit 110 and/or other element. As such, Acquisition Interface 120 provides input into Artificial Intelligence Unit 110 for knowledge structuring, anticipating, decision making, and/or other functionalities later in the process. Acquisition Interface 120 also comprises other disclosed functionalities.

Acquisition Interface 120 can employ various techniques for obtaining instruction sets, data, and/or other information. In one example, Acquisition Interface 120 can attach to and/or obtain Processor's 11, Application Program's 18, Logic Circuit's 250, and/or other processing element's instruction sets, data, and/or other information through tracing or profiling techniques. Tracing or profiling may be used for outputting Processor's 11, Application Program's 18, Logic Circuit's 250, and/or other processing element's instruction sets, data, and/or other information at runtime. For instance, tracing or profiling may include adding trace code (i.e. instrumentation, etc.) to an application and/or outputting trace information to a specific target. The outputted trace information (i.e. instruction sets, data, and/or other information, etc.) can then be provided to or recorded into a file, data structure, repository, an application, and/or other system or target that may receive such trace information. As such, Acquisition Interface 120 can utilize tracing or profiling to obtain instruction sets, data, and/or other information and provide them as input into Artificial Intelligence Unit 110. In some aspects, instrumentation can be performed in source code, bytecode, compiled, interpreted, or otherwise translated code, machine code, and/or other code. In other aspects, instrumentation can be performed in various elements of a computing system such as memory, virtual machine, runtime engine/environment, operating system, compiler, interpreter, translator, processor registers, execution stack, program counter, and/or other elements. In yet other aspects, instrumentation can be performed in various abstraction layers of a computing system such as in software layer (i.e. Application Program 18, etc.), in virtual machine (if VM is used), in operating system, in Processor 11, and/or in other layers or areas that may exist in a particular computing system implementation. In yet other aspects, instrumentation can be performed at various time periods in an application's execution such as source code write time, compile time, interpretation time, translation

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time, linking time, loading time, runtime, and/or other time periods. In yet other aspects, instrumentation can be performed at various granularities or code segments such as some or all lines of code, some or all statements, some or all instructions or instruction sets, some or all basic blocks, some or all functions/routines/subroutines, and/or some or all other code segments.

In some embodiments, Application Program 18 can be automatically instrumented. In one example, Acquisition Interface 120 can access Application Program's 18 source code, bytecode, or machine code and select instrumentation points of interest. Selecting instrumentation points may include finding locations in the source code, bytecode, or machine code corresponding to function calls, function entries, function exits, object creations, object destructions, event handler calls, new lines (i.e. to instrument all lines of code, etc.), thread creations, throws, and/or other points of interest. Instrumentation code can then be inserted at the instrumentation points of interest to output Application Program's 18 instruction sets, data, and/or other information. In response to executing instrumentation code, Application Program's 18 instruction sets, data, and/or other information may be received by Acquisition Interface 120. In some aspects, Application Program's 18 source code, bytecode, or machine code can be dynamically instrumented. For example, instrumentation code can be dynamically inserted into Application Program 18 at runtime.

In other embodiments, Application Program 18 can be manually instrumented. In one example, a programmer can instrument a function call by placing an instrumenting instruction immediately after the function call as in the following example.

```
Object1.moveRight(73);
traceApplication('Object1.moveRight(73);');
```

In another example, an instrumenting instruction can be placed immediately before the function call, or at the beginning, end, or anywhere within the function itself. A programmer may instrument all function calls or only function calls of interest. In a further example, a programmer can instrument all lines of code or only code lines of interest. In a further example, a programmer can instrument other elements utilized or implemented within Application Program 18 such as objects and/or any of their functions, data structures and/or any of their functions, event handlers and/or any of their functions, threads and/or any of their functions, and/or other elements or functions. Similar instrumentation as in the preceding examples can be performed automatically or dynamically. In some designs where manual code instrumentation is utilized, Acquisition Interface 120 can optionally be omitted and Application Program's 18 instruction sets, data, and/or other information may be transmitted directly to Artificial Intelligence Unit 110.

In some embodiments, VSADO Unit 100 can be selective in learning instruction sets, data, and/or other information to those implemented, utilized, or related to an object, data structure, repository, thread, function, and/or other element of Application Program 18. In some aspects, Acquisition Interface 120 can obtain Application Program's 18 instruction sets, data, and/or other information implemented, utilized, or related to a certain object in an object oriented Application Program 18.

In some embodiments, various computing systems and/or platforms may provide native tools for obtaining instruction sets, data, and/or other information. Also, independent vendors may provide portable tools with similar functionalities that can be utilized across different computing systems and/or platforms. These native and portable tools may provide a wide range of functionalities to obtain runtime and other information such as instrumentation,

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tracing or profiling, logging application or system messages, outputting custom text messages, outputting objects or data structures, outputting functions/routines/subroutines or their invocations, outputting variable or parameter values, outputting thread or process behaviors, outputting call or other stacks, outputting processor registers, providing runtime memory access, providing inputs and/or outputs, performing live application monitoring, and/or other capabilities. One of ordinary skill in art will understand that, while all possible variations of the techniques to obtain instruction sets, data, and/or other information are too voluminous to describe, these techniques are within the scope of this disclosure.

In one example, obtaining instruction sets, data, and/or other information can be implemented through the .NET platform's native tools for application tracing or profiling such as System.Diagnostics.Trace, System.Diagnostics.Debug, and System.Diagnostics.TraceSource classes for tracing execution flow, and System.Diagnostics.Process, System.Diagnostics.EventLog, and System.Diagnostics.PerformanceCounter classes for profiling code, accessing local and remote processes, starting and stopping system processes, and interacting with Windows event logs, etc. For instance, a set of trace switches can be created that output an application's information. The switches can be configured using the .config file. For a Web application, this may typically be Web.config file associated with the project. In a Windows application, this file may typically be named applicationName.exe.config. Trace code can be added to application code automatically or manually as previously described. Appropriate listener can be created where the trace output is received. Trace code may output trace messages to a specific target such as a file, a log, a database, an object, a data structure, and/or other repository or system. Acquisition Interface 120 or Artificial Intelligence Unit 110 can then read or obtain the trace information from these targets. In some aspects, trace code may output trace messages directly to Acquisition Interface 120. In other aspects, trace code may output trace messages directly to Artificial Intelligence Unit 110. In the case of outputting trace messages to Acquisition Interface 120 or directly to Artificial Intelligence Unit 110, custom listeners can be built to accommodate these specific targets. Other platforms, tools, and/or techniques can provide equivalent or similar functionalities as the above described ones.

In another example, obtaining instruction sets, data, and/or other information can be implemented through the .NET platform's Profiling API that can be used to create a custom profiler application for tracing, monitoring, interfacing with, and/or managing a profiled application. The Profiling API provides an interface that includes methods to notify the profiler of events in the profiled application. The Profiling API may also provide an interface to enable the profiler to call back into the profiled application to obtain information about the state of the profiled application. The Profiling API may further provide call stack profiling functionalities. Call stack (also referred to as execution stack, control stack, runtime stack, machine stack, the stack, etc.) includes a data structure that can store information about active subroutines of an application. The Profiling API may provide a stack snapshot method, which enables a trace of the stack at a particular point in time. The Profiling API may also provide a shadow stack method, which tracks the call stack at every instant. A shadow stack can obtain function arguments, return values, and information about generic instantiations. A function such as FunctionEnter can be utilized to notify the profiler that control is being passed to a function and can provide information about the stack frame and function arguments. A function such as FunctionLeave can be utilized to notify the profiler that a function is about to return to the caller and can provide information about the stack frame and function return value. An alternative to call stack profiling includes call stack sampling in which the profiler can periodically examine the stack. In some aspects, the Profiling

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API enables the profiler to change the in-memory code stream for a routine before it is just-in-time (JIT) compiled where the profiler can dynamically add instrumentation code to all or particular routines of interest. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, obtaining instruction sets, data, and/or other information can be implemented through Java platform's APIs for application tracing or profiling such as Java Virtual Machine Profiling Interface (JVMPI), Java Virtual Machine Tool Interface (JVMTI), and/or other APIs or tools. These APIs can be used for instrumentation of an application, for notification of Java Virtual Machine (VM) events, and/or other functionalities. One of the tracing or profiling techniques that can be utilized includes bytecode instrumentation. The profiler can insert bytecodes into all or some of the classes. In application execution profiling, for example, these bytecodes may include methodEntry and methodExit calls. In memory profiling, for example, the bytecodes may be inserted after each new or after each constructor. In some aspects, insertion of instrumentation bytecode can be performed either by a post-compiler or a custom class loader. An alternative to bytecode instrumentation includes monitoring events generated by the JVMPI or JVMTI interfaces. Both APIs can generate events for method entry/exit, object allocation, and/or other events. In some aspects, JVMTI can be utilized for dynamic bytecode instrumentation where insertion of instrumentation bytecodes is performed at runtime. The profiler may insert the necessary instrumentation when a selected class is invoked in an application. This can be accomplished using the JVMTI's redefineClasses method, for example. This approach also enables changing of the level of profiling as the application is running. If needed, these changes can be made adaptively without restarting the application. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, obtaining instruction sets, data, and/or other information can be implemented through JVMTI's programming interface that enables creation of software agents that can monitor and control a Java application. An agent may use the functionality of the interface to register for notification of events as they occur in the application, and to query and control the application. A JVMTI agent may use JVMTI functions to extract information from a Java application. A JVMTI agent can be utilized to obtain an application's runtime information such as method calls, memory allocation, CPU utilization, lock contention, and/or other information. JVMTI may include functions to obtain information about variables, fields, methods, classes, and/or other information. JVMTI may also provide notification for numerous events such as method entry and exit, exception, field access and modification, thread start and end, and/or other events. Examples of JVMTI built-in methods include GetMethodName to obtain the name of an invoked method, GetThreadInfo to obtain information for a specific thread, GetClassSignature to obtain information about the class of an object, GetStackTrace to obtain information about the stack including information about stack frames, and/or other methods. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, obtaining instruction sets, data, and/or other information can be implemented through java.lang.Runtime class that provides an interface for application tracing or profiling. Examples of methods provided in java.lang.Runtime that can be used to obtain an application's instruction sets, data, and/or other information include tracemethodcalls, traceinstructions, and/or other methods. These methods prompt the Java Virtual Machine to output trace information for a method or instruction in the virtual machine as it is executed. The destination of trace output may be system dependent and include a file, a listener, and/or other destinations where Acquisition Interface 120, Artificial Intelligence Unit 110, and/or other disclosed elements can access needed information. In

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addition to tracing or profiling tools native to their respective computing systems and/or platforms, many independent tools exist that provide tracing or profiling functionalities on more than one computing system and/or platform. Examples of these tools include Pin, DynamoRIO, KernInst, DynInst, Kprobes, OpenPAT, DTrace, SystemTap, and/or others. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, obtaining instruction sets, data, and/or other information can be implemented through logging tools of the platform and/or operating system on which an application runs. Some logging tools may include nearly full feature sets of the tracing or profiling tools previously described. In one example, Visual Basic enables logging of runtime messages through its Microsoft.VisualBasic.Logging namespace that provides a log listener where the log listener may direct logging output to a file and/or other target. In another example, Java enables logging through its java.util.logging class. In some aspects, obtaining an application's instruction sets, data, and/or other information can be implemented through logging capabilities of the operating system on which an application runs. For example, Windows NT features centralized log service that applications and operating-system components can utilize to report their events including any messages. Windows NT provides functionalities for system, application, security, and/or other logging. An application log may include events logged by applications. Windows NT, for example, may include support for defining an event source (i.e. application that created the event, etc.). Windows Vista, for example, supports a structured XML log-format and designated log types to allow applications to more precisely log events and to help interpret the events. Examples of different types of event logs include administrative, operational, analytic, debug, and/or other log types including any of their subcategories. Examples of event attributes that can be utilized include eventID, level, task, opcode, keywords, and/or other event attributes. Windows wevtutil tool enables access to events, their structures, registered event publishers, and/or their configuration even before the events are fired. Wevtutil supports capabilities such as retrieval of the names of all logs on a computing device; retrieval of configuration information for a specific log; retrieval of event publishers on a computing device; reading events from an event log, from a log file, or using a structured query; exporting events from an event log, from a log file, or using a structured query to a specific target; and/or other capabilities. Operating system logs can be utilized solely if they contain sufficient information on an application's instruction sets, data, and/or other information. Alternatively, operating system logs can be utilized in combination with another source of information (i.e. trace information, call stack, processor registers, memory, etc.) to reconstruct the application's instruction sets, data, and/or other information needed for Artificial Intelligence Unit 110 and/or other elements. In addition to logging capabilities native to their respective platforms and/or operating systems, many independent tools exist that provide logging on different platforms and/or operating systems. Examples of these tools include Log4j, Logback, SmartInspect, NLog, log4net, Microsoft Enterprise Library, ObjectGuy Framework, and/or others. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, obtaining instruction sets, data, and/or other information can be implemented through tracing or profiling the operating system on which an application runs. As in tracing or profiling an application, one of the techniques that can be utilized includes adding instrumentation code to the operating system's source code. Such instrumentation code can be added to the operating system's source code before kernel compilation or recompilation, for instance. This type of instrumentation may involve defining or finding locations in the operating

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system's source code where instrumentation code may be inserted. Kernel instrumentation can also be performed without the need for kernel recompilation or rebooting. In some aspects, instrumentation code can be added at locations of interest through binary rewriting of compiled kernel code. In other aspects, kernel instrumentation can be performed dynamically where instrumentation code is added and/or removed where needed at runtime. For instance, dynamic instrumentation may overwrite kernel code with a branch instruction that redirects execution to instrumentation code or instrumentation routine. In yet other aspects, kernel instrumentation can be performed using just-in-time (JIT) dynamic instrumentation where execution may be redirected to a copy of kernel's code segment that includes instrumentation code. This type of instrumentation may include a JIT compiler and creation of a copy of the original code segment having instrumentation code or calls to instrumentation routines embedded into the original code segment. Instrumentation of the operating system may enable total system visibility including visibility into an application's behavior by enabling generation of low level trace information. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, obtaining instruction sets, data, and/or other information can be implemented through tracing or profiling the processor on which an application runs. For example, some Intel processors provide Intel Processor Trace (i.e. Intel PT, etc.), a low-level tracing feature that enables recording executed instruction sets, and/or other data or information of one or more applications. Intel PT is facilitated by the Processor Trace Decoder Library along with its related tools. Intel PT is a low-overhead execution tracing feature that records information about application execution on each hardware thread using dedicated hardware facilities. The recorded execution/trace information is collected in data packets that can be buffered internally before being sent to a memory subsystem or another system or element (i.e. Acquisition Interface 120, Artificial Intelligence Unit 110, etc.). Intel PT also enables navigating the recorded execution/trace information via reverse stepping commands. Intel PT can be included in an operating system's core files and provided as a feature of the operating system. Intel PT can trace globally some or all applications running on an operating system. Acquisition Interface 120 or Artificial Intelligence Unit 110 can read or obtain the recorded execution/trace information from Intel PT. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, obtaining instruction sets, data, and/or other information can be implemented through branch tracing or profiling. Branch tracing may include an abbreviated instruction trace in which only the successful branch instruction sets are traced or recorded. Branch tracing can be implemented through utilizing dedicated processor commands, for example. Executed branches may be saved into special branch trace store area of memory. With the availability and reference to a compiler listing of the application together with branch trace information, a full path of executed instruction sets can be reconstructed. The full path can also be reconstructed with a memory dump (containing the program storage) and branch trace information. In some aspects, branch tracing can be utilized for pre-learning or automated learning of an application's instruction sets, data, and/or other information where a number of application simulations (i.e. simulations of likely/common operations, etc.) are performed. As such, the application's operation can be learned automatically saving the time that would be needed to learn the application's operation directed by a user. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, obtaining instruction sets, data, and/or other information can be implemented through assembly language. Assembly language is a low-level programming language for a computer or other

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programmable device in which there is a strong correlation between the language and the architecture's machine instruction sets. Syntax, addressing modes, operands, and/or other elements of an assembly language instruction set may translate directly into numeric (i.e. binary, etc.) representations of that particular instruction set. Because of this direct relationship with the architecture's machine instruction sets, assembly language can be a powerful tool for tracing or profiling an application's execution in processor registers, memory, and/or other computing system components. For example, using assembly language, memory locations of a loaded application can be accessed, instrumented, and/or otherwise manipulated. In some aspects, assembly language can be used to rewrite or overwrite original in-memory instruction sets of an application with instrumentation instruction sets. In other aspects, assembly language can be used to redirect application's execution to instrumentation routine/subroutine or other code segment elsewhere in memory by inserting a jump into the application's in-memory code, by redirecting program counter, or by other techniques. Some operating systems may implement protection from changes to applications loaded into memory. Operating system, processor, or other low level commands such as Linux mprotect command or similar commands in other operating systems may be used to unprotect the protected locations in memory before the change. In yet other aspects, assembly language can be used to obtain instruction sets, data, and/or other information through accessing and/or reading instruction register, program counter, other processor registers, memory locations, and/or other components of a computing system. In yet other aspects, high-level programming languages may call or execute an external assembly language program to facilitate obtaining instruction sets, data, and/or other information as previously described. In yet other aspects, relatively low-level programming languages such as C may allow embedding assembly language directly in their source code such as, for example, using asm keyword of C. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, it may be sufficient to obtain user or other inputs, variables, parameters, and/or other data in some procedural, simple object oriented, or other applications. In one instance, a simple procedural application executes a sequence of instruction sets until the end of the program. During its execution, the application may receive user or other input, store the input in a variable, and perform calculations using the variable to reach a result. The value of the variable can be obtained or traced. In another instance, a more complex procedural application comprises one or more functions/routines/subroutines each of which may include a sequence of instruction sets. The application may execute a main sequence of instruction sets with a branch to a function/routine/subroutine. During its execution, the application may receive user or other input, store the input in a variable, and pass the variable as a parameter to the function/routine/subroutine. The function/routine/subroutine may perform calculations using the parameter and return a value that the rest of the application can use to reach a result. The value of the variable or parameter passed to the function/routine/subroutine, and/or return value can be obtained or traced. Values of user or other inputs, variables, parameters, and/or other items of interest can be obtained through previously described tracing, instrumentation, and/or other techniques. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

Referring to Fig. 3, in yet another example, obtaining instruction sets, data, and/or other information may be implemented through tracing, profiling, or sampling of instruction sets or data in processor registers, memory, or other computing system components where instruction sets, data, and/or other information may be stored or utilized. For example, Instruction Register 212 may be part of Processor 11 and it may store the instruction set currently

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being executed or decoded. In some processors, Program Counter 211 (also referred to as instruction pointer, instruction address register, instruction counter, or part of instruction sequencer) may be incremented after fetching an instruction set, and it may hold or point to the memory address of the next instruction set to be executed. In a processor where the incrementation precedes the fetch, Program Counter 211 may point to the current instruction set being executed. In the instruction cycle, an instruction set may be loaded into Instruction Register 212 after Processor 11 fetches it from location in Memory 12 pointed to by Program Counter 211. Instruction Register 212 may hold the instruction set while it is decoded by Instruction Decoder 213, prepared, and executed. In some aspects, data (i.e. operands, etc.) needed for instruction set execution may be loaded from Memory 12 into a register within Register Array 214. In other aspects, the data may be loaded directly into Arithmetic Logic Unit 215. For instance, as instruction sets pass through Instruction Register 212 during application execution, they may be transmitted to Acquisition Interface 120 as shown. Examples of the steps in execution of a machine instruction set may include decoding the opcode (i.e. portion of a machine instruction set that may specify the operation to be performed), determining where the operands may be located (depending on architecture, operands may be in registers, the stack, memory, I/O ports, etc.), retrieving the operands, allocating processor resources to execute the instruction set (needed in some types of processors), performing the operation indicated by the instruction set, saving the results of execution, and/or other execution steps. Examples of the types of machine instruction sets that can be utilized include arithmetic, data handling, logical, program control, as well as special and/or other instruction set types. In addition to the ones described or shown, examples of other computing system or processor components that can be used during an instruction cycle include memory address register (MAR) that may hold the address of a memory block to be read from or written to; memory data register (MDR) that may hold data fetched from memory or data waiting to be stored in memory; data registers that may hold numeric values, characters, small bit arrays, or other data; address registers that may hold addresses used by instruction sets that indirectly access memory; general purpose registers (GPRs) that may store both data and addresses; conditional registers that may hold truth values often used to determine whether some instruction set should or should not be executed; floating point registers (FPRs) that may store floating point numbers; constant registers that may hold read-only values such as zero, one, or pi; special purpose registers (SPRs) such as status register, program counter, or stack pointer that may hold information on program state; machine-specific registers that may store data and settings related to a particular processor; Register Array 214 that may include an array of any number of processor registers; Arithmetic Logic Unit 215 that may perform arithmetic and logic operations; control unit that may direct processor's operation; and/or other circuits or components. Tracing, profiling, or sampling of processor registers, memory, or other computing system components can be implemented in a program, combination of hardware and program, or purely hardware system. Dedicated hardware may be built to perform tracing, profiling, or sampling of processor registers or any computing system components with marginal or no impact to computing overhead.

One of ordinary skill in art will recognize that Fig. 3 depicts one of many implementations of processor or computing system components, and that various additional components can be included, or some of the disclosed ones can be excluded, or a combination thereof can be utilized in alternate implementations. Processor or computing system components may be arranged or connected differently in alternate implementations. Processor or computing system components may also be connected with external elements using various connections. For instance, the connection between Instruction Register 212 and Acquisition Interface 120 may include any number or

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types of connections such as, for example, a dedicated connection for each bit of Instruction Register 212 (i.e. 32 connections for a 32 bit Instruction Register 212, etc.). Any of the described or other connections or interfaces may be implemented among any processor or computing system components and Acquisition Interface 120 or other elements.

Referring to Figs. 4A-4B, in yet another example, obtaining instruction sets, data, and/or other information may be implemented through tracing, profiling, or sampling of Logic Circuit 250. While Processor 11 includes any type or embodiment of logic circuit, Logic Circuit 250 is described separately here to offer additional detail on its functioning. Some Devices 98 may not need the processing capabilities of an entire Processor 11, but instead a more tailored Logic Circuit 250. Examples of such Devices 98 include home appliances, audio or video electronics, vehicle systems, toys, industrial machines, robots, and/or others. Logic Circuit 250 comprises the functionality for performing logic operations. Logic Circuit 250 comprises the functionality for performing logic operations using the circuit's inputs and producing outputs based on the logic operations performed on the inputs. Logic Circuit 250 may generally be implemented using transistors, diodes, and/or other electronic switches, but can also be constructed using vacuum tubes, electromagnetic relays (relay logic), fluidic logic, pneumatic logic, optics, molecules, or even mechanical elements. In some aspects, Logic Circuit 250 may be or include a microcontroller, field-programmable gate array (FPGA), application-specific integrated circuit (ASIC), and/or other computing circuit or device. In other aspects, Logic Circuit 250 may be or include any circuit or device comprising one or more logic gates, one or more transistors, one or more switches, and/or one or more other logic components. In further aspects, Logic Circuit 250 may be or include any integrated or other circuit or device that can perform logic operations. Logic may generally refer to Boolean logic utilized in binary operations, but other logics can also be used. Input into Logic Circuit 250 may include or refer to a value inputted into the Logic Circuit 250, therefore, these terms may be used interchangeably herein depending on context. In one example, Logic Circuit 250 may perform some logic operations using four input values and produce two output values. As the four input values are delivered to or received by Logic Circuit 250, they may be obtained by Acquisition Interface 120 through the four hardwired connections as shown in Fig. 4A. In another example, Logic Circuit 250 may perform some logic operations using four input values and produce two output values. As the two output values are generated by or transmitted out of Logic Circuit 250, they may be obtained by Acquisition Interface 120 through the two hardwired connections as shown in Fig. 4B. In a further example, instead of or in addition to obtaining input and/or output values of Logic Circuit 250, the state of Logic Circuit 250 may be obtained by reading or accessing values from one or more Logic Circuit's 250 internal components such as registers, memories, buses, and/or others (i.e. similar to the previously described tracing, profiling, and/or sampling of Processor 11 components, etc.). Tracing, profiling, or sampling of Logic Circuit 250 can be implemented in a program, combination of hardware and program, or purely hardware system. Dedicated hardware may be built to perform tracing, profiling or sampling of Logic Circuit 250 with marginal or no impact to computing overhead. Any of the elements and/or techniques for tracing, profiling, or sampling of Logic Circuit 250 can similarly be implemented with Processor 11 and/or other processing elements. In some designs, VSADO Unit 100 may include clamps and/or other elements to attach VSADO Unit 100 to inputs (i.e. input wires, etc.) into and/or outputs (i.e. output wires, etc.) from Logic Circuit 250. Such clamps and/or attachment elements enable seamless attachment of VSADO Unit 100 to any circuit or computing device without the need to redesign or alter the circuit or computing device.

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In some embodiments, VSADO Unit 100 may learn input values directly from an actuator (previously described, not shown). For example, Logic Circuit 250 or other processing element may control an actuator that enables Device 98 to perform mechanical, physical, and/or other operations. An actuator may receive one or more input values or control signals from Logic Circuit 250 or other processing element directing the actuator to perform specific operations. As one or more input values or control signals are delivered to or received by the actuator, they may be obtained by Acquisition Interface 120 as previously described with respect to obtaining input values of Logic Circuit 250. Specifically, for instance, one or more input values or control signals of an actuator may be obtained by Acquisition Interface 120 via hardwired or other connections.

One of ordinary skill in art will recognize that Figs. 4A-4B depict one of many implementations of Logic Circuit 250 and that any number of input and/or output values can be utilized in alternate implementations. One of ordinary skill in art will also recognize that Logic Circuit 250 may include any number and/or combination of logic components to implement any logic operations.

Other additional techniques or elements may be utilized as needed for obtaining instruction sets, data, and/or other information, or some of the disclosed techniques or elements may be excluded, or a combination thereof may be utilized in alternate embodiments.

Referring to Figs. 5A-5C, some embodiments of Instruction Sets 526 are illustrated. In some aspects, Instruction Set 526 includes one or more instructions or commands of Application Program 18. For example, Instruction Set 526 may include one or more instructions or commands of a high-level programming language such as Java or SQL, a low-level language such as assembly or machine language, an intermediate language or construct such as bytecode, and/or any other language or construct. In other aspects, Instruction Set 526 includes one or more inputs into and/or outputs from Logic Circuit 250, Processor 11, Application Program 18, and/or other processing element. In further aspects, Instruction Set 526 includes one or more values or states of registers and/or other components of Logic Circuit 250, Processor 11, and/or other processing element. In general, Instruction Set 526 may include one or more instructions, commands, keywords, symbols (i.e. parentheses, brackets, commas, semicolons, etc.), operators (i.e. =, <, >, etc.), variables, values, objects, data structures, functions (i.e. Function1(), FIRST(), MIN(), SQRT(), etc.), parameters, states, signals, inputs, outputs, characters, digits, references thereto, and/or other components for performing an operation.

In an embodiment shown in Fig. 5A, Instruction Set 526 includes code of a high-level programming language (i.e. Java, C++, etc.) comprising the following function call construct: Function1 (Parameter1, Parameter2, Parameter3, ...). An example of a function call applying the above construct includes the following Instruction Set 526: moveTo(Object1, 29, 17). The function or reference thereto “moveTo(Object1, 29, 17)” may be an Instruction Set 526 directing Object1 to move to a location with coordinates 29 and 17, for example. In another embodiment shown in Fig. 5B, Instruction Set 526 includes structured query language (SQL). In a further embodiment shown in Fig. 5C, Instruction Set 526 includes bytecode (i.e. Java bytecode, Python bytecode, CLR bytecode, etc.). In a further embodiment shown in Fig. 5D, Instruction Set 526 includes assembly code. In a further embodiment shown in Fig. 5E, Instruction Set 526 includes machine code.

Referring to Figs. 6A-6B, some embodiments of Extra Information 527 (also referred to as Extra Info 527) are illustrated. In an embodiment shown in Fig. 6A, Digital Picture 525 may include or be associated with Extra Info 527. In an embodiment shown in Fig. 6B, Instruction Set 526 may include or be associated with Extra Info 527.

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Extra Info 527 comprises the functionality for storing any information useful in comparisons or decision making performed in autonomous device operation, and/or other functionalities. One or more Extra Infos 527 can be stored in, appended to, or associated with a Digital Picture 525, Instruction Set 526, and/or other element. In some embodiments, the system can obtain Extra Info 527 at a time of capturing or receiving of Digital Picture 525. In other embodiments, the system can obtain Extra Info 527 at a time of acquisition of Instruction Set 526. In general, the system or any element thereof can obtain Extra Info 527 at any time. Examples of Extra Info 527 include time information, location information, computed information, observed information, sensory information, contextual information, and/or other information. Any information can be utilized that can provide information for enhanced comparisons or decision making performed in autonomous device operation. Which information is utilized and/or stored in Extra Info 527 can be set by a user, by VSADO system administrator, or automatically by the system. Extra Info 527 may include or be referred to as contextual information, and vice versa. Therefore, these terms may be used interchangeably herein depending on context.

In some aspects, time information (i.e. time stamp, etc.) can be utilized and/or stored in Extra Info 527. Time information can be useful in comparisons or decision making performed in autonomous device operation related to a specific time period as Device 98 may be required to perform specific operations at certain parts of day, month, year, and/or other time periods. Time information can be obtained from the system clock, online clock, oscillator, or other time source. In one example, a thermostat device may be directed to turn heat on in the morning and/or turn heat off during the day. In a further example, a personal computer device may be directed to start or stop an application program or process on a particular day of the month. In general, Extra Info 527 may include time information related to when Device 98 performed an operation. In other aspects, location information (i.e. coordinates, address, etc.) can be utilized and/or stored in Extra Info 527. Location information can be useful in comparisons or decision making performed in autonomous device operation related to a specific place as Device 98 may be required to perform specific operations at certain places. Location information can be obtained from a positioning system (i.e. radio signal triangulation in smartphones or tablets, GPS capabilities, etc.) if one is available. In one example, a smartphone device may be directed to engage a vibrate mode in a school or house of worship. In another example, a vehicle may be directed to turn right at a particular road crossing. In general, Extra Info 527 may include location information related to where Device 98 performed an operation. In further aspects, computed information can be utilized and/or stored in Extra Info 527. Computed information can be useful in comparisons or decision making performed in autonomous device operation where information can be calculated, inferred, or derived from other available information. VSADO Unit 100 may include computational functionalities to create Extra Info 527 by performing calculations or inferences using other information. In one example, Device's 98 speed can be computed or estimated from the Device's 98 location and/or time information. In another example, Device's 98 bearing (i.e. angle or direction of movement, etc.) can be computed or estimated from the Device's 98 location information by utilizing Pythagorean theorem, trigonometry, and/or other theorems, formulas, or disciplines. In a further example, speeds, bearings, distances, and/or other properties of objects around Device 98 can similarly be computed or inferred, thereby providing geo-spatial and situational awareness and/or capabilities to the Device 98. In further aspects, observed information can be utilized and/or stored in Extra Info 527. Observed information can be useful in comparisons or decision making performed in autonomous device operation related to a specific object or environment as Device 98 may be required to perform certain operations around specific objects or in specific

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environments. For example, an object or environment can be recognized by processing one or more Digital Pictures 525 from Picture Capturing Apparatus 90. Any features, functionalities, and embodiments of Picture Recognizer 350 (later described) can be utilized for such recognizing. In one example, book shelves recognized in the background of one or more Digital Pictures 525 from Picture Capturing Apparatus 90 may indicate a library or book store. In another example, trees recognized in the background of one or more Digital Pictures 525 from Picture Capturing Apparatus 90 may indicate a park. In a further example, a pedestrian recognized in one or more Digital Pictures 525 from Picture Capturing Apparatus 90 may indicate a street. In further aspects, sensory information can be utilized and/or stored in Extra Info 527. Examples of sensory information include acoustic information, visual information, tactile information, and/or others. Sensory information can be useful in comparisons or decision making performed in autonomous device operation related to a specific object or environment as Device 98 may be required to perform certain operations around specific objects or in specific environments. For example, an object or environment can be recognized by processing digital sound from a sound capturing apparatus (i.e. microphone, etc., not shown). Any features, functionalities, and embodiments of a speech or sound recognizer (not shown) can be utilized for such recognizing. In one example, sound of waves recognized in digital sound from a sound capturing apparatus may indicate a beach. In another example, sound of a horn recognized in digital sound from a sound capturing apparatus may indicate a proximal vehicle. In some designs where acoustic information includes one or more digital sound samples of Device's 98 surrounding captured by a sound capturing apparatus, the digital sound samples can be learned and/or used similar to Digital Pictures 525 of Device's 98 visual surrounding. In such designs, both Digital Pictures 525 and digital sound samples of a device's surrounding can be learned and/or used for autonomous device operation. In further aspects, other information can be utilized and/or stored in Extra Info 527. Examples of such other information include user specific information (i.e. skill level, age, gender, etc.), group user information (i.e. access level, etc.), version of Application Program 18, the type of Application Program 18, the type of Processor 11, the type of Logic Circuit 250, the type of Device 98, and/or other information.

Referring to Fig. 7, an embodiment where VSADO Unit 100 is part of or operating on Processor 11 is illustrated. In one example, VSADO Unit 100 may be a hardware element or circuit embedded or built into Processor 11. In another example, VSADO Unit 100 may be a program operating on Processor 11.

Referring to Fig. 8, an embodiment where VSADO Unit 100 resides on Server 96 accessible over Network 95 is illustrated. Any number of Devices 98 may connect to such remote VSADO Unit 100 and the remote VSADO Unit 100 may learn their operations in various visual surroundings. In turn, any number of Devices 98 can utilize the remote VSADO Unit 100 for autonomous operation. A remote VSADO Unit 100 can be offered as a network service (i.e. online application, etc.). In some aspects, a remote VSADO Unit 100 (i.e. global VSADO Unit 100, etc.) may reside on the Internet and be available to all the world's Devices 98 configured to transmit their operations in various visual surroundings and/or configured to utilize the remote VSADO Unit 100 for autonomous operation. Server 96 may be or include any type or form of a remote computing device such as an application server, a network service server, a cloud server, a cloud, and/or other remote computing device. Server 96 may include any features, functionalities, and embodiments of the previously described Computing Device 70. It should be understood that Server 96 does not have to be a separate computing device and that Server 96, its elements, or its functionalities can be implemented on Device 98. Network 95 may include various networks, connection types, protocols, interfaces, APIs, and/or other elements or techniques known in art all of which are within the scope of this

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disclosure. Any of the previously described networks, network or connection types, networking interfaces, and/or other networking elements or techniques can similarly be utilized. Any of the disclosed elements may reside on Server 96 in alternate implementations. In one example, Artificial Intelligence Unit 110 can reside on Server 96 and Acquisition Interface 120 and/or Modification Interface 130 can reside on Device 98. In another example, Knowledgebase 530 can reside on Server 96 and the rest of the elements of VSADO Unit 100 can reside on Device 98. Any other combination of local and remote elements can be implemented.

Referring to Fig. 9, an embodiment where Picture Capturing Apparatus 90 is part of Remote Device 97 accessible over Network 95 is illustrated. In such embodiments, VSADO Unit 100 may learn Device's 98 operation based on another device's visual surrounding. Such embodiments can be utilized, for instance, in any situation where one device controls (i.e. remote control, etc.) another device, any situation where some or all of the processing is on one device and picture capturing capabilities are on another device, and/or other situations. In one example, a drone controlling device (i.e. Device 98) may receive its visual input from a camera on the drone (i.e. Remote Device 97). In another example, a toy controlling device (i.e. Device 98) may receive its visual input from a camera on the toy (i.e. Remote Device 97). In a further example, a people or crowd analyzing computing device (i.e. Device 98) may receive its visual input from a camera of a monitoring device (i.e. Remote Device 97). Any of the disclosed elements in addition to Picture Capturing Apparatus 90 may reside on Remote Device 97 in alternate implementations as previously described with respect to Server 96.

Referring to Fig. 10, an embodiment of VSADO Unit 100 comprising Picture Recognizer 350 is illustrated. VSADO Unit 100 can utilize Picture Recognizer 350 to detect or recognize persons, objects, and/or their activities in one or more digital pictures from Picture Capturing Apparatus 90. In general, VSADO Unit 100 and/or other disclosed elements can use Picture Recognizer 350 for any operation supported by Picture Recognizer 350. Picture Recognizer 350 comprises the functionality for detecting or recognizing persons or objects in visual data. Picture Recognizer 350 comprises the functionality for detecting or recognizing activities in visual data. Picture Recognizer 350 comprises the functionality for tracking persons, objects, and/or their activities in visual data. Picture Recognizer 350 comprises other disclosed functionalities. Visual data includes digital motion pictures, digital still pictures (i.e. bitmaps, etc.), and/or other visual data. Examples of file formats that can be utilized to store visual data include AVI, DivX, MPEG, JPEG, GIF, TIFF, PNG, PDF, and/or other file formats. Picture Recognizer 350 may detect or recognize a person and/or his/her activities as well as track the person and/or his/her activities in one or more digital pictures or streams of digital pictures (i.e. motion pictures, video, etc.). Picture Recognizer 350 may detect or recognize a human head or face, upper body, full body, or portions/combinations thereof. In some aspects, Picture Recognizer 350 may detect or recognize persons, objects, and/or their activities from a digital picture by comparing regions of pixels from the digital picture with collections of pixels comprising known persons, objects, and/or their activities. The collections of pixels comprising known persons, objects, and/or their activities can be learned or manually, programmatically, or otherwise defined. The collections of pixels comprising known persons, objects, and/or their activities can be stored in any data structure or repository (i.e. one or more files, database, etc.) that resides locally on Device 98, or remotely on a remote computing device (i.e. server, cloud, etc.) accessible over a network. In other aspects, Picture Recognizer 350 may detect or recognize persons, objects, and/or their activities from a digital picture by comparing features (i.e. lines, edges, ridges, corners, blobs, regions, etc.) of the digital picture with features of known persons, objects, and/or their activities. The features of known persons, objects,

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and/or their activities can be learned or manually, programmatically, or otherwise defined. The features of known persons, objects, and/or their activities can be stored in any data structure or repository (i.e. neural network, one or more files, database, etc.) that resides locally on Device 98, or remotely on a remote computing device (i.e. server, cloud, etc.) accessible over a network. Typical steps or elements in a feature oriented picture recognition include pre-processing, feature extraction, detection/segmentation, decision-making, and/or others, or a combination thereof, each of which may include its own sub-steps or sub-elements depending on the application. In further aspects, Picture Recognizer 350 may detect or recognize multiple persons, objects, and/or their activities from a digital picture using the aforementioned pixel or feature comparisons, and/or other detection or recognition techniques. For example, a picture may depict two persons in two of its regions both of whom Picture Recognizer 350 can detect simultaneously. In further aspects, where persons, objects, and/or their activities span multiple pictures, Picture Recognizer 350 may detect or recognize persons, objects, and/or their activities by applying the aforementioned pixel or feature comparisons and/or other detection or recognition techniques over a stream of digital pictures (i.e. motion picture, video, etc.). For example, once a person is detected in a digital picture (i.e. frame, etc.) of a stream of digital pictures (i.e. motion picture, video, etc.), the region of pixels comprising the detected person or the person's features can be searched in other pictures of the stream of digital pictures, thereby tracking the person through the stream of digital pictures. In further aspects, Picture Recognizer 350 may detect or recognize a person's activities by identifying and/or analyzing differences between a detected region of pixels of one picture (i.e. frame, etc.) and detected regions of pixels of other pictures in a stream of digital pictures. For example, a region of pixels comprising a person's face can be detected in multiple consecutive pictures of a stream of digital pictures (i.e. motion picture, video, etc.). Differences among the detected regions of the consecutive pictures may be identified in the mouth part of the person's face to indicate smiling or speaking activity. Any technique for recognizing speech from mouth/lip movements can be used in this and other examples. In further aspects, Picture Recognizer 350 may detect or recognize persons, objects, and/or their activities using one or more artificial neural networks, which may include statistical techniques. Examples of artificial neural networks that can be used in Picture Recognizer 350 include convolutional neural networks (CNNs), time delay neural networks (TDNNs), deep neural networks, and/or others. In one example, picture recognition techniques and/or tools involving convolutional neural networks may include identifying and/or analyzing tiled and/or overlapping regions or features of a digital picture, which may then be used to search for pictures with matching regions or features. In another example, features of different convolutional neural networks responsible for spatial and temporal streams can be fused to detect persons, objects, and/or their activities in streams of digital pictures (i.e. motion pictures, videos, etc.). In general, Picture Recognizer 350 may include any machine learning, deep learning, and/or other artificial intelligence techniques. Any other techniques known in art can be utilized in Picture Recognizer 350. For example, thresholds for similarity, statistical, and/or optimization techniques can be utilized to determine a match in any of the above-described detection or recognition techniques. Picture Recognizer 350 comprises any features, functionalities, and embodiments of Similarity Comparison 125 (later described).

In some exemplary embodiments, object recognition techniques and/or tools such as OpenCV (Open Source Computer Vision) library, CamFind API, Kooaba, 6px API, Dextro API, and/or others can be utilized for detecting or recognizing objects (i.e. objects, animals, people, etc.) in digital pictures. In some aspects, object recognition techniques and/or tools involve identifying and/or analyzing object features such as lines, edges, ridges,

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corners, blobs, regions, and/or their relative positions, sizes, shapes, etc., which may then be used to search for pictures with matching features. For example, OpenCV library can detect an object (i.e. car, pedestrian, door, building, animal, person, etc.) in one or more digital pictures captured by Picture Capturing Apparatus 90 or stored in an electronic repository, which can then be utilized in VSADO Unit 100, Artificial Intelligence Unit 110, and/or other elements.

In other exemplary embodiments, facial recognition techniques and/or tools such as OpenCV (Open Source Computer Vision) library, Animetrics FaceR API, Lambda Labs Facial Recognition API, Face++ SDK, Neven Vision (also known as N-Vision) Engine, and/or others can be utilized for detecting or recognizing faces in digital pictures. In some aspects, facial recognition techniques and/or tools involve identifying and/or analyzing facial features such as the relative position, size, and/or shape of the eyes, nose, cheekbones, jaw, etc., which may then be used to search for pictures with matching features. For example, FaceR API can detect a person's face in one or more digital pictures captured by Picture Capturing Apparatus 90 or stored in an electronic repository, which can then be utilized in VSADO Unit 100, Artificial Intelligence Unit 110, and/or other elements.

Referring to Fig. 11, an embodiment of Artificial Intelligence Unit 110 is illustrated. Artificial Intelligence Unit 110 comprises interconnected Knowledge Structuring Unit 520, Knowledgebase 530, Decision-making Unit 540, and Confirmation Unit 550. Other additional elements can be included as needed, or some of the disclosed ones can be excluded, or a combination thereof can be utilized in alternate embodiments.

Artificial Intelligence Unit 110 comprises the functionality for learning Device's 98 operation in various visual surroundings. Artificial Intelligence Unit 110 comprises the functionality for learning one or more digital pictures correlated with any instruction sets, data, and/or other information. In some aspects, Artificial Intelligence Unit 110 comprises the functionality for learning one or more Digital Pictures 525 of Device's 98 surrounding correlated with any Instruction Sets 526 and/or Extra Info 527. In other aspects, Artificial Intelligence Unit 110 comprises the functionality for learning one or more Digital Pictures 525 of Device's 98 surrounding some of which may not be correlated with any Instruction Sets 526 and/or Extra Info 527. Further, Artificial Intelligence Unit 110 comprises the functionality for anticipating Device's 98 operation in various visual surroundings. Artificial Intelligence Unit 110 comprises the functionality for anticipating one or more instruction sets, data, and/or other information. Artificial Intelligence Unit 110 comprises the functionality for anticipating one or more Instruction Sets 526 based on one or more incoming Digital Pictures 525 of Device's 98 surrounding. Artificial Intelligence Unit 110 comprises the functionality for anticipating one or more Instruction Sets 526 to be used or executed in Device's 98 autonomous operation. Artificial Intelligence Unit 110 also comprises other disclosed functionalities.

Knowledge Structuring Unit 520, Knowledgebase 530, and Decision-making Unit 540 are described later.

Confirmation Unit 550 comprises the functionality for confirming, modifying, evaluating (i.e. rating, etc.), and/or canceling one or more anticipatory Instruction Sets 526, and/or other functionalities. Confirmation Unit 550 is an optional element that can be omitted depending on implementation. In some embodiments, Confirmation Unit 550 can serve as a means of confirming anticipatory Instruction Sets 526. For example, Decision-making Unit 540 may determine one or more anticipatory Instruction Sets 526 and provide them to User 50 for confirmation. User 50 may be provided with an interface (i.e. graphical user interface, selectable list of anticipatory Instruction Sets 526, etc.) to approve or confirm execution of the anticipatory Instruction Sets 526. In some aspects, Confirmation Unit 550 can automate User 50 confirmation. In one example, if one or more incoming Digital Pictures 525 from Picture

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Capturing Apparatus 90 and one or more Digital Pictures 525 from a Knowledge Cell 800 were found to be a perfect or highly similar match, anticipatory Instruction Sets 526 correlated with the one or more Digital Pictures 525 from the Knowledge Cell 800 can be automatically executed without User's 50 confirmation. Conversely, if one or more incoming Digital Pictures 525 from Picture Capturing Apparatus 90 and one or more Digital Pictures 525 from a Knowledge Cell 800 were found to be less than a highly similar match, anticipatory Instruction Sets 526 correlated with the one or more Digital Pictures 525 from the Knowledge Cell 800 can be presented to User 50 for confirmation and/or modifying. A threshold that defines a highly or otherwise similar match can be utilized in such implementations. Such threshold can be defined by a user, by VSADO system administrator, or automatically by the system based on experience, testing, inquiry, analysis, synthesis, or other techniques, knowledge, or input. In other embodiments, Confirmation Unit 550 can serve as a means of modifying or editing anticipatory Instruction Sets 526. For example, Decision-making Unit 540 may determine one or more anticipatory Instruction Sets 526 and provide them to User 50 for modification. User 50 may be provided with an interface (i.e. graphical user interface, etc.) to modify the anticipatory Instruction Sets 526 before their execution. In further embodiments, Confirmation Unit 550 can serve as a means of evaluating or rating anticipatory Instruction Sets 526 if they matched User's 50 intended operation. For example, Decision-making Unit 540 may determine one or more anticipatory Instruction Sets 526, which the system may automatically execute. User 50 may be provided with an interface (i.e. graphical user interface, etc.) to rate (i.e. on a scale from 0 to 1, etc.) how well Decision-making Unit 540 predicted the executed anticipatory Instruction Sets 526. In some aspects, rating can be automatic and based on a particular function or method that rates how well the anticipatory Instruction Sets 526 matched the desired operation. In one example, a rating function or method can assign a higher rating to anticipatory Instruction Sets 526 that were least modified in the confirmation process. In another example, a rating function or method can assign a higher rating to anticipatory Instruction Sets 526 that were canceled least number of times by User 50. Any other automatic rating function or method can be utilized. In yet other embodiments, Confirmation Unit 550 can serve as a means of canceling anticipatory Instruction Sets 526 if they did not match User's 50 intended operation. For example, Decision-making Unit 540 may determine one or more anticipatory Instruction Sets 526, which the system may automatically execute. The system may save the state of Device 98, Processor 11 (save its register values, etc.), Logic Circuit 250, Application Program 18 (i.e. save its variables, data structures, objects, location of its current instruction, etc.), and/or other processing elements before executing anticipatory Instruction Sets 526. User 50 may be provided with an interface (i.e. graphical user interface, selectable list of prior executed anticipatory Instruction Sets 526, etc.) to cancel one or more of the prior executed anticipatory Instruction Sets 526, and restore Device 98, Processor 11, Logic Circuit 250, Application Program 18, and/or other processing elements to a prior state. In some aspects, Confirmation Unit 550 can optionally be disabled or omitted in order to provide an uninterrupted operation of Device 98, Processor 11, Logic Circuit 250, and/or Application Program 18. For example, a microwave oven may be suitable for implementing the user confirmation step, whereas, a robot or vehicle may be less suitable for implementing such interrupting step due to the real time nature of robot or vehicle operation.

Referring to Fig. 12, an embodiment of Knowledge Structuring Unit 520 correlating individual Digital Pictures 525 with any Instruction Sets 526 and/or Extra Info 527 is illustrated. Knowledge Structuring Unit 520 comprises the functionality for structuring the knowledge of a device's operation in various visual surroundings, and/or other functionalities. Knowledge Structuring Unit 520 comprises the functionality for correlating one or more

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Digital Pictures 525 of Device's 98 surrounding with any Instruction Sets 526 and/or Extra Info 527. Knowledge Structuring Unit 520 comprises the functionality for creating or generating Knowledge Cell 800 and storing one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 into the Knowledge Cell 800. As such, Knowledge Cell 800 comprises the functionality for storing one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527. Knowledge Cell 800 includes a unit of knowledge of how Device 98 operated in a visual surrounding. Once created or generated, Knowledge Cells 800 can be used in/as neurons, nodes, vertices, or other elements in any of the data structures or arrangements (i.e. neural networks, graphs, sequences, etc.) used for storing the knowledge of Device's 98 operation in various visual surroundings, thereby facilitating learning functionalities herein. It should be noted that Extra Info 527 may be optionally used in some implementations to enable enhanced comparisons or decision making in autonomous device operation where applicable, and that Extra Info 527 can be omitted in alternate implementations.

In some embodiments, Knowledge Structuring Unit 520 receives one or more Digital Pictures 525 from Picture Capturing Apparatus 90. Knowledge Structuring Unit 520 may also receive one or more Instruction Sets 526 from Acquisition Interface 120. Knowledge Structuring Unit 520 may further receive any Extra Info 527. Although, Extra Info 527 is not shown in this and/or other figures for clarity of illustration, it should be noted that any Digital Picture 525, Instruction Set 526, and/or other element may include or be associated with Extra Info 527. Knowledge Structuring Unit 520 may correlate one or more Digital Pictures 525 with any Instruction Sets 526 and/or Extra Info 527. Knowledge Structuring Unit 520 may then create Knowledge Cell 800 and store the one or more Digital Pictures 525 correlated with Instruction Sets 526 and/or Extra Info 527 into the Knowledge Cell 800. Knowledge Cell 800 may include any data structure or arrangement that can facilitate such storing. For example, Knowledge Structuring Unit 520 may create Knowledge Cell 800ax and structure within it Digital Picture 525a1 correlated with Instruction Sets 526a1-526a3 and/or any Extra Info 527 (not shown). Knowledge Structuring Unit 520 may further structure within Knowledge Cell 800ax a Digital Picture 525a2 correlated with Instruction Set 526a4 and/or any Extra Info 527 (not shown). Knowledge Structuring Unit 520 may further structure within Knowledge Cell 800ax a Digital Picture 525a3 without a correlated Instruction Set 526 and/or Extra Info 527. Knowledge Structuring Unit 520 may further structure within Knowledge Cell 800ax a Digital Picture 525a4 correlated with Instruction Sets 526a5-526a6 and/or any Extra Info 527 (not shown). Knowledge Structuring Unit 520 may further structure within Knowledge Cell 800ax a Digital Picture 525a5 without a correlated Instruction Set 526 and/or Extra Info 527. Knowledge Structuring Unit 520 may structure within Knowledge Cell 800ax additional Digital Pictures 525 correlated with any number (including zero [i.e. uncorrelated]) of Instruction Sets 526 and/or Extra Info 527 by following the same logic as described above.

In some embodiments, Knowledge Structuring Unit 520 may correlate a Digital Picture 525 with one or more temporally corresponding Instruction Sets 526 and/or Extra Info 527. This way, Knowledge Structuring Unit 520 can structure the knowledge of Device's 98 operation at or around the time of the capturing of Digital Pictures 525 of Device's 98 surrounding. Such functionality enables spontaneous or seamless learning of Device's 98 operation in various visual surroundings as user operates the device in real life situations. In some designs, Knowledge Structuring Unit 520 may receive a stream of Instruction Sets 526 used or executed to effect Device's 98 operations as well as a stream of Digital Pictures 525 of Device's 98 surrounding as the operations are performed. Knowledge Structuring Unit 520 can then correlate Digital Pictures 525 from the stream of Digital Pictures 525 with

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temporally corresponding Instruction Sets 526 from the stream of Instruction Sets 526 and/or any Extra Info 527. Digital Pictures 525 without a temporally corresponding Instruction Set 526 may be uncorrelated, for instance. In some aspects, Instruction Sets 526 and/or Extra Info 527 that temporally correspond to a Digital Picture 525 may include Instruction Sets 526 used and/or Extra Info 527 obtained at the time of capturing the Digital Picture 525. In other aspects, Instruction Sets 526 and/or Extra Info 527 that temporally correspond to a Digital Picture 525 may include Instruction Sets 526 used and/or Extra Info 527 obtained within a certain time period before and/or after capturing the Digital Picture 525. For example, Instruction Sets 526 and/or Extra Info 527 that temporally correspond to a Digital Picture 525 may include Instruction Sets 526 used and/or Extra Info 527 obtained within 50 milliseconds, 1 second, 3 seconds, 20 seconds, 1 minute, 41 minutes, 2 hours, or any other time period before and/or after capturing the Digital Picture 525. Such time periods can be defined by a user, by VSADO system administrator, or automatically by the system based on experience, testing, inquiry, analysis, synthesis, or other techniques, knowledge, or input. In other aspects, Instruction Sets 526 and/or Extra Info 527 that temporally correspond to a Digital Picture 525 may include Instruction Sets 526 used and/or Extra Info 527 obtained from the time of capturing of the Digital Picture 525 to the time of capturing of a next Digital Picture 525. In further aspects, Instruction Sets 526 and/or Extra Info 527 that temporally correspond to a Digital Picture 525 may include Instruction Sets 526 used and/or Extra Info 527 obtained from the time of capturing of a previous Digital Picture 525 to the time of capturing of the Digital Picture 525. Any other temporal relationship or correspondence between Digital Pictures 525 and correlated Instruction Sets 526 and/or Extra Info 527 can be implemented.

In some embodiments, Knowledge Structuring Unit 520 can structure the knowledge of Device's 98 operation in a visual surrounding into any number of Knowledge Cells 800. In some aspects, Knowledge Structuring Unit 520 can structure into a Knowledge Cell 800 a single Digital Picture 525 correlated with any Instruction Sets 526 and/or Extra Info 527. In other aspects, Knowledge Structuring Unit 520 can structure into a Knowledge Cell 800 any number (i.e. 2, 3, 5, 8, 19, 33, 99, 1715, 21822, 393477, 6122805, etc.) of Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527. In a special case, Knowledge Structuring Unit 520 can structure all Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 into a single long Knowledge Cell 800. In further aspects, Knowledge Structuring Unit 520 can structure Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 into a plurality of Knowledge Cells 800. In a special case, Knowledge Structuring Unit 520 can store periodic streams of Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 into a plurality of Knowledge Cells 800 such as hourly, daily, weekly, monthly, yearly, or other periodic Knowledge Cells 800.

In some embodiments, Knowledge Structuring Unit 520 may be responsive to a triggering object, action, event, time, and/or other stimulus. In some aspects, the system can detect or recognize an object in Device's 98 visual surrounding, and Knowledge Structuring Unit 520 can structure into a Knowledge Cell 800 one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 related to the object. For example, Knowledge Structuring Unit 520 can structure into a Knowledge Cell 800 one or more Digital Pictures 525 of a pizza from a microwave oven (i.e. Device 98, etc.) correlated with any Instruction Sets 526 (i.e. inputs, outputs, or states of the microwave oven's microcontroller, etc.) causing the microwave oven to bake the pizza. Knowledge Structuring Unit 520 can also structure into the Knowledge Cell 800 any Extra Info 527 (i.e. time, location, computed, observed, sensory, and/or other information, etc.). In other aspects, the system can detect or recognize a specific action or

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operation performed by Device 98, and Knowledge Structuring Unit 520 can structure into a Knowledge Cell 800 one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 related to the action or operation. For example, Knowledge Structuring Unit 520 can structure into a Knowledge Cell 800 one or more Digital Pictures 525 depicting screwing of a screw by a robotic arm (i.e. Device 98, etc.) correlated with any Instruction Sets 526 causing the robotic arm to screw the screw. Knowledge Structuring Unit 520 can also structure into the Knowledge Cell 800 any Extra Info 527 (i.e. time, location, computed, observed, sensory, and/or other information, etc.). In further aspects, the system can detect a person in Device's 98 visual surrounding, and Knowledge Structuring Unit 520 can structure into a Knowledge Cell 800 one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 related to the person. For example, Knowledge Structuring Unit 520 can structure into a Knowledge Cell 800 one or more Digital Pictures 525 of a pedestrian in front of a vehicle (i.e. Device 98, etc.) correlated with any Instruction Sets 526 causing the vehicle to stop. Knowledge Structuring Unit 520 can also structure into the Knowledge Cell 800 any Extra Info 527 (i.e. time, location, computed, observed, sensory, and/or other information, etc.). In further aspects, the system can detect or recognize a significant change in Device's 98 visual surrounding, and Knowledge Structuring Unit 520 can structure into a Knowledge Cell 800 one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 related to the change in visual surrounding. For example, the system can detect a vehicle's (i.e. Device 98, etc.) changing direction (i.e. turning left, right, etc.) and Knowledge Structuring Unit 520 can structure into a Knowledge Cell 800 one or more Digital Pictures 525 correlated with any Instruction Sets 526 causing the change of direction. Knowledge Structuring Unit 520 can also structure into the Knowledge Cell 800 any Extra Info 527 (i.e. time, location, computed, observed, sensory, and/or other information, etc.). A vehicle's changing direction may be detected as a significant change in the vehicle's visual surrounding as the view of the vehicle's scenery changes significantly. Any features, functionalities, and embodiments of Picture Recognizer 350 can be utilized in the aforementioned detecting or recognizing. In general, Knowledge Structuring Unit 520 can structure into a Knowledge Cell 800 any Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 related to any triggering object, action, event, time, and/or other stimulus.

In some embodiments, Device 98 may include a plurality of Picture Capturing Apparatuses 90. In one example, different Picture Capturing Apparatuses 90 may capture Digital Pictures 525 of different angles or sides of Device 98. In another example, different Picture Capturing Apparatuses 90 may be placed on different sub-devices, sub-systems, or elements of Device 98. Using multiple Picture Capturing Apparatuses 90 may provide additional visual detail in learning and/or using Device's 98 surrounding for autonomous Device 98 operation. In some designs where multiple Picture Capturing Apparatuses 90 are utilized, multiple VSADO Units 100 can also be utilized (i.e. one VSADO Unit 100 for each Picture Capturing Apparatus 90, etc.). Digital Pictures 525 of Device's 98 surrounding can be correlated with any Instruction Sets 526 and/or Extra Info 527 as previously described. In other designs where multiple Picture Capturing Apparatuses 90 are utilized, collective Digital Pictures 525 of Device's 98 surrounding from multiple Picture Capturing Apparatuses 90 can be correlated with any Instruction Sets 526 and/or Extra Info 527.

In some embodiments, Device 98 may include a plurality of Logic Circuits 250, Processors 11, Application Programs 18, and/or other processing elements. For example, each processing element may control a sub-device, sub-system, or an element of Device 98. Using multiple processing elements may provide enhanced control over

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Device's 98 operation. In some designs where multiple processing elements are utilized, multiple VSADO Units 100 can also be utilized (i.e. one VSADO Unit 100 for each processing element, etc.). Digital Pictures 525 of Device's 98 surrounding can be correlated with any Instruction Sets 526 and/or Extra Info 527 as previously described. In other designs where multiple processing elements are utilized, Digital Pictures 525 of Device's 98 surrounding can be correlated with any collective Instruction Sets 526 and/or Extra Info 527 used or executed by a plurality of processing elements.

Any combination of the aforementioned multiple Picture Capturing Apparatuses 90, multiple processing elements, and/or other elements can be implemented in alternate embodiments.

Referring to Fig. 13, another embodiment of Knowledge Structuring Unit 520 correlating individual Digital Pictures 525 with any Instruction Sets 526 and/or Extra Info 527 is illustrated. In such embodiments, Knowledge Structuring Unit 520 may generate Knowledge Cells 800 each comprising a single Digital Picture 525 correlated with any Instruction Sets 526 and/or Extra Info 527.

Referring to Fig. 14, an embodiment of Knowledge Structuring Unit 520 correlating streams of Digital Pictures 525 with any Instruction Sets 526 and/or Extra Info 527 is illustrated. In some aspects, a stream of Digital Pictures 525 may include a collection, a group, a sequence, or other plurality of Digital Pictures 525. In other aspects, a stream of Digital Pictures 525 may include one or more Digital Pictures 525. In further aspects, a stream of Digital Pictures 525 may include a digital motion picture (i.e. digital video, etc.) or portion thereof. For example, Knowledge Structuring Unit 520 may create Knowledge Cell 800ax and structure within it a stream of Digital Pictures 525a1-525an correlated with Instruction Set 526a1 and/or any Extra Info 527 (not shown). Knowledge Structuring Unit 520 may further structure within Knowledge Cell 800ax a stream of Digital Pictures 525b1-525bn correlated with Instruction Sets 526a2-526a4 and/or and Extra Info 527 (not shown). Knowledge Structuring Unit 520 may further structure within Knowledge Cell 800ax a stream of Digital Pictures 525c1-525cn without correlated Instruction Sets 526 and/or Extra Info 527. Knowledge Structuring Unit 520 may further structure within Knowledge Cell 800ax a stream of Digital Pictures 525d1-525dn correlated with Instruction Sets 526a5-526a6 and/or any Extra Info 527 (not shown). Knowledge Structuring Unit 520 may further structure within Knowledge Cell 800ax additional streams of Digital Pictures 525 correlated with any number (including zero [i.e. uncorrelated]) of Instruction Sets 526 and/or Extra Info 527 by following the same logic as described above. The number of Digital Pictures 525 in some or all streams of Digital Pictures 525a1-525an, 525b1-525bn, etc. may be equal or different. It should be noted that n or other such letters or indicia may follow the sequence and/or context where they are indicated. Also, a same letter or indicia such as n may represent a different number in different elements of a drawing.

Referring to Fig. 15, another embodiment of Knowledge Structuring Unit 520 correlating streams of Digital Pictures 525 with any Instruction Sets 526 and/or Extra Info 527 is illustrated. In such embodiments, Knowledge Structuring Unit 520 may generate Knowledge Cells 800 each comprising a single stream of Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527.

Knowledgebase 530 comprises the functionality for storing the knowledge of a device's operation in various visual surroundings, and/or other functionalities. Knowledgebase 530 comprises the functionality for storing one or more Digital Pictures 525 of Device's 98 surrounding correlated with any Instruction Sets 526 and/or Extra Info 527. Knowledgebase 530 comprises the functionality for storing one or more Knowledge Cells 800 each including one or more Digital Pictures 525 of Device's 98 surrounding correlated with any Instruction Sets 526 and/or Extra Info 527.

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In some aspects, Digital Pictures 525 correlated with Instruction Sets 526 and/or Extra Info 527 can be stored directly within Knowledgebase 530 without using Knowledge Cells 800 as the intermediary data structures. In some embodiments, Knowledgebase 530 may be or include Neural Network 530a (later described). In other embodiments, Knowledgebase 530 may be or include Graph 530b (later described). In further embodiments, Knowledgebase 530 may be or include Collection of Sequences 530c (later described). In further embodiments, Knowledgebase 530 may be or include Sequence 533 (later described). In further embodiments, Knowledgebase 530 may be or include Collection of Knowledge Cells 530d (later described). In general, Knowledgebase 530 may be or include any data structure or arrangement capable of storing the knowledge of a device's operation in various visual surroundings. Knowledgebase 530 may reside locally on Device 98, or remotely (i.e. remote Knowledgebase 530, etc.) on a remote computing device (i.e. server, cloud, etc.) accessible over a network.

Knowledgebase 530 from one Device 98 or VSADO Unit 100 can be transferred to one or more other Devices 98 or VSADO Units 100. Therefore, the knowledge of Device's 98 operation in various visual surroundings learned on one Device 98 or VSADO Unit 100 can be transferred to one or more other Devices 98 or VSADO Units 100. In one example, Knowledgebase 530 can be copied or downloaded to a file or other repository from one Device 98 or VSADO Unit 100 and loaded or inserted into another Device 98 or VSADO Unit 100. In another example, Knowledgebase 530 from one Device 98 or VSADO Unit 100 can be available on a server accessible by other Devices 98 or VSADO Units 100 over a network. Once loaded into or accessed by a receiving Device 98 or VSADO Unit 100, the receiving Device 98 or VSADO Unit 100 can then implement the knowledge of Device's 98 operation in various visual surroundings learned on the originating Device 98 or VSADO Unit 100. This functionality enables User 50 such as a professional Device 98 operator to record his/her knowledge, methodology, or style of operating Device 98 in various visual surroundings and/or sell his/her knowledge to other users.

Referring to Fig. 16, the disclosed artificially intelligent systems, devices, and methods for learning and/or using visual surrounding for autonomous device operation may include various artificial intelligence models and/or techniques. The disclosed systems, devices, and methods are independent of the artificial intelligence model and/or technique used and any model and/or technique can be utilized to facilitate the functionalities described herein. Examples of these models and/or techniques include deep learning, supervised learning, unsupervised learning, neural networks (i.e. convolutional neural network, recurrent neural network, deep neural network, etc.), search-based, logic and/or fuzzy logic-based, optimization-based, tree/graph/other data structure-based, hierarchical, symbolic and/or sub-symbolic, evolutionary, genetic, multi-agent, deterministic, probabilistic, statistical, and/or other models and/or techniques.

In one example shown in Model A, the disclosed artificially intelligent systems, devices, and methods for learning and/or using visual surrounding for autonomous device operation may include a neural network (also referred to as artificial neural network, etc.). As such, machine learning, knowledge structuring or representation, decision making, pattern recognition, and/or other artificial intelligence functionalities may include a network of Nodes 852 (also referred to as neurons, etc.) and Connections 853 similar to that of a brain. Node 852 can store any data, object, data structure, and/or other item, or reference thereto. Node 852 may also include a function for transforming or manipulating any data, object, data structure, and/or other item. Examples of such transformation functions include mathematical functions (i.e. addition, subtraction, multiplication, division, sin, cos, log, derivative, integral, etc.), object manipulation functions (i.e. creating an object, modifying an object, deleting an object,

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appending objects, etc.), data structure manipulation functions (i.e. creating a data structure, modifying a data structure, deleting a data structure, creating a data field, modifying a data field, deleting a data field, etc.), and/or other transformation functions. Connection 853 may include or be associated with a value such as a symbolic label or numeric attribute (i.e. weight, cost, capacity, length, etc.). A computational model can be utilized to compute values from inputs based on a pre-programmed or learned function or method. For example, a neural network may include one or more input neurons that can be activated by inputs. Activations of these neurons can then be passed on, weighted, and transformed by a function to other neurons. Neural networks may range from those with only one layer of single direction logic to multi-layer of multi-directional feedback loops. A neural network can use weights to change the parameters of the network's throughput. A neural network can learn by input from its environment or from self-teaching using written-in rules. A neural network can be utilized as a predictive modeling approach in machine learning. An exemplary embodiment of a neural network (i.e. Neural Network 530a, etc.) is described later.

In another example shown in Model B, the disclosed artificially intelligent systems, devices, and methods for learning and/or using visual surrounding for autonomous device operation may include a graph or graph-like data structure. As such, machine learning, knowledge structuring or representation, decision making, pattern recognition, and/or other artificial intelligence functionalities may include Nodes 852 (also referred to as vertices or points, etc.) and Connections 853 (also referred to as edges, arrows, lines, arcs, etc.) organized as a graph. In general, any Node 852 in a graph can be connected to any other Node 852. A Connection 853 may include unordered pair of Nodes 852 in an undirected graph or ordered pair of Nodes 852 in a directed graph. Nodes 852 can be part of the graph structure or external entities represented by indices or references. A graph can be utilized as a predictive modeling approach in machine learning. Nodes 852, Connections 853, and/or other elements or operations of a graph may include any features, functionalities, and embodiments of the aforementioned Nodes 852, Connections 853, and/or other elements or operations of a neural network, and vice versa. An exemplary embodiment of a graph (i.e. Graph 530b, etc.) is described later.

In a further example shown in Model C, the disclosed artificially intelligent systems, devices, and methods for learning and/or using visual surrounding for autonomous device operation may include a tree or tree-like data structure. As such, machine learning, knowledge structuring or representation, decision making, pattern recognition, and/or other artificial intelligence functionalities may include Nodes 852 and Connections 853 (also referred to as references, edges, etc.) organized as a tree. In general, a Node 852 in a tree can be connected to any number (i.e. including zero, etc.) of children Nodes 852. A tree can be utilized as a predictive modeling approach in machine learning. Nodes 852, Connections 853, and/or other elements or operations of a tree may include any features, functionalities, and embodiments of the aforementioned Nodes 852, Connections 853, and/or other elements or operations of a neural network and/or graph, and vice versa.

In a further example shown in Model D, the disclosed artificially intelligent systems, devices, and methods for learning and/or using visual surrounding for autonomous device operation may include a sequence or sequence-like data structure. As such, machine learning, knowledge structuring or representation, decision making, pattern recognition, and/or other artificial intelligence functionalities may include a structure of Nodes 852 and/or Connections 853 organized as a sequence. In some aspects, Connections 853 may be optionally omitted from a sequence as the sequential order of Nodes 852 in a sequence may be implied in the structure. A sequence can be utilized as a predictive modeling approach in machine learning. Nodes 852, Connections 853, and/or other elements

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or operations of a sequence may include any features, functionalities, and embodiments of the aforementioned Nodes 852, Connections 853, and/or other elements or operations of a neural network, graph, and/or tree, and vice versa. An exemplary embodiment of a sequence (i.e. Collection of Sequences 530c, Sequence 533, etc.) is described later.

In yet another example, the disclosed artificially intelligent systems, devices, and methods for learning and/or using visual surrounding for autonomous device operation may include a search-based model and/or technique. As such, machine learning, knowledge structuring or representation, decision making, pattern recognition, and/or other artificial intelligence functionalities may include searching through a collection of possible solutions. For example, a search method can search through a neural network, graph, tree, sequence, or other data structure that includes data elements of interest. A search may use heuristics to limit the search for solutions by eliminating choices that are unlikely to lead to the goal. Heuristic techniques may provide a best guess solution. A search can also include optimization. For example, a search may begin with a guess and then refine the guess incrementally until no more refinements can be made. In a further example, the disclosed systems, devices, and methods may include logic-based model and/or technique. As such, machine learning, knowledge structuring or representation, decision making, pattern recognition, and/or other artificial intelligence functionalities can use formal or other type of logic. Logic based models may involve making inferences or deriving conclusions from a set of premises. As such, a logic based system can extend existing knowledge or create new knowledge automatically using inferences. Examples of the types of logic that can be utilized include propositional or sentential logic that comprises logic of statements which can be true or false; first-order logic that allows the use of quantifiers and predicates and that can express facts about objects, their properties, and their relations with each other; fuzzy logic that allows degrees of truth to be represented as a value between 0 and 1 rather than simply 0 (false) or 1 (true), which can be used for uncertain reasoning; subjective logic that comprises a type of probabilistic logic that may take uncertainty and belief into account, which can be suitable for modeling and analyzing situations involving uncertainty, incomplete knowledge and different world views; and/or other types of logic. In a further example, the disclosed systems, devices, and methods may include a probabilistic model and/or technique. As such, machine learning, knowledge structuring or representation, decision making, pattern recognition, and/or other artificial intelligence functionalities can be implemented to operate with incomplete or uncertain information where probabilities may affect outcomes. Bayesian network, among other models, is an example of a probabilistic tool used for purposes such as reasoning, learning, planning, perception, and/or others. One of ordinary skill in art will understand that the aforementioned artificial intelligence models and/or techniques are described merely as examples of a variety of possible implementations, and that while all possible artificial intelligence models and/or techniques are too voluminous to describe, other artificial intelligence models and/or techniques known in art are within the scope of this disclosure. One of ordinary skill in art will also recognize that an intelligent system may solve a specific problem by using any model and/or technique that works such as, for example, some systems can be symbolic and logical, some can be sub-symbolic neural networks, some can be deterministic or probabilistic, some can be hierarchical, some may include searching techniques, some may include optimization techniques, while others may use other or a combination of models and/or techniques. In general, any artificial intelligence model and/or technique can be utilized that can facilitate the functionalities described herein.

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Referring to Figs. 17A-17C, embodiments of interconnected Knowledge Cells 800 and updating weights of Connections 853 are illustrated. As shown for example in Fig. 17A, Knowledge Cell 800za is connected to Knowledge Cell 800zb and Knowledge Cell 800zc by Connection 853z1 and Connection 853z2, respectively. Each of Connection 853z1 and Connection 853z2 may include or be associated with occurrence count, weight, and/or other parameter or data. The number of occurrences may track or store the number of observations that a Knowledge Cell 800 was followed by another Knowledge Cell 800 indicating a connection or relationship between them. For example, Knowledge Cell 800za was followed by Knowledge Cell 800zb 10 times as indicated by the number of occurrences of Connection 853z1. Also, Knowledge Cell 800za was followed by Knowledge Cell 800zc 15 times as indicated by the number of occurrences of Connection 853z2. The weight of Connection 853z1 can be calculated or determined as the number of occurrences of Connection 853z1 divided by the sum of occurrences of all connections (i.e. Connection 853z1 and Connection 853z2, etc.) originating from Knowledge Cell 800za. Therefore, the weight of Connection 853z1 can be calculated or determined as 10/(10+15)=0.4, for example. Also, the weight of Connection 853z2 can be calculated or determined as 15/(10+15)=0.6, for example. Therefore, the sum of weights of Connection 853z1, Connection 853z2, and/or any other Connections 853 originating from Knowledge Cell 800za may equal to 1 or 100%. As shown for example in Fig. 17B, in the case that Knowledge Cell 800zd is inserted and an observation is made that Knowledge Cell 800zd follows Knowledge Cell 800za, Connection 853z3 can be created between Knowledge Cell 800za and Knowledge Cell 800zd. The occurrence count of Connection 853z3 can be set to 1 and weight determined as 1/(10+15+1)=0.038. The weights of all other connections (i.e. Connection 853z1, Connection 853z2, etc.) originating from Knowledge Cell 800za may be updated to account for the creation of Connection 853z3. Therefore, the weight of Connection 853z1 can be updated as 10/(10+15+1)=0.385. The weight of Connection 853z2 can also be updated as 15/(10+15+1)=0.577. As shown for example in Fig. 17C, in the case that an additional occurrence of Connection 853z1 is observed (i.e. Knowledge Cell 800zb followed Knowledge Cell 800za, etc.), occurrence count of Connection 853z1 and weights of all connections (i.e. Connection 853z1, Connection 853z2, and Connection 853z3, etc.) originating from Knowledge Cell 800za may be updated to account for this observation. The occurrence count of Connection 853z1 can be increased by 1 and its weight updated as 11/(11+15+1)=0.407. The weight of Connection 853z2 can also be updated as 15/(11+15+1)=0.556. The weight of Connection 853z3 can also be updated as 1/(11+15+1)=0.037.

Referring to Fig. 18, an embodiment of learning Knowledge Cells 800 comprising one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 using Collection of Knowledge Cells 530d is illustrated. Collection of Knowledge Cells 530d comprises the functionality for storing any number of Knowledge Cells 800. In some aspects, Knowledge Cells 800 may be stored into or applied onto Collection of Knowledge Cells 530d in a learning or training process. In effect, Collection of Knowledge Cells 530d may store Knowledge Cells 800 that can later be used to enable autonomous Device 98 operation. In some embodiments, Knowledge Structuring Unit 520 structures or generates Knowledge Cells 800 as previously described and the system applies them onto Collection of Knowledge Cells 530d, thereby implementing learning Device's 98 operation in various visual surroundings. The term apply or applying may refer to storing, copying, inserting, updating, or other similar action, therefore, these terms may be used interchangeably herein depending on context. The system can perform Similarity Comparisons 125 (later described) of a newly structured Knowledge Cell 800 from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Collection of Knowledge Cells 530d. If a substantially similar

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Knowledge Cell 800 is not found in Collection of Knowledge Cells 530d, the system may insert (i.e. copy, store, etc.) the Knowledge Cell 800 from Knowledge Structuring Unit 520 into Collection of Knowledge Cells 530d, for example. On the other hand, if a substantially similar Knowledge Cell 800 is found in Collection of Knowledge Cells 530d, the system may optionally omit inserting the Knowledge Cell 800 from Knowledge Structuring Unit 520 as inserting a substantially similar Knowledge Cell 800 may not add much or any additional knowledge to the Collection of Knowledge Cells 530d, for example. Also, inserting a substantially similar Knowledge Cell 800 can optionally be omitted to save storage resources and limit the number of Knowledge Cells 800 that may later need to be processed or compared. Any features, functionalities, and embodiments of Similarity Comparison 125, importance index (later described), similarity index (later described), and/or other disclosed elements can be utilized to facilitate determination of substantial or other similarity and whether to insert a newly structured Knowledge Cell 800 into Collection of Knowledge Cells 530d.

For example, the system can perform Similarity Comparisons 125 (later described) of Knowledge Cell 800ba from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Collection of Knowledge Cells 530d. In the case that a substantially similar match is found between Knowledge Cell 800ba and any of the Knowledge Cells 800 in Collection of Knowledge Cells 530d, the system may perform no action. The system can then perform Similarity Comparisons 125 of Knowledge Cell 800bb from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Collection of Knowledge Cells 530d. In the case that a substantially similar match is not found, the system may insert a new Knowledge Cell 800 into Collection of Knowledge Cells 530d and copy Knowledge Cell 800bb into the inserted new Knowledge Cell 800. The system can then perform Similarity Comparisons 125 of Knowledge Cell 800bc from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Collection of Knowledge Cells 530d. In the case that a substantially similar match is found between Knowledge Cell 800bc and any of the Knowledge Cells 800 in Collection of Knowledge Cells 530d, the system may perform no action. The system can then perform Similarity Comparisons 125 of Knowledge Cell 800bd from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Collection of Knowledge Cells 530d. In the case that a substantially similar match is not found, the system may insert a new Knowledge Cell 800 into Collection of Knowledge Cells 530d and copy Knowledge Cell 800bd into the inserted new Knowledge Cell 800. The system can then perform Similarity Comparisons 125 of Knowledge Cell 800be from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Collection of Knowledge Cells 530d. In the case that a substantially similar match is not found, the system may insert a new Knowledge Cell 800 into Collection of Knowledge Cells 530d and copy Knowledge Cell 800be into the inserted new Knowledge Cell 800. Applying any additional Knowledge Cells 800 from Knowledge Structuring Unit 520 onto Collection of Knowledge Cells 530d follows similar logic or process as the above-described.

Referring to Fig. 19, an embodiment of learning Knowledge Cells 800 comprising one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 using Neural Network 530a is illustrated. Neural Network 530a includes a number of neurons or Nodes 852 interconnected by Connections 853 as previously described. Knowledge Cells 800 are shown instead of Nodes 852 to simplify the illustration as Node 852 includes a Knowledge Cell 800, for example. Therefore, Knowledge Cells 800 and Nodes 852 can be used interchangeably herein depending on context. It should be noted that Node 852 may include other elements and/or functionalities instead of or in addition to Knowledge Cell 800. In some aspects, Knowledge Cells 800 may be stored into or applied onto Neural Network 530a individually or collectively in a learning or training process. In some designs,

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Neural Network 530a comprises a number of Layers 854 each of which may include one or more Knowledge Cells 800. Knowledge Cells 800 in successive Layers 854 can be connected by Connections 853. Connection 853 may include or be associated with occurrence count, weight, and/or other parameter or data as previously described. Neural Network 530a may include any number of Layers 854 comprising any number of Knowledge Cells 800. In some aspects, Neural Network 530a may store Knowledge Cells 800 interconnected by Connections 853 where following a path through the Neural Network 530a can later be used to enable autonomous Device 98 operation. It should be understood that, in some embodiments, Knowledge Cells 800 in one Layer 854 of Neural Network 530a need not be connected only with Knowledge Cells 800 in a successive Layer 854, but also in any other Layer 854, thereby creating shortcuts (i.e. shortcut Connections 853, etc.) through Neural Network 530a. A Knowledge Cell 800 can also be connected to itself such as, for example, in recurrent neural networks. In general, any Knowledge Cell 800 can be connected with any other Knowledge Cell 800 anywhere else in Neural Network 530a. In further embodiments, back-propagation of any data or information can be implemented. In one example, back-propagation of similarity (i.e. similarity index, etc.) of compared Knowledge Cells 800 in a path through Neural Network 530a can be implemented. In another example, back-propagation of errors can be implemented. Such back-propagations can then be used to adjust occurrence counts and/or weights of Connections 853 for better future predictions, for example. Any other back-propagation can be implemented for other purposes. Any combination of Nodes 852 (i.e. Nodes 852 comprising Knowledge Cells 800, etc.), Connections 853, Layers 854, and/or other elements or techniques can be implemented in alternate embodiments. Neural Network 530a may include any type or form of a neural network known in art such as a feed-forward neural network, a back-propagating neural network, a recurrent neural network, a convolutional neural network, deep neural network, and/or others including a custom neural network.

In some embodiments, Knowledge Structuring Unit 520 structures or generates Knowledge Cells 800 and the system applies them onto Neural Network 530a, thereby implementing learning Device's 98 operation in various visual surroundings. The system can perform Similarity Comparisons 125 (later described) of a Knowledge Cell 800 from Knowledge Structuring Unit 520 with Knowledge Cells 800 in a corresponding Layer 854 of Neural Network 530a. If a substantially similar Knowledge Cell 800 is not found in the corresponding Layer 854 of Neural Network 530a, the system may insert (i.e. copy, store, etc.) the Knowledge Cell 800 from Knowledge Structuring Unit 520 into the corresponding Layer 854 of Neural Network 530a, and create a Connection 853 to the inserted Knowledge Cell 800 from a Knowledge Cell 800 in a prior Layer 854 including assigning an occurrence count to the new Connection 853, calculating a weight of the new Connection 853, and updating any other Connections 853 originating from the Knowledge Cell 800 in the prior Layer 854. On the other hand, if a substantially similar Knowledge Cell 800 is found in the corresponding Layer 854 of Neural Network 530a, the system may update occurrence count and weight of Connection 853 to that Knowledge Cell 800 from a Knowledge Cell 800 in a prior Layer 854, and update any other Connections 853 originating from the Knowledge Cell 800 in the prior Layer 854.

For example, the system can perform Similarity Comparisons 125 (later described) of Knowledge Cell 800ba from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Layer 854a of Neural Network 530a. In the case that a substantially similar match is found between Knowledge Cell 800ba and Knowledge Cell 800ea, the system may perform no action since Knowledge Cell 800ea is the initial Knowledge Cell 800. The system can then perform Similarity Comparisons 125 of Knowledge Cell 800bb from Knowledge Structuring Unit 520 with Knowledge

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Cells 800 in Layer 854b of Neural Network 530a. In the case that a substantially similar match is found between Knowledge Cell 800bb and Knowledge Cell 800eb, the system may update occurrence count and weight of Connection 853e1 between Knowledge Cell 800ea and Knowledge Cell 800eb, and update weights of other Connections 853 originating from Knowledge Cell 800ea as previously described. The system can then perform Similarity Comparisons 125 of Knowledge Cell 800bc from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Layer 854c of Neural Network 530a. In the case that a substantially similar match is not found, the system may insert Knowledge Cell 800ec into Layer 854c and copy Knowledge Cell 800bc into the inserted Knowledge Cell 800ec. The system may also create Connection 853e2 between Knowledge Cell 800eb and Knowledge Cell 800ec with occurrence count of 1 and weight calculated based on the occurrence count as previously described. The system may also update weights of other Connections 853 (one in this example) originating from Knowledge Cell 800eb as previously described. The system can then perform Similarity Comparisons 125 of Knowledge Cell 800bd from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Layer 854d of Neural Network 530a. In the case that a substantially similar match is not found, the system may insert Knowledge Cell 800ed into Layer 854d and copy Knowledge Cell 800bd into the inserted Knowledge Cell 800ed. The system may also create Connection 853e3 between Knowledge Cell 800ec and Knowledge Cell 800ed with occurrence count of 1 and weight of 1. The system can then perform Similarity Comparisons 125 of Knowledge Cell 800be from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Layer 854e of Neural Network 530a. In the case that a substantially similar match is not found, the system may insert Knowledge Cell 800ee into Layer 854e and copy Knowledge Cell 800be into the inserted Knowledge Cell 800ee. The system may also create Connection 853e4 between Knowledge Cell 800ed and Knowledge Cell 800ee with occurrence count of 1 and weight of 1. Applying any additional Knowledge Cells 800 from Knowledge Structuring Unit 520 onto Neural Network 530a follows similar logic or process as the above-described.

Similarity Comparison 125 comprises the functionality for comparing or matching Knowledge Cells 800 or portions thereof, and/or other functionalities. Similarity Comparison 125 comprises the functionality for comparing or matching Digital Pictures 525 or portions thereof. Similarity Comparison 125 comprises the functionality for comparing or matching streams of Digital Pictures 525 or portions thereof. Similarity Comparison 125 comprises the functionality for comparing or matching Instruction Sets 526, Extra Info 527, text (i.e. characters, words, phrases, etc.), pictures, sounds, data, and/or other elements or portions thereof. Similarity Comparison 125 may include functions, rules, and/or logic for performing matching or comparisons and for determining that while a perfect match is not found, a similar or partial match has been found. In some aspects, Similarity Comparison 125 may include determining substantial similarity or substantial match of compared elements. In other aspects, a partial match may include a substantial or otherwise similar match, and vice versa. Although, substantial similarity or substantial match is frequently used herein, it should be understood that any level of similarity, however high or low, may be utilized as defined by the rules (i.e. thresholds, etc.) for similarity. The rules for similarity or similar match can be defined by a user, by VSADO system administrator, or automatically by the system based on experience, testing, inquiry, analysis, synthesis, or other techniques, knowledge, or input. In some designs, Similarity Comparison 125 comprises the functionality to automatically define appropriately strict rules for determining similarity of the compared elements. Similarity Comparison 125 can therefore set, reset, and/or adjust the strictness of the rules for finding or determining similarity of the compared elements, thereby fine tuning Similarity Comparison 125 so that the rules for

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determining similarity are appropriately strict. In some aspects, the rules for determining similarity may include a similarity threshold. As such, Similarity Comparison 125 can determine similarity of compared elements if their similarity exceeds a similarity threshold. In other aspects, the rules for determining similarity may include a difference threshold. As such, Similarity Comparison 125 can determine similarity of compared elements if their difference is lower than a difference threshold. In further aspects, the rules for determining similarity may include other thresholds.

In some embodiments, in determining similarity of Knowledge Cells 800, Similarity Comparison 125 can compare one or more Digital Pictures 525 or portions (i.e. regions, features, pixels, etc.) thereof from one Knowledge Cell 800 with one or more Digital Pictures 525 or portions thereof from another Knowledge Cell 800. In some aspects, total equivalence is achieved when all Digital Pictures 525 or portions thereof of the compared Knowledge Cells 800 match. If total equivalence is not found, Similarity Comparison 125 may attempt to determine substantial or other similarity. Any features, functionalities, and embodiments of the previously described Picture Recognizer 350 can be used in determining such substantial similarity.

In some embodiments where compared Knowledge Cells 800 include a single Digital Picture 525, Similarity Comparison 125 can compare Digital Picture 525 from one Knowledge Cell 800 with Digital Picture 525 from another Knowledge Cell 800 using comparison techniques for individual pictures described below. In some embodiments where compared Knowledge Cells 800 include streams of Digital Pictures 525 (i.e. motion pictures, videos, etc.), Similarity Comparison 125 can compare a stream of Digital Pictures 525 from one Knowledge Cell 800 with a stream of Digital Pictures 525 from another Knowledge Cell 800. Such comparison may include comparing Digital Pictures 525 from one Knowledge Cell 800 with corresponding (i.e. similarly positioned, temporally related, etc.) Digital Pictures 525 from another Knowledge Cell 800. In one example, a 67th Digital Picture 525 from one Knowledge Cell 800 can be compared with a 67th Digital Picture 525 from another Knowledge Cell 800. In another example, a 67th Digital Picture 525 from one Knowledge Cell 800 can be compared with a number of Digital Picture 525 around (i.e. preceding and/or following) a 67th Digital Picture 525 from another Knowledge Cell 800. This way, flexibility can be implemented in finding a substantially similar Digital Picture 525 if the Digital Pictures 525 in the compared Knowledge Cells 800 are not perfectly aligned. In other aspects, Similarity Comparison 125 can utilize Dynamic Time Warping (DTW) and/or other techniques know in art for comparing and/or aligning temporal sequences (i.e. streams of Digital Pictures 525, etc.) that may vary in time or speed. Once the corresponding (i.e. similarly positioned, temporally related, time warped/aligned, etc.) Digital Pictures 525 in the compared streams of Digital Pictures 525 are compared and their substantial similarity determined using comparison techniques for individual pictures described below, Similarity Comparison 125 can utilize a threshold for the number or percentage of matching or substantially matching Digital Pictures 525 for determining substantial similarity of the compared Knowledge Cells 800. In some aspects, substantial similarity can be achieved when most of the Digital Pictures 525 or portions (i.e. regions, features, pixels, etc.) thereof of the compared Knowledge Cells 800 match or substantially match. In other aspects, substantial similarity can be achieved when at least a threshold number or percentage of Digital Pictures 525 or portions thereof of the compared Knowledge Cells 800 match or substantially match. Similarly, substantial similarity can be achieved when a number or percentage of matching or substantially matching Digital Pictures 525 or portions thereof of the compared Knowledge Cells 800 exceeds a threshold. In further aspects, substantial similarity can be achieved when all but a threshold number or percentage of Digital Pictures 525

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or portions thereof of the compared Knowledge Cells 800 match or substantially match. Such thresholds can be defined by a user, by VSADO system administrator, or automatically by the system based on experience, testing, inquiry, analysis, synthesis, or other techniques, knowledge, or input. In one example, substantial similarity can be achieved when at least 1, 2, 3, 4, or any other threshold number of Digital Pictures 525 or portions thereof of the compared Knowledge Cells 800 match or substantially match. Similarly, substantial similarity can be achieved when the number of matching or substantially matching Digital Pictures 525 or portions thereof of the compared Knowledge Cells 800 exceeds 1, 2, 3, 4, or any other threshold number. In another example, substantial similarity can be achieved when at least 10%, 21%, 30%, 49%, 66%, 89%, 93%, or any other percentage of Digital Pictures 525 or portions thereof of the compared Knowledge Cells 800 match or substantially match. Similarly, substantial similarity can be achieved when the percentage of matching or substantially matching Digital Pictures 525 or portions thereof of the compared Knowledge Cells 800 exceeds 10%, 21%, 30%, 49%, 66%, 89%, 93%, or any other threshold percentage. In other embodiments, substantial similarity of the compared Knowledge Cells 800 can be achieved in terms of matches or substantial matches in more important (i.e. as indicated by importance index [later described], etc.) Digital Pictures 525 or portions thereof, thereby tolerating mismatches in less important Digital Pictures 525 or portions thereof. In one example, substantial similarity can be achieved when matches or substantial matches are found with respect to more substantive Digital Pictures 525 (i.e. pictures comprising content of interest [i.e. persons, objects, etc.], etc.) or portions thereof of the compared Knowledge Cells 800, thereby tolerating mismatches in less substantive Digital Pictures 525 (i.e. pictures comprising background, insignificant content, etc.) or portions thereof. In another example, substantial similarity can be achieved when matches or substantial matches are found in earlier Digital Pictures 525 or portions thereof of the compared Knowledge Cells 800, thereby tolerating mismatches in later Digital Pictures 525 or portions thereof. In general, any importance or weight can be assigned to any Digital Picture 525 or portion thereof, and/or other elements. In some designs, Similarity Comparison 125 can be configured to omit any Digital Picture 525 or portion thereof from the comparison. In one example, less substantive Digital Pictures 525 or portions thereof can be omitted. In another example, some or all Digital Pictures 525 or portions thereof related to a specific time period can be omitted. In a further example, later Digital Pictures 525 or portions thereof can be omitted. In further embodiments, substantial similarity can be achieved taking into account the number of Digital Pictures 525 of the compared Knowledge Cells 800. For example, substantial similarity can be achieved if the number, in addition to the content, of Digital Pictures 525 of the compared Knowledge Cells 800 match or substantially match. In further embodiments, substantial similarity can be achieved taking into account the objects detected within Digital Pictures 525 and/or other features of Digital Pictures 525 of the compared Knowledge Cells 800. For example, substantial similarity can be achieved if same or similar objects are detected in Digital Pictures 525 of the compared Knowledge Cells 800. Any features, functionalities, and embodiments of Picture Recognizer 350 can be used in such detection. In some aspects, Similarity Comparison 125 can compare the number, objects detected, and/or other features of Digital Pictures 525 as an initial check before proceeding to further detailed comparisons.

Similarity Comparison 125 can automatically adjust (i.e. increase or decrease) the strictness of the rules for determining substantial similarity of Knowledge Cells 800. In some aspects, such adjustment in strictness can be done by Similarity Comparison 125 in response to determining that total equivalence of compared Knowledge Cells 800 had not been found. Similarity Comparison 125 can keep adjusting the strictness of the rules until substantially

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similarity is found. All the rules or settings of substantial similarity can be set, reset, or adjusted by Similarity Comparison 125 in response to another strictness level determination. For example, Similarity Comparison 125 may attempt to find a match or substantial match in a certain percentage (i.e. 95%, etc.) of Digital Pictures 525 or portions thereof from the compared Knowledge Cells 800. If the comparison does not determine substantial similarity of compared Knowledge Cells 800, Similarity Comparison 125 may decide to decrease the strictness of the rules. In response, Similarity Comparison 125 may attempt to find fewer matching or substantially matching Digital Pictures 525 or portions thereof than in the previous attempt using stricter rules. If the comparison still does not determine substantial similarity of compared Knowledge Cells 800, Similarity Comparison 125 may further decrease (i.e. down to a certain minimum strictness or threshold, etc.) the strictness by requiring fewer Digital Pictures 525 or portions thereof to match or substantially match, thereby further increasing a chance of finding substantial similarity in compared Knowledge Cells 800. In further aspects, an adjustment in strictness can be done by Similarity Comparison 125 in response to determining that multiple substantially similar Knowledge Cells 800 had been found. Similarity Comparison 125 can keep adjusting the strictness of the rules until a best of the substantially similar Knowledge Cells 800 is found. For example, Similarity Comparison 125 may attempt to find a match or substantial match in a certain percentage (i.e. 70%, etc.) of Digital Pictures 525 or portions thereof from the compared Knowledge Cells 800. If the comparison determines a number of substantially similar Knowledge Cells 800, Similarity Comparison 125 may decide to increase the strictness of the rules to decrease the number of substantially similar Knowledge Cells 800. In response, Similarity Comparison 125 may attempt to find more matching or substantially matching Digital Pictures 525 or portions thereof in addition to the earlier found Digital Pictures 525 or portions thereof to limit the number of substantially similar Knowledge Cells 800. If the comparison still provides more than one substantially similar Knowledge Cell 800, Similarity Comparison 125 may further increase the strictness by requiring additional Digital Pictures 525 or portions thereof to match or substantially match, thereby further narrowing the number of substantially similar Knowledge Cells 800 until a best substantially similar Knowledge Cells 800 is found.

In some embodiments, in determining substantial similarity of individual Digital Pictures 525 (i.e. Digital Pictures 525 from the compared Knowledge Cells 800, etc.), Similarity Comparison 125 can compare one or more regions of one Digital Picture 525 with one or more regions of another Digital Picture 525. A region may include a collection of pixels. In some aspects, a region may include detected or recognized content of interest such as an object or person. Such region may be detected using any features, functionalities, and embodiments of Picture Recognizer 350. In other aspects, a region may include content defined using a picture segmentation technique. Examples of picture segmentation techniques include thresholding, clustering, region-growing, edge detection, curve propagation, level sets, graph partitioning, model-based segmentation, trainable segmentation (i.e. artificial neural networks, etc.), and/or others. In further aspects, a region may include content defined using any technique. In further aspects, a region may include any arbitrary region comprising any arbitrary content. Once regions of the compared Digital Pictures 525 are known, Similarity Comparison 125 can compare the regions to determine substantial similarity of the compared Digital Pictures 525. In some aspects, total equivalence is found when all regions of one Digital Picture 525 match all regions of another Digital Picture 525. In other aspects, if total equivalence is not found, Similarity Comparison 125 may attempt to determine substantial similarity of compared Digital Pictures 525. In one example, substantial similarity can be achieved when most of the regions of the

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compared Digital Picture 525 match or substantially match. In another example, substantial similarity can be achieved when at least a threshold number (i.e. 1, 2, 5, 11, 39, etc.) or percentage (i.e. 38%, 63%, 77%, 84%, 98%, etc.) of regions of the compared Digital Pictures 525 match or substantially match. Similarly, substantial similarity can be achieved when the number or percentage of matching or substantially matching regions of the compared Digital Pictures 525 exceeds a threshold number (i.e. 1, 2, 5, 11, 39, etc.) or a threshold percentage (i.e. 48%, 63%, 77%, 84%, 98%, etc.). In a further example, substantial similarity can be achieved when all but a threshold number or percentage of regions of the compared Digital Pictures 525 match or substantially match. Such thresholds can be defined by a user, by VSADO system administrator, or automatically by the system based on experience, testing, inquiry, analysis, synthesis, and/or other techniques, knowledge, or input. In further aspects, Similarity Comparison 125 can utilize the type of regions for determining substantial similarity of Digital Pictures 525. For example, substantial similarity can be achieved when matches or substantial matches are found with respect to more substantive, larger, and/or other regions, thereby tolerating mismatches in less substantive, smaller, and/or other regions. In further aspects, Similarity Comparison 125 can utilize the importance (i.e. as indicated by importance index [later described], etc.) of regions for determining substantial similarity of Digital Pictures 525. For example, substantial similarity can be achieved when matches or substantial matches are found with respect to more important regions such as the above described more substantive, larger, and/or other regions, thereby tolerating mismatches in less important regions such as less substantive, smaller, and/or other regions. In further aspects, Similarity Comparison 125 can omit some of the regions from the comparison in determining substantial similarity of Digital Pictures 525. In one example, isolated regions can be omitted from comparison. In another example, less substantive or smaller regions can be omitted from comparison. In general, any region can be omitted from comparison. In further aspects, Similarity Comparison 125 can focus on certain regions of interest from the compared Digital Pictures 525. For example, substantial similarity can be achieved when matches or substantial matches are found with respect to regions comprising persons or parts (i.e. head, arm, leg, etc.) thereof, large objects, close objects, and/or other content of interest, thereby tolerating mismatches in regions comprising the background, insignificant content, and/or other content. In further aspects, Similarity Comparison 125 can detect or recognize persons or objects in the compared Digital Pictures 525 using regions. Any features, functionalities, and embodiments of Picture Recognizer 350 can be used in such detection or recognition. Once a person or object is detected in a Digital Picture 525, Similarity Comparison 125 may attempt to detect the person or object in the compared Digital Picture 525. In one example, substantial similarity can be achieved when the compared Digital Pictures 525 comprise one or more same persons or objects. In another example concerning streams of Digital Pictures 525, substantial similarity can be achieved when the compared streams of Digital Pictures 525 comprise a detected person or object in at least a threshold number or percentage of their pictures.

Similarity Comparison 125 can automatically adjust (i.e. increase or decrease) the strictness of the rules for determining substantial similarity of Digital Pictures 525 using regions. In some aspects, such adjustment in strictness can be done by Similarity Comparison 125 in response to determining that total equivalence of compared Digital Pictures 525 had not been found. Similarity Comparison 125 can keep adjusting the strictness rules until a substantial similarity is found. All the rules or settings of substantial similarity can be set, reset, or adjusted by Similarity Comparison 125 in response to another strictness level determination. For example, Similarity Comparison 125 may attempt to find a match or substantial match in a certain percentage (i.e. 74%, etc.) of regions from the

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compared Digital Pictures 525. If the comparison does not determine substantial similarity of compared Digital Pictures 525, Similarity Comparison 125 may decide to decrease the strictness of the rules. In response, Similarity Comparison 125 may attempt to find fewer matching or substantially matching regions than in the previous attempt using stricter rules. If the comparison still does not determine substantial similarity of compared Digital Pictures 525, Similarity Comparison 125 may further decrease the strictness (i.e. down to a certain minimum strictness or threshold, etc.) by requiring fewer regions to match or substantially match, thereby further increasing a chance of finding substantial similarity in compared Digital Pictures 525.

Where a reference to a region is used herein it should be understood that a portion of a region or a collection of regions can be used instead of or in addition to the region. In one example, instead of or in addition to regions, individual pixels and/or features that constitute a region can be compared. In another example, instead of or in addition to regions, collections of regions can be compared. As such, any operations, rules, logic, and/or functions operating on regions similarly apply to any portion of a region and/or any collection of regions. In general, whole regions, portions of a region, and/or collections of regions, including any operations thereon, can be combined to arrive at desired results. Some or all of the above-described rules, logic, and/or techniques can be utilized alone or in combination with each other or with other rules, logic, and/or techniques. One of ordinary skill in art will recognize that other techniques known in art for determining similarity of digital pictures, streams of digital pictures, and/or other data that would be too voluminous to describe are within the scope of this disclosure.

In some embodiments, in determining substantial similarity of individual Digital Pictures 525 (i.e. Digital Pictures 525 from the compared Knowledge Cells 800, etc.), Similarity Comparison 125 can compare one or more features of one Digital Picture 525 with one or more features of another Digital Picture 525. A feature may include a collection of pixels. Some of the steps or elements in a feature oriented technique include pre-processing, feature extraction, detection/segmentation, decision-making, and/or others, or a combination thereof, each of which may include its own sub-steps or sub-elements depending on the application. Examples of features that can be used include lines, edges, ridges, corners, blobs, and/or others. Examples of feature extraction techniques include Canny, Sobe, Kayyali, Harris & Stephens et al, SUSAN, Level Curve Curvature, FAST, Laplacian of Gaussian, Difference of Gaussians, Determinant of Hessian, MSER, PCBR, Grey-level Blobs, and/or others. Once features of the compared Digital Pictures 525 are known, Similarity Comparison 125 can compare the features to determine substantial similarity. In some aspects, total equivalence is found when all features of one Digital Picture 525 match all features of another Digital Picture 525. In other aspects, if total equivalence is not found, Similarity Comparison 125 may attempt to determine substantial similarity of compared Digital Pictures 525. In one example, substantial similarity can be achieved when most of the features of the compared Digital Picture 525 match or substantially match. In another example, substantial similarity can be achieved when at least a threshold number (i.e. 3, 22, 47, 93, 128, 431, etc.) or percentage (i.e. 49%, 53%, 68%, 72%, 95%, etc.) of features of the compared Digital Pictures 525 match or substantially match. Similarly, substantial similarity can be achieved when the number or percentage of matching or substantially matching features of the compared Digital Pictures 525 exceeds a threshold number (i.e. 3, 22, 47, 93, 128, 431, etc.) or a threshold percentage (i.e. 49%, 53%, 68%, 72%, 95%, etc.). In a further example, substantial similarity can be achieved when all but a threshold number or percentage of features of the compared Digital Pictures 525 match or substantially match. Such thresholds can be defined by a user, by VSADO system administrator, or automatically by the system based on experience, testing, inquiry, analysis, synthesis, and/or other

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techniques, knowledge, or input. In further aspects, Similarity Comparison 125 can utilize the type of features for determining substantial similarity of Digital Pictures 525. In one example, substantial similarity can be achieved when matches or substantial matches are found with respect to edges, thereby tolerating mismatches in blobs. In another example, substantial similarity can be achieved when matches or substantial matches are found with respect to more substantive, larger, and/or other features, thereby tolerating mismatches in less substantive, smaller, and/or other features. In further aspects, Similarity Comparison 125 can utilize the importance (i.e. as indicated by importance index [later described], etc.) of features for determining substantial similarity of Digital Pictures 525. For example, substantial similarity can be achieved when matches or substantial matches are found with respect to more important features such as the above described more substantive, larger, and/or other features, thereby tolerating mismatches in less important features such as less substantive, smaller, and/or other features. In further aspects, Similarity Comparison 125 can omit some of the features from the comparison in determining substantial similarity of Digital Pictures 525. In one example, isolated features can be omitted from comparison. In another example, less substantive or smaller features can be omitted from comparison. In general, any feature can be omitted from comparison. In further aspects, Similarity Comparison 125 can focus on features in certain regions of interest of the compared Digital Pictures 525. For example, substantial similarity can be achieved when matches or substantial matches are found with respect to features in regions comprising persons or parts (i.e. head, arm, leg, etc.) thereof, large objects, close objects, and/or other objects, thereby tolerating mismatches in features of regions comprising the background, insignificant content, and/or other regions. In further aspects, Similarity Comparison 125 can detect or recognize persons or objects in the compared Digital Pictures 525. Any features, functionalities, and embodiments of Picture Recognizer 350 can be used in such detection or recognition. Once a person or object is detected in a Digital Picture 525, Similarity Comparison 125 may attempt to detect the person or object in the compared Digital Picture 525. In one example, substantial similarity can be achieved when the compared Digital Pictures 525 comprise one or more same persons or objects. In another example concerning streams of Digital Pictures 525, substantial similarity can be achieved when the compared streams of Digital Pictures 525 comprise a detected person or object in at least a threshold number or percentage of their pictures. In further aspects, Similarity Comparison 125 may include identifying and/or analyzing tiled and/or overlapping features, which can then be combined (i.e. similar to some process steps in convolutional neural networks, etc.) and compared to determine substantial similarity of Digital Pictures 525.

Similarity Comparison 125 can automatically adjust (i.e. increase or decrease) the strictness of the rules for determining substantial similarity of Digital Pictures 525 using features. In some aspects, such adjustment in strictness can be done by Similarity Comparison 125 in response to determining that total equivalence of compared Digital Pictures 525 had not been found. Similarity Comparison 125 can keep adjusting the strictness rules until a substantial similarity is found. All the rules or settings of substantial similarity can be set, reset, or adjusted by Similarity Comparison 125 in response to another strictness level determination. For example, Similarity Comparison 125 may attempt to find a match or substantial match in a certain percentage (i.e. 89%, etc.) of features from the compared Digital Pictures 525. If the comparison does not determine substantial similarity of compared Digital Pictures 525, Similarity Comparison 125 may decide to decrease the strictness of the rules. In response, Similarity Comparison 125 may attempt to find fewer matching or substantially matching features than in the previous attempt using stricter rules. If the comparison still does not determine substantial similarity of compared Digital Pictures 525,

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Similarity Comparison 125 may further decrease the strictness (i.e. down to a certain minimum strictness or threshold, etc.) by requiring fewer features to match or substantially match, thereby further increasing a chance of finding substantial similarity in compared Digital Pictures 525.

Where a reference to a feature is used herein it should be understood that a portion of a feature or a collection of features can be used instead of or in addition to the feature. In one example, instead of or in addition to features, individual pixels that constitute a feature can be compared. In another example, instead of or in addition to features, collections of features can be compared. In a further example, levels of features where a feature on one level includes one or more features from another level (i.e. prior level, etc.) can be compared. As such, any operations, rules, logic, and/or functions operating on features similarly apply to any portion of a feature and/or any collection of features. In general, whole features, portions of a feature, and/or collections of features, including any operations thereon, can be combined to arrive at desired results. Some or all of the above-described rules, logic, and/or techniques can be utilized alone or in combination with each other or with other rules, logic, and/or techniques. One of ordinary skill in art will recognize that other techniques known in art for determining similarity of digital pictures, streams of digital pictures, and/or other data that would be too voluminous to describe are within the scope of this disclosure.

In some embodiments, in determining substantial similarity of individual Digital Pictures 525 (i.e. Digital Pictures 525 from the compared Knowledge Cells 800, etc.), Similarity Comparison 125 can compare pixels of one Digital Picture 525 with pixels of another Digital Picture 525. In some aspects, total equivalence is found when all pixels of one Digital Picture 525 match all pixels of another Digital Picture 525. In other aspects, if total equivalence is not found, Similarity Comparison 125 may attempt to determine substantial similarity. In one example, substantial similarity can be achieved when most of the pixels from the compared Digital Pictures 525 match or substantially match. In another example, substantial similarity can be achieved when at least a threshold number (i.e. 449, 2219, 92229, 442990, 1000028, etc.) or percentage (i.e. 39%, 45%, 58%, 72%, 92%, etc.) of pixels from the compared Digital Pictures 525 match or substantially match. Similarly, substantial similarity can be achieved when the number or percentage of matching or substantially matching pixels from the compared Digital Pictures 525 exceeds a threshold number (i.e. 449, 2219, 92229, 442990, 1000028, etc.) or a threshold percentage (i.e. 39%, 45%, 58%, 72%, 92%, etc.). In a further example, substantial similarity can be achieved when all but a threshold number or percentage of pixels from the compared Digital Pictures 525 match or substantially match. Such thresholds can be defined by a user, by VSADO system administrator, or automatically by the system based on experience, testing, inquiry, analysis, synthesis, and/or other techniques, knowledge, or input. In further aspects, Similarity Comparison 125 can omit some of the pixels from the comparison in determining substantial similarity of Digital Pictures 525. In one example, pixels composing the background or any insignificant content can be omitted from comparison. In general, any pixel can be omitted from comparison. In further aspects, Similarity Comparison 125 can focus on pixels in certain regions of interest in determining substantial similarity of Digital Pictures 525. For example, substantial similarity can be achieved when matches or substantial matches are found with respect to pixels in regions comprising persons or parts (i.e. head, arm, leg, etc.) thereof, large objects, close objects, and/or other content of interest, thereby tolerating mismatches in pixels in regions comprising the background, insignificant content, and/or other content.

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Similarity Comparison 125 can automatically adjust (i.e. increase or decrease) the strictness of the rules for determining substantial similarity of Digital Pictures 525 using pixels. In some aspects, such adjustment in strictness can be done by Similarity Comparison 125 in response to determining that total equivalence of compared Digital Pictures 525 had not been found. Similarity Comparison 125 can keep adjusting the strictness rules until a substantial similarity is found. All the rules or settings of substantial similarity can be set, reset, or adjusted by Similarity Comparison 125 in response to another strictness level determination. For example, Similarity Comparison 125 may attempt to find a match or substantial match in a certain percentage (i.e. 77%, etc.) of pixels from the compared Digital Pictures 525. If the comparison does not determine substantial similarity of compared Digital Pictures 525, Similarity Comparison 125 may decide to decrease the strictness of the rules. In response, Similarity Comparison 125 may attempt to find fewer matching or substantially matching pixels than in the previous attempt using stricter rules. If the comparison still does not determine substantial similarity of compared Digital Pictures 525, Similarity Comparison 125 may further decrease the strictness (i.e. down to a certain minimum strictness or threshold, etc.) by requiring fewer pixels to match or substantially match, thereby further increasing a chance of finding substantial similarity in compared Digital Pictures 525.

Where a reference to a pixel is used herein it should be understood that a collection of pixels can be used instead of or in addition to the pixel. For example, instead of or in addition to pixels, collections of pixels can be compared. As such, any operations, rules, logic, and/or functions operating on pixels similarly apply to any collection of pixels. In general, pixels and/or collections of pixels, including any operations thereon, can be combined to arrive at desired results. Some or all of the above-described rules, logic, and/or techniques can be utilized alone or in combination with each other or with other rules, logic, and/or techniques. Any of the previously described features, functionalities, and embodiments of Similarity Comparison 125 for determining substantial similarity of Digital Pictures 525 using regions and/or features can similarly be used for pixels. One of ordinary skill in art will recognize that other techniques known in art for determining similarity of digital pictures, streams of digital pictures, and/or other data that would be too voluminous to describe are within the scope of this disclosure.

Other aspects or properties of digital pictures or pixels can be taken into account by Similarity Comparison 125 in digital picture comparisons. Examples of such aspects or properties include color adjustment, size adjustment, content manipulation, transparency (i.e. alpha channel, etc.), use of mask, and/or others. In some implementations, as digital pictures can be captured by various picture taking equipment, in various environments, and under various lighting conditions, Similarity Comparison 125 can adjust lighting or color of pixels or otherwise manipulate pixels before or during comparison. Lighting or color adjustment (also referred to as gray balance, neutral balance, white balance, etc.) may generally include manipulating or rebalancing the intensities of the colors (i.e. red, green, and/or blue if RGB color model is used, etc.) of one or more pixels. For example, Similarity Comparison 125 can adjust lighting or color of all pixels of one picture to make it more comparable to another picture. Similarity Comparison 125 can also incrementally adjust the pixels such as increasing or decreasing the red, green, and/or blue pixel values by a certain amount in each cycle of comparisons in order to find a substantially similar match at one of the incremental adjustment levels. Any of the publically available, custom, or other lighting or color adjustment techniques or programs can be utilized such as color filters, color balancing, color correction, and/or others. In other implementations, Similarity Comparison 125 can resize or otherwise transform a digital picture before or during comparison. Such resizing or transformation may include increasing or decreasing the

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number of pixels of a digital picture. For example, Similarity Comparison 125 can increase or decrease the size of a digital picture proportionally (i.e. increase or decrease length and/or width keeping aspect ratio constant, etc.) to equate its size with the size of another digital picture. Similarity Comparison 125 can also incrementally resize a digital picture such as increasing or decreasing the size of the digital picture proportionally by a certain amount in each cycle of comparisons in order to find a substantially similar match at one of the incremental sizes. Any of the publically available, custom, or other digital picture resizing techniques or programs can be utilized such as nearest-neighbor interpolation, bilinear interpolation, bicubic interpolation, and/or others. In further implementations, Similarity Comparison 125 can manipulate content (i.e. all pixels, one or more regions, one or more depicted objects/persons, etc.) of a digital picture before or during comparison. Such content manipulation may include moving, centering, aligning, resizing, transforming, and/or otherwise manipulating content of a digital picture. For example, Similarity Comparison 125 can move, center, or align content of one picture to make it more comparable to another picture. Any of the publically available, custom, or other digital picture manipulation techniques or programs can be utilized such as pixel moving, warping, distorting, aforementioned interpolations, and/or others. In further implementations, in digital pictures comprising transparency features or functionalities, Similarity Comparison 125 can utilize a threshold for acceptable number or percentage transparency difference similar to the below-described threshold for the acceptable color difference. Alternatively, transparency can be applied to one or more pixels of a digital picture and color difference may then be determined between compared pixels taking into account the transparency related color effect. Alternatively, transparent pixels can be excluded from comparison. In further implementations, certain regions or subsets of pixels can be ignored or excluded during comparison using a mask. In general, any region or subset of a picture determined to contain no content of interest can be excluded from comparison using a mask. Examples of such regions or subsets include background, transparent or partially transparent regions, regions comprising insignificant content, or any arbitrary region or subset. Similarity Comparison 125 can perform any other pre-processing or manipulation of digital pictures or pixels before or during comparison.

In any of the comparisons involving digital pictures or pixels, Similarity Comparison 125 can utilize a threshold for acceptable number or percentage difference in determining a match for each compared pixel. A pixel in a digital picture can be encoded using various techniques such as RGB (i.e. red, green, blue), CMYK (i.e. cyan, magenta, yellow, and key [black]), binary value, hexadecimal value, numeric value, and/or others. For instance, in RGB color scheme, each of red, green, and blue colors is encoded with a value 0-255 or its binary equivalent. In one example, a threshold for acceptable difference (i.e. absolute difference, etc.) can be set at 10 for each of the three colors. Therefore, a pixel encoded as R130, G240, B50 matches or is sufficiently similar to a compared pixel encoded as R135, G231, B57 because the differences in all three colors fall within the acceptable difference threshold (i.e. 10 in this example, etc.). Furthermore, a pixel encoded as R130, G240, B50 does not match or is not sufficiently similar to a compared pixel encoded as R143, G231, B57 because the difference in red value falls outside the acceptable difference threshold. Any other number threshold can be used such as 1, 3, 8, 15, 23, 77, 132, 197, 243, and/or others. A threshold for acceptable percentage difference can similarly be utilized such as 0.12%, 2%, 7%, 14%, 23%, 36%, 65%, and/or others. In some aspects, a threshold for acceptable number or percentage difference in red, green, and blue can be set to be different for each color. A similar difference determination can be utilized in pixels encoded in any other color scheme. The aforementioned thresholds can be

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defined by a user, by VSADO system administrator, or automatically by the system based on experience, testing, inquiry, analysis, synthesis, or other techniques, knowledge, or input.

In some embodiments, Similarity Comparison 125 can compare one or more Extra Info 527 (i.e. time information, location information, computed information, observed information, sensory information, contextual information, and/or other information, etc.) in addition to or instead of comparing Digital Pictures 525 or portions thereof in determining substantial similarity of Knowledge Cells 800. Extra Info 527 can be set to be less, equally, or more important (i.e. as indicated by importance index [later described], etc.) than Digital Pictures 525, regions, features, pixels, and/or other elements in the comparison. Since Extra Info 527 may include any contextual or other information that can be useful in determining similarity of any compared elements, Extra Info 527 can be used to enhance any of the aforementioned similarity determinations.

In some embodiments, Similarity Comparison 125 can also compare one or more Instruction Sets 526 in addition to or instead of comparing Digital Pictures 525 or portions thereof in determining substantial similarity of Knowledge Cells 800. In some aspects, Similarity Comparison 125 can compare portions of Instruction Sets 526 to determine substantial similarity of Instruction Sets 526. Similar thresholds for the number or percentage of matching portions of the compared Instruction Sets 526 can be utilized in Instruction Set 526 comparisons. Such thresholds can be defined by a user, by VSADO system administrator, or automatically by the system based on experience, testing, inquiry, analysis, synthesis, and/or other techniques, knowledge, or input. In other aspects, Similarity Comparison 125 can compare text (i.e. character comparison, word/phrase search/comparison, semantic comparison, etc.) or other data (i.e. bit comparison, object or data structure comparison, etc.) to determine substantial similarity of Instruction Sets 526. Any other comparison technique can be utilized in comparing Instruction Sets 526 in alternate implementations. Instruction Sets 526 can be set to be less, equally, or more important (i.e. as indicated by importance index [later described], etc.) than Digital Pictures 525, regions, features, pixels, Extra Info 527, and/or other elements in the comparison.

In some embodiments, an importance index (not shown) or other importance ranking technique can be used in any of the previously described comparisons or other processing involving elements of different importance. Importance index indicates importance of the element to or with which the index is assigned or associated. For example, importance index may indicate importance of a Knowledge Cell 800, Digital Picture 525, Instruction Set 526, Extra Info 527, region, feature, and/or other element to or with which the index is assigned or associated. In some aspects, importance index on a scale from 0 to 1 can be utilized, although, any other range can also be utilized. Importance index can be stored in or associated with the element to which the index pertains. Association of importance indexes can be implemented using a table where one column comprises elements and another column comprises their associated importance indexes, for example. Importance indexes of various elements can be defined by a user, by VSADO system administrator, or automatically by the system based on experience, testing, inquiry, analysis, synthesis, or other techniques, knowledge, or input. In one example, a higher Importance index can be assigned to more substantive Digital Pictures 525 (i.e. pictures comprising content of interest [i.e. persons, objects, etc.], etc.). In another example, a higher importance index can be assigned to Digital Pictures 525 that are correlated with Instruction Sets 526. Any importance index can be assigned to or associated with any element described herein. Any importance ranking technique can be utilized as or instead of importance index in alternate embodiments.

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In some embodiments, Similarity Comparison 125 may generate a similarity index (not shown) for any compared elements. Similarity index indicates how well an element is matched with another element. For example, similarity index indicates how well a Knowledge Cell 800, Digital Picture 527, Instruction Set 526, Extra Info 527, region, feature, and/or other element is matched with a compared element. In some aspects, similarity index on a scale from 0 to 1 can be utilized, although, any other range can also be utilized. Similarity index can be generated by Similarity Comparison 125 whether substantial or other similarity between the compared elements is achieved or not. In one example, similarity index can be determined for a Knowledge Cell 800 based on a ratio/percentage of matched or substantially matched Digital Pictures 525 relative to the number of Digital Pictures 525 in the compared Knowledge Cell 800. Specifically, similarity index of 0.93 is determined if 93% of Digital Pictures 525 of one Knowledge Cell 800 match or substantially match Digital Pictures 525 of another Knowledge Cell 800. In some designs, importance (i.e. as indicated by importance index, etc.) of one or more Digital Pictures 525 can be included in the calculation of a weighted similarity index. Similar determination of similarity index can be implemented with Digital Pictures 525, Instruction Sets 526, Extra Info 527, regions, features, pixels, and/or other elements or portions thereof. Any combination of the aforementioned similarity index determinations or calculations can be utilized in alternate embodiments. Any similarity ranking technique can be utilized to determine or calculate similarity index in alternate embodiments.

Referring to Fig. 20, an embodiment of learning Knowledge Cells 800 comprising one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 using Neural Network 530a comprising shortcut Connections 853 is illustrated. In some designs, Knowledge Cells 800 in one Layer 854 of Neural Network 530a can be connected with Knowledge Cells 800 in any Layer 854, not only in a successive Layer 854, thereby creating shortcuts (i.e. shortcut Connections 853, etc.) through Neural Network 530a. In some aspects, creating a shortcut Connection 853 can be implemented by performing Similarity Comparisons 125 of a Knowledge Cell 800 from Knowledge Structuring Unit 520 with Knowledge Cells 800 in any Layer 854 when applying (i.e. storing, copying, etc.) the Knowledge Cell 800 from Knowledge Structuring Unit 520 onto Neural Network 530a. Once created, shortcut Connections 853 enable a wider variety of Knowledge Cells 800 to be considered when selecting a path through Neural Network 530a. In some embodiments, Knowledge Structuring Unit 520 structures or generates Knowledge Cells 800 and the system applies them onto Neural Network 530a, thereby implementing learning Device's 98 operation in various visual surroundings. The system can perform Similarity Comparisons 125 of a Knowledge Cell 800 from Knowledge Structuring Unit 520 with Knowledge Cells 800 in a corresponding and/or other Layers 854 of Neural Network 530a. If a substantially similar Knowledge Cell 800 is not found in the corresponding or other Layers 854 of Neural Network 530a, the system may insert (i.e. copy, store, etc.) the Knowledge Cell 800 from Knowledge Structuring Unit 520 into the corresponding (or another) Layer 854 of Neural Network 530a, and create a Connection 853 to the inserted Knowledge Cell 800 from a prior Knowledge Cell 800 including assigning an occurrence count to the new Connection 853, calculating a weight of the new Connection 853, and updating any other Connections 853 originating from the prior Knowledge Cell 800. On the other hand, if a substantially similar Knowledge Cell 800 is found in the corresponding or other Layers 854 of Neural Network 530a, the system may update occurrence count and weight of Connection 853 to that Knowledge Cell 800 from a prior Knowledge Cell 800, and update any other Connections 853 originating from the prior Knowledge Cell 800. Any of the previously described and/or other techniques for comparing, inserting, updating, and/or other operations on Knowledge Cells

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800, Connections 853, Layers 854, and/or other elements can similarly be utilized in Neural Network 530a that comprises shortcut Connections 853.

Referring to Fig. 21, an embodiment of learning Knowledge Cells 800 comprising one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 using Graph 530b is illustrated. In some aspects, any Knowledge Cell 800 can be connected with any other Knowledge Cell 800 in Graph 530b. In other aspects, any Knowledge Cell 800 can be connected with itself and/or any other Knowledge Cell 800 in Graph 530b. In some embodiments, Knowledge Structuring Unit 520 structures or generates Knowledge Cells 800 and the system applies (i.e. store, copy, etc.) them onto Graph 530b, thereby implementing learning Device's 98 operation in various visual surroundings. The system can perform Similarity Comparisons 125 of a Knowledge Cell 800 from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Graph 530b. If a substantially similar Knowledge Cell 800 is not found in Graph 530b, the system may insert (i.e. copy, store, etc.) the Knowledge Cell 800 from Knowledge Structuring Unit 520 into Graph 530b, and create a Connection 853 to the inserted Knowledge Cell 800 from a prior Knowledge Cell 800 including assigning an occurrence count to the new Connection 853, calculating a weight of the new Connection 853, and updating any other Connections 853 originating from the prior Knowledge Cell 800. On the other hand, if a substantially similar Knowledge Cell 800 is found in Graph 530b, the system may update occurrence count and weight of Connection 853 to that Knowledge Cell 800 from a prior Knowledge Cell 800, and update any other Connections 853 originating from the prior Knowledge Cell 800. Any of the previously described and/or other techniques for comparing, inserting, updating, and/or other operations on Knowledge Cells 800, Connections 853, and/or other elements can similarly be utilized in Graph 530b.

For example, the system can perform Similarity Comparisons 125 of Knowledge Cell 800ba from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Graph 530b. In the case that a substantially similar match is not found, the system may insert Knowledge Cell 800ha into Graph 530b and copy Knowledge Cell 800ba into the inserted Knowledge Cell 800ha. The system can then perform Similarity Comparisons 125 of Knowledge Cell 800bb from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Graph 530b. In the case that a substantially similar match is found between Knowledge Cell 800bb and Knowledge Cell 800hb, the system may create Connection 853h1 between Knowledge Cell 800ha and Knowledge Cell 800hb with occurrence count of 1 and weight of 1. The system can then perform Similarity Comparisons 125 of Knowledge Cell 800bc from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Graph 530b. In the case that a substantially similar match is found between Knowledge Cell 800bc and Knowledge Cell 800hc, the system may update occurrence count and weight of Connection 853h2 between Knowledge Cell 800hb and Knowledge Cell 800hc, and update weights of other outgoing Connections 853 (one in this example) originating from Knowledge Cell 800hb as previously described. The system can then perform Similarity Comparisons 125 of Knowledge Cell 800bd from Knowledge Structuring Unit 520 with Knowledge Cells 800 in Graph 530b. In the case that a substantially similar match is not found, the system may insert Knowledge Cell 800hd into Graph 530b and copy Knowledge Cell 800bd into the inserted Knowledge Cell 800hd. The system may also create Connection 853h3 between Knowledge Cell 800hc and Knowledge Cell 800hd with occurrence count of 1 and weight calculated based on the occurrence count as previously described. The system may also update weights of other outgoing Connections 853 (one in this example) originating from Knowledge Cell 800hc as previously described. The system can then perform Similarity Comparisons 125 of Knowledge Cell 800be from Knowledge Structuring Unit 520 with Knowledge Cells 800 in

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Graph 530b. In the case that a substantially similar match is not found, the system may insert Knowledge Cell 800he into Graph 530b and copy Knowledge Cell 800be into the inserted Knowledge Cell 800he. The system may also create Connection 853h4 between Knowledge Cell 800hd and Knowledge Cell 800he with occurrence count of 1 and weight of 1. Applying any additional Knowledge Cells 800 from Knowledge Structuring Unit 520 onto Graph 530b follows similar logic or process as the above-described.

Referring to Fig. 22, an embodiment of learning Knowledge Cells 800 comprising one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527 using Collection of Sequences 530c is illustrated. Collection of Sequences 530c comprises the functionality for storing one or more Sequences 533. Sequence 533 comprises the functionality for storing multiple Knowledge Cells 800. In some aspects, a Sequence 533 may include Knowledge Cells 800 relating to a single operation of Device 98. For example, Knowledge Structuring Unit 520 structures or generates Knowledge Cells 800 and the system applies them onto Collection of Sequences 530c, thereby implementing learning Device's 98 operation in various visual surroundings. The system can perform Similarity Comparisons 125 of Knowledge Cells 800 from Knowledge Structuring Unit 520 with corresponding Knowledge Cells 800 in Sequences 533 of Collection of Sequences 530c to find a Sequence 533 comprising Knowledge Cells 800 that are substantially similar to the Knowledge Cells 800 from Knowledge Structuring Unit 520. If Sequence 533 comprising such substantially similar Knowledge Cells 800 is not found in Collection of Sequences 530c, the system may create a new Sequence 533 comprising the Knowledge Cells 800 from Knowledge Structuring Unit 520 and insert (i.e. copy, store, etc.) the new Sequence 533 into Collection of Sequences 530c. On the other hand, if Sequence 533 comprising substantially similar Knowledge Cells 800 is found in Collection of Sequences 530c, the system may optionally omit inserting the Knowledge Cells 800 from Knowledge Structuring Unit 520 into Collection of Sequences 530c as inserting a similar Sequence 533 may not add much or any additional knowledge. This approach can save storage resources and limit the number of Knowledge Cells 800 that may later need to be processed or compared. In other aspects, a Sequence 533 may include Knowledge Cells 800 relating to a part of an operation of Device 98. Similar learning process as the above described can be utilized in such implementations. In further aspects, one or more long Sequences 533 each including Knowledge Cells 800 of multiple operations of Device 98 can be utilized. In one example, Knowledge Cells 800 of all operations can be stored in a single long Sequence 533 in which case Collection of Sequences 530c as a separate element can be omitted. In another example, Knowledge Cells 800 of multiple operations can be included in a plurality of long Sequences 533 such as hourly, daily, weekly, monthly, yearly, or other periodic or other Sequences 533. Similarity Comparisons 125 can be performed by traversing the one or more long Sequences 533 to find a match or substantially similar match. For instance, the system can perform Similarity Comparisons 125 of Knowledge Cells 800 from Knowledge Structuring Unit 520 with corresponding Knowledge Cells 800 in subsequences of a long Sequence 533 in incremental or other traversing pattern to find a subsequence comprising Knowledge Cells 800 that are substantially similar to the Knowledge Cells 800 from Knowledge Structuring Unit 520. The incremental traversing pattern may start from one end of a long Sequence 533 and move the comparison subsequence up or down one or any number of incremental Knowledge Cells 800 at a time. Other traversing patterns or methods can be employed such as starting from the middle of the Sequence 533 and subdividing the resulting sub-sequences in a recursive pattern, or any other traversing pattern or method. If a subsequence comprising substantially similar Knowledge Cells 800 is not found in the long Sequence 533, the system may concatenate or append the Knowledge

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Cells 800 from Knowledge Structuring Unit 520 to the long Sequence 533. In further aspects, Connections 853 can optionally be used in Sequence 533 to connect Knowledge Cells 800. For example, a Knowledge Cell 800 can be connected not only with a next Knowledge Cell 800 in the Sequence 533, but also with any other Knowledge Cell 800 in the Sequence 533, thereby creating alternate routes or shortcuts through the Sequence 533. Any number of Connections 853 connecting any Knowledge Cells 800 can be utilized. Any of the previously described and/or other techniques for comparing, inserting, updating, and/or other operations on Knowledge Cells 800, Connections 853, and/or other elements can similarly be utilized in Sequences 533 and/or Collection of Sequences 530c.

Any of the previously described data structures or arrangements of Knowledge Cells 800 such as Neural Network 530a, Graph 530b, Collection of Sequences 530c, Sequence 533, Collection of Knowledge Cells 530d, and/or others can be used alone, or in combination with each other or with other elements, in alternate embodiments. In one example, a path in Neural Network 530a or Graph 530b may include its own separate sequence of Knowledge Cells 800 that are not interconnected with Knowledge Cells 800 in other paths. In another example, a part of a path in Neural Network 530a or Graph 530b may include a sequence of Knowledge Cells 800 interconnected with Knowledge Cells 800 in other paths, whereas, another part of the path may include its own separate sequence of Knowledge Cells 800 that are not interconnected with Knowledge Cells 800 in other paths. Any other combinations or arrangements of Knowledge Cells 800 can be implemented.

Referring to Fig. 23, an embodiment of determining anticipatory Instruction Sets 526 from a single Knowledge Cell 800 is illustrated. Knowledge Cell 800 may be part of a Knowledgebase 530 (i.e. Neural Network 530a, Graph 530b, Collection of Sequences 530c, Sequence 533, Collection of Knowledge Cells 530d, etc.) such as Collection of Knowledge Cells 530d. Decision-making Unit 540 comprises the functionality for anticipating or determining a device's operation in various visual surroundings. Decision-making Unit 540 comprises the functionality for anticipating or determining Instruction Sets 526 (i.e. anticipatory Instruction Sets 526, etc.) to be used or executed in Device's 98 autonomous operation based on incoming Digital Pictures 525 of Device's 98 visual surrounding. Decision-making Unit 540 also comprises other disclosed functionalities.

In some aspects, Decision-making Unit 540 may anticipate or determine Instruction Sets 526 (i.e. anticipatory Instruction Sets 526, etc.) for autonomous Device 98 operation by performing Similarity Comparisons 125 of incoming Digital Pictures 525 or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from Knowledge Cells 800 in Knowledgebase 530 (i.e. Neural Network 530a, Graph 530b, Collection of Sequences 530c, Sequence 533, Collection of Knowledge Cells 530d, etc.). A Knowledge Cell 800 includes a unit of knowledge (i.e. one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527, etc.) of how Device 98 operated in a visual surrounding as previously described. When Digital Pictures 525 or portions thereof of a similar visual surrounding are detected in the future, Decision-making Unit 540 can anticipate the Instruction Sets 526 (i.e. anticipatory Instruction Sets 526, etc.) previously learned in a similar visual surrounding, thereby enabling autonomous Device 98 operation. In some aspects, Decision-making Unit 540 can perform Similarity Comparisons 125 of incoming Digital Pictures 525 from Picture Capturing Apparatus 90 with Digital Pictures 525 from Knowledge Cells 800 in Knowledgebase 530 (i.e. Neural Network 530a, Graph 530b, Collection of Sequences 530c, Sequence 533, Collection of Knowledge Cells 530d, etc.). If one or more substantially similar Digital Pictures 525 or portions thereof are found in a Knowledge Cell 800 from Knowledgebase 530, Instruction Sets 526 (i.e. anticipatory Instruction Sets 526, etc.) for autonomous Device 98 operation can be

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anticipated in Instruction Sets 526 correlated with the one or more Digital Pictures 525 from the Knowledge Cell 800. In some designs, subsequent one or more Instruction Sets 526 for autonomous Device 98 operation can be anticipated in Instruction Sets 526 correlated with subsequent Digital Pictures 525 from the Knowledge Cell 800 (or other Knowledge Cells 800), thereby anticipating not only current, but also additional future Instruction Sets 526. Although, Extra Info 527 is not shown in this and/or other figures for clarity of illustration, it should be noted that any Digital Picture 525, Instruction Set 526, and/or other element may include or be associated with Extra Info 527 and that Decision-making Unit 540 can utilize Extra Info 527 for enhanced decision making.

For example, Decision-making Unit 540 can perform Similarity Comparisons 125 of Digital Picture 525I1 or portion thereof from Picture Capturing Apparatus 90 with Digital Picture 525a1 or portion thereof from Knowledge Cell 800oa. Digital Picture 525a1 or portion thereof from Knowledge Cell 800oa may be found substantially similar. Decision-making Unit 540 can anticipate Instruction Sets 526a1-526a3 correlated with Digital Picture 525a1, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Picture 525I2 or portion thereof from Picture Capturing Apparatus 90 with Digital Picture 525a2 or portion thereof from Knowledge Cell 800oa. Digital Picture 525a2 or portion thereof from Knowledge Cell 800oa may be found substantially similar. Decision-making Unit 540 can anticipate Instruction Set 526a4 correlated with Digital Picture 525a2, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Picture 525I3 or portion thereof from Picture Capturing Apparatus 90 with Digital Picture 525a3 or portion thereof from Knowledge Cell 800oa. Digital Picture 525a3 or portion thereof from Knowledge Cell 800oa may be found substantially similar. Decision-making Unit 540 may not anticipate any Instruction Sets 526 since none are correlated with Digital Picture 525a3. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Picture 525I4 or portion thereof from Picture Capturing Apparatus 90 with Digital Picture 525a4 or portion thereof from Knowledge Cell 800oa. Digital Picture 525a4 or portion thereof from Knowledge Cell 800oa may not be found substantially similar. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Picture 525I5 or portion thereof from Picture Capturing Apparatus 90 with Digital Picture 525a5 or portion thereof from Knowledge Cell 800oa. Digital Picture 525a5 or portion thereof from Knowledge Cell 800oa may not be found substantially similar. Decision-making Unit 540 can implement similar logic or process for any additional Digital Picture 525 from Picture Capturing Apparatus 90, and so on.

It should be understood that any of the described elements and/or techniques in the foregoing example can be omitted, used in a different combination, or used in combination with other elements and/or techniques, in which case the selection of Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Instruction Sets 526, etc.) thereof would be affected accordingly. In one example, Extra Info 527 can be included in the Similarity Comparisons 125 as previously described. In another example, as history of incoming Digital Pictures 525 becomes available, Decision-making Unit 540 can perform collective Similarity Comparisons 125 of the history of Digital Pictures 525 or portions thereof from Picture Capturing Apparatus 90 with subsequences of Digital Pictures 525 or portions thereof from Knowledge Cell 800. In a further example, the described comparisons in a single Knowledge Cell 800 may be performed on any number of Knowledge Cells 800 sequentially or in parallel. Parallel processors such as a plurality of Processors 11 or cores thereof can be utilized for such parallel processing. In a further example, various arrangements of Digital Pictures 525 and/or other elements in a Knowledge Cell 800 can be utilized as previously described. One of ordinary skill in art will understand that the foregoing exemplary embodiment is described merely

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as an example of a variety of possible implementations, and that while all of its variations are too voluminous to describe, they are within the scope of this disclosure.

Referring to Fig. 24, an embodiment of determining anticipatory Instruction Sets 526 by traversing a single Knowledge Cell 800 is illustrated. Knowledge Cell 800 may be part of a Knowledgebase 530 (i.e. Neural Network 530a, Graph 530b, Collection of Sequences 530c, Sequence 533, Collection of Knowledge Cells 530d, etc.) such as Collection of Knowledge Cells 530d. For example, Decision-making Unit 540 can perform Similarity Comparisons 125 of Digital Picture 525I1 or portion thereof from Picture Capturing Apparatus 90 with Digital Picture 525a1 or portion thereof from Knowledge Cell 800oa. Digital Picture 525a1 or portion thereof from Knowledge Cell 800oa may not be found substantially similar. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Picture 525I1 or portion thereof from Picture Capturing Apparatus 90 with Digital Picture 525a2 or portion thereof from Knowledge Cell 800oa. Digital Picture 525a2 or portion thereof from Knowledge Cell 800oa may not be found substantially similar. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Picture 525I1 or portion thereof from Picture Capturing Apparatus 90 with Digital Picture 525a3 or portion thereof from Knowledge Cell 800oa. Digital Picture 525a3 or portion thereof from Knowledge Cell 800oa may be found substantially similar. Decision-making Unit 540 may not anticipate any Instruction Sets 526 since none are correlated with Digital Picture 525a3. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Picture 525I2 or portion thereof from Picture Capturing Apparatus 90 with Digital Picture 525a4 or portion thereof from Knowledge Cell 800oa. Digital Picture 525a4 or portion thereof from Knowledge Cell 800oa may be found substantially similar. Decision-making Unit 540 can anticipate Instruction Sets 526a5-526a6 correlated with Digital Picture 525a4, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Picture 525I3 or portion thereof from Picture Capturing Apparatus 90 with Digital Picture 525a5 or portion thereof from Knowledge Cell 800oa. Digital Picture 525a5 or portion thereof from Knowledge Cell 800oa may be found substantially similar. Decision-making Unit 540 may not anticipate any Instruction Sets 526 since none are correlated with Digital Picture 525a5. Decision-making Unit 540 can implement similar logic or process for any additional Digital Pictures 525 from Picture Capturing Apparatus 90, and so on.

It should be understood that any of the described elements and/or techniques in the foregoing example can be omitted, used in a different combination, or used in combination with other elements and/or techniques, in which case the selection of Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Instruction Sets 526, etc.) thereof would be affected accordingly. In one example, Extra Info 527 can be included in the Similarity Comparisons 125 as previously described. In another example, as history of incoming Digital Pictures 525 becomes available, Decision-making Unit 540 can perform collective Similarity Comparisons 125 of the history of Digital Pictures 525 or portions thereof from Picture Capturing Apparatus 90 with subsequences of Digital Pictures 525 or portions thereof from Knowledge Cell 800. In a further example, traversing may be performed in incremental traversing pattern such as starting from one end of Knowledge Cell 800 and moving the comparison subsequence up or down the list one or any number of incremental Digital Pictures 525 at a time. Other traversing patterns or methods can be employed such as starting from the middle of the Knowledge Cell 800 and subdividing the resulting subsequence in a recursive pattern, or any other traversing pattern or method. In a further example, the described traversing of a single Knowledge Cell 800 may be performed on any number of Knowledge Cells 800 sequentially or in parallel. Parallel processors such as a plurality of Processors 11 or cores thereof can be utilized for such parallel processing. In a

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further example, various arrangements of Digital Pictures 525 and/or other elements in a Knowledge Cell 800 can be utilized as previously described. One of ordinary skill in art will understand that the foregoing exemplary embodiment is described merely as an example of a variety of possible implementations, and that while all of its variations are too voluminous to describe, they are within the scope of this disclosure.

Referring to Fig. 25, an embodiment of determining anticipatory Instruction Sets 526 using collective similarity comparisons is illustrated. For example, Decision-making Unit 540 can perform Similarity Comparisons 125 of Digital Picture 525I1 or portion thereof from Picture Capturing Apparatus 90 with corresponding Digital Pictures 525 or portions thereof from Knowledge Cells 800 in Collection of Knowledge Cells 530d. Digital Picture 525c1 or portion thereof from Knowledge Cell 800rc may be found substantially similar with highest similarity. Decision-making Unit 540 can anticipate any Instruction Sets 526 (not shown) correlated with Digital Picture 525c1, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform collective Similarity Comparisons 125 of Digital Pictures 525I1-525I2 or portions thereof from Picture Capturing Apparatus 90 with corresponding Digital Pictures 525 or portions thereof from Knowledge Cells 800 in Collection of Knowledge Cells 530d. Digital Pictures 525c1-525c2 or portions thereof from Knowledge Cell 800rc may be found substantially similar with highest similarity. Decision-making Unit 540 can anticipate any Instruction Sets 526 (not shown) correlated with Digital Picture 525c2, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform collective Similarity Comparisons 125 of Digital Pictures 525I1-525I3 or portions thereof from Picture Capturing Apparatus 90 with corresponding Digital Pictures 525 or portions thereof from Knowledge Cells 800 in Collection of Knowledge Cells 530d. Digital Pictures 525d1-525d3 or portions thereof from Knowledge Cell 800rd may be found substantially similar with highest similarity. Decision-making Unit 540 can anticipate any Instruction Sets 526 (not shown) correlated with Digital Picture 525d3, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform collective Similarity Comparisons 125 of Digital Pictures 525I1-525I4 or portions thereof from Picture Capturing Apparatus 90 with corresponding Digital Pictures 525 or portions thereof from Knowledge Cells 800 in Collection of Knowledge Cells 530d. Digital Pictures 525d1-525d4 or portions thereof from Knowledge Cell 800rd may be found substantially similar with highest similarity. Decision-making Unit 540 can anticipate any Instruction Sets 526 (not shown) correlated with Digital Picture 525d4, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform collective Similarity Comparisons 125 of Digital Pictures 525I1-525I5 or portions thereof from Picture Capturing Apparatus 90 with corresponding Digital Pictures 525 or portions thereof from Knowledge Cells 800 in Collection of Knowledge Cells 530d. Digital Pictures 525d1-525d5 or portions thereof from Knowledge Cell 800rd may be found substantially similar with highest similarity. Decision-making Unit 540 can anticipate any Instruction Sets 526 (not shown) correlated with Digital Picture 525d5, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can implement similar logic or process for any additional Digital Picture 525 from Picture Capturing Apparatus 90, and so on.

In some embodiments, various elements and/or techniques can be utilized in the aforementioned similarity determinations with respect to collectively compared Digital Pictures 525 and/or other elements. In some aspects, similarity of collectively compared Digital Pictures 525 can be determined based on similarities or similarity indexes of the individually compared Digital Pictures 525. In one example, an average of similarities or similarity indexes of individually compared Digital Pictures 525 can be used to determine similarity of collectively compared Digital Pictures 525. In another example, a weighted average of similarities or similarity indexes of individually compared

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Digital Pictures 525 can be used to determine similarity of collectively compared Digital Pictures 525. For instance, to affect the weighting of collective similarity, a higher weight or importance (i.e. importance index, etc.) can be assigned to the similarities or similarity indexes of some (i.e. more substantive, etc.) Digital Pictures 525 and lower for other (i.e. less substantive, etc.) Digital Pictures 525. Any other higher or lower weight or importance assignment can be implemented. In other aspects, any of the previously described or other thresholds for substantial similarity of individually compared elements can be similarly utilized for collectively compared elements. In one example, substantial similarity of collectively compared Digital Pictures 525 can be achieved when their collective similarity or similarity index exceeds a similarity threshold. In another example, substantial similarity of collectively compared Digital Pictures 525 can be achieved when at least a threshold number or percentage of Digital Pictures 525 or portions thereof of the collectively compared Digital Pictures 525 match or substantially match. Similarly, substantial similarity of collectively compared Digital Pictures 525 can be achieved when a number or percentage of matching or substantially matching Digital Pictures 525 or portions thereof of the collectively compared Digital Pictures 525 exceeds a threshold. Such thresholds can be defined by a user, by VSADO system administrator, or automatically by the system based on experience, testing, inquiry, analysis, synthesis, or other techniques, knowledge, or input. Similar elements and/or techniques as the aforementioned can be used for similarity determinations of other collectively compared elements such as Instruction Sets 526, Extra Info 527, Knowledge Cells 800, and/or others. Similarity determinations of collectively compared elements may include any features, functionalities, and embodiments of Similarity Comparison 125, and vice versa.

It should be understood that any of the described elements and/or techniques in the foregoing example can be omitted, used in a different combination, or used in combination with other elements and/or techniques, in which case the selection of Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Instruction Sets 526, etc.) thereof would be affected accordingly. Any of the elements and/or techniques utilized in other examples or embodiments described herein such as using Extra Info 527 in Similarity Comparisons 125, traversing of Knowledge Cells 800 or other elements, using history of Digital Pictures 525 or Knowledge Cells 800 for collective Similarity Comparisons 125, using various arrangements of Digital Pictures 525 and/or other elements in a Knowledge Cell 800, and/or others can similarly be utilized in this example. One of ordinary skill in art will understand that the foregoing exemplary embodiment is described merely as an example of a variety of possible implementations, and that while all of its variations are too voluminous to describe, they are within the scope of this disclosure.

Referring to Fig. 26, an embodiment of determining anticipatory Instruction Sets 526 using Neural Network 530a is illustrated. In some aspects, determining anticipatory Instruction Sets 526 using Neural Network 530a may include selecting a path of Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Instruction Sets 526, etc.) thereof through Neural Network 530a. Decision-making Unit 540 can utilize various elements and/or techniques for selecting a path through Neural Network 530a. Although, these elements and/or techniques are described using Neural Network 530a below, they can similarly be used in any Knowledgebase 530 (i.e. Graph 530b, Collection of Sequences 530c, Sequence 533, Collection of Knowledge Cells 530d, etc.) where applicable.

In some embodiments, Decision-making Unit 540 can utilize similarity index in selecting Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Instruction Sets 526, etc.) thereof in a path through Neural Network 530a. For instance, similarity index may indicate how well one or more Digital Pictures 525 or portions thereof are matched with one or more other Digital Pictures 525 or portions thereof as previously described. In one example, Decision-

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making Unit 540 may select a Knowledge Cell 800 comprising one or more Digital Pictures 525 with highest similarity index even if Connection 853 pointing to that Knowledge Cell 800 has less than the highest weight. Therefore, similarity index or other such element or parameter can override or disregard the weight of a Connection 853 or other element. In another example, Decision-making Unit 540 may select a Knowledge Cell 800 comprising one or more Digital Pictures 525 whose similarity index is higher than or equal to a weight of Connection 853 pointing to that Knowledge Cell 800. In a further example, Decision-making Unit 540 may select a Knowledge Cell 800 comprising one or more Digital Pictures 525 whose similarity index is lower than or equal to a weight of Connection 853 pointing to that Knowledge Cell 800. Similarity index can be set to be more, less, or equally important than a weight of a Connection 853.

In other embodiments, Decision-making Unit 540 can utilize Connections 853 in selecting Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Instruction Sets 526, etc.) thereof in a path through Neural Network 530a. In some aspects, Decision-making Unit 540 can take into account weights of Connections 853 among the interconnected Knowledge Cells 800 in choosing from which Knowledge Cell 800 to compare one or more Digital Pictures 525 first, second, third, and so on. Specifically, for instance, Decision-making Unit 540 can perform Similarity Comparison 125 with one or more Digital Pictures 525 from Knowledge Cell 800 pointed to by the highest weight Connection 853 first, Digital Pictures 525 from Knowledge Cell 800 pointed to by the second highest weight Connection 853 second, and so on. In other aspects, Decision-making Unit 540 can stop performing Similarity Comparisons 125 as soon as it finds one or more substantially similar Digital Pictures 525 in an interconnected Knowledge Cell 800. In further aspects, Decision-making Unit 540 may only follow the highest weight Connection 853 to arrive at a Knowledge Cell 800 comprising one or more Digital Pictures 525 to be compared, thereby disregarding Connections 853 with less than the highest weight. In further aspects, Decision-making Unit 540 may ignore Connections 853 and/or their weights.

In further embodiments, Decision-making Unit 540 can utilize a bias to adjust similarity index, weight of a Connection 853, and/or other element or parameter used in selecting Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Instruction Sets 526, etc.) thereof in a path through Neural Network 530a. In one example, Decision-making Unit 540 may select a Knowledge Cell 800 comprising one or more Digital Pictures 525 whose similarity index multiplied by or adjusted for a bias is higher than or equal to a weight of Connection 853 pointing to that Knowledge Cell 800. In another example, Decision-making Unit 540 may select a Knowledge Cell 800 comprising one or more Digital Pictures 525 whose similarity index multiplied by or adjusted for a bias is lower than or equal to a weight of Connection 853 pointing to that Knowledge Cell 800. In a further example, bias can be used to resolve deadlock situations where similarity index is equal to a weight of a Connection 853. In some aspects, bias can be expressed in percentages such as 0.3 percent, 1.2 percent, 25.7 percent, 79.8 percent, 99.9 percent, 100.1 percent, 155.4 percent, 298.6 percent, 1105.5 percent, and so on. For example, a bias below 100 percent decreases an element or parameter to which it is applied, a bias equal to 100 percent does not change the element or parameter to which it is applied, and a bias higher than 100 percent increases the element or parameter to which it is applied. In general, any amount of bias can be utilized. Bias can be applied to one or more of a weight of a Connection 853, similarity index, any other element or parameter, and/or all or any combination of them. Also, different biases can be applied to each of a weight of a Connection 853, similarity index, or any other element or parameter. For example, 30 percent bias can be applied to similarity index and 15 percent bias can be applied to a weight of a Connection

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853. Also, different biases can be applied to various Layers 854 of Neural Network 530a, and/or other disclosed elements. Bias can be defined by a user, by VSADO system administrator, or automatically by the system based on experience, testing, inquiry, analysis, synthesis, or other techniques, knowledge, or input.

Any other element and/or technique can be utilized in selecting Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Instruction Sets 526, etc.) thereof in a path through Neural Network 530a.

In some embodiments, Neural Network 530a may include knowledge (i.e. interconnected Knowledge Cells 800 comprising one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527, etc.) of how Device 98 operated in various visual surroundings. In some aspects, determining anticipatory Instruction Sets 526 using Neural Network 530a may include selecting a path of Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Instruction Sets 526, etc.) thereof through Neural Network 530a. Individual and/or collective Similarity Comparisons 125 can be used to determine substantial similarity of the individually and/or collectively compared Digital Pictures 525 or portions thereof. Substantial similarity may be used primarily for selecting a path through Neural Network 530a, whereas, weight of any Connection 853 may be used secondarily or not at all.

For example, Decision-making Unit 540 can perform Similarity Comparisons 125 of Digital Pictures 525a1-525an or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from one or more Knowledge Cells 800 in Layer 854a (or any other one or more Layers 854, etc.). Digital Pictures 525 or portions thereof from Knowledge Cell 800ta may be found collectively substantially similar with highest similarity. As the comparisons of individual Digital Pictures 525 are performed to determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Pictures 525b1-525bn or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from one or more Knowledge Cells 800 in Layer 854b interconnected with Knowledge Cell 800ta. Digital Pictures 525 or portions thereof from Knowledge Cell 800tb may be found collectively substantially similar with highest similarity, thus, Decision-making Unit 540 may follow Connection 853t1 disregarding its less than highest weight. As the comparisons of individual Digital Pictures 525 are performed to determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Since Connection 853t2 is the only connection from Knowledge Cell 800tb, Decision-making Unit 540 may follow Connection 853t2 and perform Similarity Comparisons 125 of Digital Pictures 525c1-525cn or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from Knowledge Cell 800tc in Layer 854c. Digital Pictures 525 or portions thereof from Knowledge Cell 800tc may be found collectively substantially similar. As the comparisons of individual Digital Pictures 525 are performed to determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Pictures 525d1-525dn or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from one or more Knowledge Cells 800 in Layer 854d interconnected with Knowledge Cell 800tc. Digital Pictures 525 or portions thereof from Knowledge Cell 800td may be found collectively substantially similar with highest similarity, thus, Decision-making Unit 540 may follow Connection 853t3. As the comparisons of individual Digital Pictures 525 are performed to

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determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Pictures 525e1-525en or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from one or more Knowledge Cells 800 in Layer 854e interconnected with Knowledge Cell 800td. Digital Pictures 525 or portions thereof from Knowledge Cell 800te may be found collectively substantially similar with highest similarity, thus, Decision-making Unit 540 may follow Connection 853t4. As the comparisons of individual Digital Pictures 525 are performed to determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can implement similar logic or process for any additional Digital Pictures 525 from Picture Capturing Apparatus 90, and so on.

The foregoing exemplary embodiment provides an example of utilizing a combination of collective Similarity Comparisons 125, individual Similarity Comparisons 125, Connections 853, and/or other elements or techniques. It should be understood that any of these elements and/or techniques can be omitted, used in a different combination, or used in combination with other elements and/or techniques, in which case the selection of Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Instruction Sets 526, etc.) thereof in a path through Neural Network 530a would be affected accordingly. Any of the elements and/or techniques utilized in other examples or embodiments described herein such as using Extra Info 527 in Similarity Comparisons 125, traversing of Knowledge Cells 800 or other elements, using history of Digital Pictures 525 or Knowledge Cells 800 for collective Similarity Comparisons 125, using various arrangements of Digital Pictures 525 and/or other elements in a Knowledge Cell 800, and/or others can similarly be utilized in this example. These elements and/or techniques can similarly be utilized in Graph 530b, Collection of Sequences 530c, Sequence 533, Collection of Knowledge Cells 530d, and/or other data structures or arrangements. In some aspects, instead of anticipating Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525, Decision-making Unit 540 can anticipate instruction Sets 526 correlated with substantially similar streams of Digital Pictures 525. In other aspects, any time that substantial similarity or other similarity threshold is not achieved in compared Digital Pictures 525 or portions thereof of any of the Knowledge Cells 800, Decision-making Unit 540 can decide to look for a substantially or otherwise similar Digital Pictures 525 or portions thereof in Knowledge Cells 800 elsewhere in Neural Network 530a such as in any Layer 854 subsequent to a current Layer 854, in the first Layer 854, in the entire Neural Network 530a, and/or others, even if such Knowledge Cell 800 may be unconnected with a prior Knowledge Cell 800. It should be noted that any of Digital Pictures 525a1-525an, Digital Pictures 525b1-525bn, Digital Pictures 525c1-525cn, Digital Pictures 525d1-525dn, Digital Pictures 525e1-525en, etc. may include one Digital Picture 525 or a stream of Digital Pictures 525. It should also be noted that any Knowledge Cell 800 may include one Digital Picture 525 or a stream of Digital Pictures 525 as previously described. One of ordinary skill in art will understand that the foregoing exemplary embodiment is described merely as an example of a variety of possible implementations, and that while all of its variations are too voluminous to describe, they are within the scope of this disclosure.

Referring to Fig. 27, an embodiment of determining anticipatory Instruction Sets 526 using Graph 530b is illustrated. Graph 530b may include knowledge (i.e. interconnected Knowledge Cells 800 comprising one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527, etc.) of how Device 98 operated

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in various visual surroundings. In some aspects, determining anticipatory Instruction Sets 526 using Graph 530b may include selecting a path of Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Instruction Sets 526, etc.) thereof through Graph 530b. Individual and/or collective Similarity Comparisons 125 can be used to determine substantial similarity of the individually and/or collectively compared Digital Pictures 525 or portions thereof. Substantial similarity may be used primarily for selecting a path through Graph 530b, whereas, weight of any Connection 853 may be used secondarily or not at all.

For example, Decision-making Unit 540 can perform Similarity Comparisons 125 of Digital Pictures 525a1-525an or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from one or more Knowledge Cells 800 in Graph 530b. Digital Pictures 525 or portions thereof from Knowledge Cell 800ua may be found collectively substantially similar with highest similarity. As the comparisons of individual Digital Pictures 525 are performed to determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Pictures 525b1-525bn or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from one or more Knowledge Cells 800 in Graph 530b interconnected with Knowledge Cell 800ua by outgoing Connections 853. Digital Pictures 525 or portions thereof from Knowledge Cell 800ub may be found collectively substantially similar with highest similarity, thus, Decision-making Unit 540 may follow Connection 853u1 disregarding its less than highest weight. As the comparisons of individual Digital Pictures 525 are performed to determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Pictures 525c1-525cn or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from one or more Knowledge Cells 800 in Graph 530b interconnected with Knowledge Cell 800ub by outgoing Connections 853. Digital Pictures 525 or portions thereof from Knowledge Cell 800uc may be found collectively substantially similar with highest similarity, thus, Decision-making Unit 540 may follow Connection 853u2 disregarding its less than highest weight. As the comparisons of individual Digital Pictures 525 are performed to determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Since Connection 853u3 is the only connection from Knowledge Cell 800uc, Decision-making Unit 540 may follow Connection 853u3 and perform Similarity Comparisons 125 of Digital Pictures 525d1-525dn or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from Knowledge Cell 800ud in Graph 530b. Digital Pictures 525 or portions thereof from Knowledge Cell 800ud may be found collectively substantially similar. As the comparisons of individual Digital Pictures 525 are performed to determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Pictures 525e1-525en or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from one or more Knowledge Cells 800 in Graph 530b interconnected with Knowledge Cell 800ud by outgoing Connections 853. Digital Pictures 525 or portions thereof from Knowledge Cell 800ue may be found collectively substantially similar with highest similarity, thus, Decision-

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making Unit 540 may follow Connection 853u4. As the comparisons of individual Digital Pictures 525 are performed to determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can implement similar logic or process for any additional Digital Pictures 525 from Activity Detector 160, and so on.

The foregoing exemplary embodiment provides an example of utilizing a combination of collective Similarity Comparisons 125, individual Similarity Comparisons 125, Connections 853, and/or other elements or techniques. It should be understood that any of these elements and/or techniques can be omitted, used in a different combination, or used in combination with other elements and/or techniques, in which case the selection of Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Instruction Sets 526, etc.) thereof in a path through Graph 530b would be affected accordingly. Any of the elements and/or techniques utilized in other examples or embodiments described herein such as using Extra Info 527 in Similarity Comparisons 125, traversing of Knowledge Cells 800 or other elements, using history of Digital Pictures 525 or Knowledge Cells 800 in collective Similarity Comparisons 125, using various arrangements of Digital Pictures 525 and/or other elements in a Knowledge Cell 800, and/or others can similarly be utilized in this example. These elements and/or techniques can similarly be utilized in Neural Network 530a, Collection of Sequences 530c, Sequence 533, Collection of Knowledge Cells 530d, and/or other data structures or arrangements. In some aspects, instead of anticipating Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525, Decision-making Unit 540 can anticipate instruction Sets 526 correlated with substantially matching streams of Digital Pictures 525. In other aspects, any time that substantial similarity or other similarity threshold is not achieved in compared Digital Pictures 525 or portions thereof of any of the Knowledge Cells 800, Decision-making Unit 540 can decide to look for a substantially or otherwise similar Digital Pictures 525 or portions thereof in Knowledge Cells 800 elsewhere in Graph 530b even if such Knowledge Cell 800 may be unconnected with a prior Knowledge Cell 800. It should be noted that any of Digital Pictures 525a1-525an, Digital Pictures 525b1-525bn, Digital Pictures 525c1-525cn, Digital Pictures 525d1-525dn, Digital Pictures 525e1-525en, etc. may include one Digital Picture 525 or a stream of Digital Pictures 525. It should also be noted that any Knowledge Cell 800 may include one Digital Picture 525 or a stream of Digital Pictures 525 as previously described. One of ordinary skill in art will understand that the foregoing exemplary embodiment is described merely as an example of a variety of possible implementations, and that while all of its variations are too voluminous to describe, they are within the scope of this disclosure.

Referring to Fig. 28, an embodiment of determining anticipatory Instruction Sets 526 using Collection of Sequences 530c is illustrated. Collection of Sequences 530c may include knowledge (i.e. sequences of Knowledge Cells 800 comprising one or more Digital Pictures 525 correlated with any Instruction Sets 526 and/or Extra Info 527, etc.) of how Device 98 operated in various visual surroundings. In some aspects, determining anticipatory Instruction Sets 526 for autonomous Device 98 operation using Collection of Sequences 530c may include selecting a Sequence 533 of Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Instruction Sets 526, etc.) thereof from Collection of Sequences 530c. Individual and/or collective Similarity Comparisons 125 can be used to determine substantial similarity of the individually and/or collectively compared Digital Pictures 525 or portions thereof.

For example, Decision-making Unit 540 can perform Similarity Comparisons 125 of Digital Pictures 525a1-525an or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from

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corresponding Knowledge Cells 800 in one or more Sequences 533 of Collection of Sequences 530c. Digital Pictures 525 or portions thereof from Knowledge Cell 800ca in Sequence 533wc may be found collectively substantially similar with highest similarity. As the comparisons of individual Digital Pictures 525 are performed to determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Pictures 525a1-525an and 525b1-525bn or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from corresponding Knowledge Cells 800 in Sequences 533 of Collection of Sequences 530c. Digital Pictures 525 or portions thereof from Knowledge Cells 800ca-800cb in Sequence 533wc may be found collectively substantially similar with highest similarity. As the comparisons of individual Digital Pictures 525 are performed to determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Pictures 525a1-525an, 525b1-525bn, and 525c1-525cn or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from corresponding Knowledge Cells 800 in Sequences 533 of Collection of Sequences 530c. Digital Pictures 525 or portions thereof from Knowledge Cells 800da-800dc in Sequence 533wd may be found substantially similar with highest similarity. As the comparisons of individual Digital Pictures 525 are performed to determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Pictures 525a1-525an, 525b1-525bn, 525c1-525cn, and 525d1-525dn or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from corresponding Knowledge Cells 800 in Sequences 533 of Collection of Sequences 530c. Digital Pictures 525 or portions thereof from Knowledge Cells 800da-800dd in Sequence 533wd may be found substantially similar with highest similarity. As the comparisons of individual Digital Pictures 525 are performed to determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can then perform Similarity Comparisons 125 of Digital Pictures 525a1-525an, 525b1-525bn, 525c1-525cn, 525d1-525dn, and 525e1-525en or portions thereof from Picture Capturing Apparatus 90 with Digital Pictures 525 or portions thereof from corresponding Knowledge Cells 800 in Sequences 533 of Collection of Sequences 530c. Digital Pictures 525 or portions thereof from Knowledge Cells 800da-800de in Sequence 533wd may be found substantially similar with highest similarity. As the comparisons of individual Digital Pictures 525 are performed to determine collective similarity, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525 as previously described, thereby enabling autonomous Device 98 operation. Decision-making Unit 540 can implement similar logic or process for any additional Digital Pictures 525 from Picture Capturing Apparatus 90, and so on.

In some embodiments, various elements and/or techniques can be utilized in the aforementioned substantial similarity determinations with respect to collectively compared Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Extra Info 527, etc.) thereof. In some aspects, substantial similarity of collectively compared Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Extra Info 527, etc.) thereof can be determined based on

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similarities or similarity indexes of the individually compared Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Extra Info 527, etc.) thereof. In one example, an average of similarities or similarity indexes of individually compared Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Extra Info 527, etc.) thereof can be used to determine similarity of collectively compared Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Extra Info 527, etc.) thereof. In another example, a weighted average of similarities or similarity indexes of individually compared Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Extra Info 527, etc.) thereof can be used to determine similarity of collectively compared Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Extra Info 527, etc.) thereof. For instance, to affect the weighting of collective similarity, a higher weight or importance (i.e. importance index, etc.) can be assigned to the similarities or similarity indexes of some Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Extra Info 527, etc.) thereof and lower for other Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Extra Info 527, etc.) thereof. Any higher or lower weight or importance assignment can be implemented. In other aspects, any of the previously described or other thresholds for substantial similarity of individually compared elements can similarly be utilized for collectively compared elements. In one example, substantial similarity of collectively compared Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Extra Info 527, etc.) thereof can be achieved when their collective similarity or similarity index exceeds a similarity threshold. In another example, substantial similarity of collectively compared Knowledge Cells 800 can be achieved when at least a threshold number or percentage of Digital Pictures 525 or portions thereof of the collectively compared Knowledge Cells 800 match or substantially match. Similarly, substantial similarity of collectively compared Knowledge Cells 800 can be achieved when a number or percentage of matching or substantially matching Digital Pictures 525 or portions thereof of the collectively compared Knowledge Cells 800 exceeds a threshold. Such thresholds can be defined by a user, by VSADO system administrator, or automatically by the system based on experience, testing, inquiry, analysis, synthesis, or other techniques, knowledge, or input. Collective similarity determinations may include any features, functionalities, and embodiments of Similarity Comparison 125, and vice versa.

The foregoing exemplary embodiment provides an example of utilizing a combination of collective Similarity Comparisons 125, individual Similarity Comparisons 125, and/or other elements or techniques. It should be understood that any of these elements and/or techniques can be omitted, used in a different combination, or used in combination with other elements and/or techniques, in which case the selection of Sequence 533 of Knowledge Cells 800 or elements (i.e. Digital Pictures 525, Instruction Sets 526, etc.) thereof would be affected accordingly. Any of the elements and/or techniques utilized in other examples or embodiments described herein such as using Extra Info 527 in Similarity Comparisons 125, traversing of Knowledge Cells 800 or other elements, using history of Digital Pictures 525 or Knowledge Cells 800 in collective Similarity Comparisons 125, using various arrangements of Digital Pictures 525 and/or other elements in a Knowledge Cell 800, and/or others can similarly be utilized in this example. These elements and/or techniques can similarly be utilized in Neural Network 530a, Graph 530b, Collection of Knowledge Cells 530d, and/or other data structures or arrangements. In some aspects, instead of anticipating Instruction Sets 526 correlated with substantially similar individual Digital Pictures 525, Decision-making Unit 540 can anticipate Instruction Sets 526 correlated with substantially matching streams of Digital Pictures 525. In other aspects, any time that substantial similarity or other similarity threshold is not achieved in compared Digital Pictures 525 or portions thereof of any of the Knowledge Cells 800, Decision-making Unit 540 can decide to look for a substantially or otherwise similar Digital Pictures 525 or portions thereof in Knowledge Cells 800 elsewhere in

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Collection of Sequences 530c such as in different Sequences 533. It should be noted that any of Digital Pictures 525a1-525an, Digital Pictures 525b1-525bn, Digital Pictures 525c1-525cn, Digital Pictures 525d1-525dn, Digital Pictures 525e1-525en, etc. may include one Digital Picture 525 or a stream of Digital Pictures 525. It should also be noted that any Knowledge Cell 800 may include one Digital Picture 525 or a stream of Digital Pictures 525 as previously described. One of ordinary skill in art will understand that the foregoing exemplary embodiment is described merely as an example of a variety of possible implementations, and that while all of its variations are too voluminous to describe, they are within the scope of this disclosure.

Referring now to Modification Interface 130. Modification Interface 130 comprises the functionality for modifying execution and/or functionality of Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element. Modification Interface 130 comprises the functionality for modifying execution and/or functionality of Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element at runtime. Modification Interface 130 comprises the functionality for modifying execution and/or functionality of Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element based on anticipatory Instruction Sets 526. In one example, Modification Interface 130 comprises the functionality to access, modify, and/or perform other manipulations on runtime engine/environment, virtual machine, operating system, compiler, just-in-time (JIT) compiler, interpreter, translator, execution stack, file, object, data structure, and/or other computing system elements. In another example, Modification Interface 130 comprises the functionality to access, modify, and/or perform other manipulations on memory, storage, bus, interfaces, and/or other computing system elements. In a further example, Modification Interface 130 comprises the functionality to access, modify, and/or perform other manipulations on Processor 11 registers and/or other Processor 11 elements. In a further example, Modification Interface 130 comprises the functionality to access, modify, and/or perform other manipulations on inputs and/or outputs of Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element. In a further example, Modification Interface 130 comprises the functionality to access, create, delete, modify, and/or perform other manipulations on functions, methods, procedures, routines, subroutines, and/or other elements of Application Program 18. In a further example, Modification Interface 130 comprises the functionality to access, create, delete, modify, and/or perform other manipulations on source code, bytecode, compiled, interpreted, or otherwise translated code, machine code, and/or other code. In a further example, Modification Interface 130 comprises the functionality to access, create, delete, modify, and/or perform other manipulations on values, variables, parameters, and/or other data or information. Modification Interface 130 comprises any features, functionalities, and embodiments of Acquisition Interface 120, and vice versa. Modification Interface 130 also comprises other disclosed functionalities.

Modification Interface 130 can employ various techniques for modifying execution and/or functionality of Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element. In some aspects, some of the previously described techniques and/or tools can be utilized. Code instrumentation, for instance, may involve inserting additional code, overwriting or rewriting existing code, and/or branching to a separate segment of code in Application Program 18 as previously described. For example, instrumented code may include the following:

```
Object1.moveLeft(12);
modifyApplication();
```

In the above sample code, instrumented call to Modification Interface's 130 function (i.e. modifyApplication(), etc.) can be placed after a function (i.e. moveLeft(12), etc.) of Application Program 18. Similar call to an application

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modifying function can be placed after or before some or all functions/routines/subroutines, some or all lines of code, some or all statements, some or all instructions or instruction sets, some or all basic blocks, and/or some or all other code segments of Application Program 18. One or more application modifying function calls can be placed anywhere in Application Program's 18 code and can be executed at any points in Application Program's 18 execution. The application modifying function (i.e. modifyApplication(), etc.) may include Artificial Intelligence Unit 110-determined anticipatory Instruction Sets 526 that can modify execution and/or functionality of Application Program 18. In some embodiments, the previously described obtaining Application Program's 18 instruction sets, data, and/or other information as well as modifying execution and/or functionality of Application Program 18 can be implemented in a single function that performs both tasks (i.e. traceAndModifyApplication(), etc.).

In some embodiments, various computing systems and/or platforms may provide native tools for modifying execution and/or functionality of Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element. Independent vendors may provide tools with similar functionalities that can be utilized across different platforms. These tools enable a wide range of techniques or capabilities such as instrumentation, self-modifying code capabilities, dynamic code capabilities, branching, code rewriting, code overwriting, hot swapping, accessing and/or modifying objects or data structures, accessing and/or modifying functions/routines/subroutines, accessing and/or modifying variable or parameter values, accessing and/or modifying processor registers, accessing and/or modifying inputs and/or outputs, providing runtime memory access, and/or other capabilities. One of ordinary skill in art will understand that, while all possible variations of the techniques for modifying execution and/or functionality of Application Program 18, Processor 11, Logic Circuit 250, and/or other processing element are too voluminous to describe, these techniques are within the scope of this disclosure.

In one example, modifying execution and/or functionality of Application Program 18 can be implemented through utilizing metaprogramming techniques, which include applications that can self-modify or that can create, modify, and/or manipulate other applications. Self-modifying code, dynamic code, reflection, and/or other techniques can be used to facilitate metaprogramming. In some aspects, metaprogramming is facilitated through a programming language's ability to access and manipulate the internals of the runtime engine directly or via an API. In other aspects, metaprogramming is facilitated through dynamic execution of expressions (i.e. anticipatory Instruction Sets 526, etc.) that can be created and/or executed at runtime. In yet other aspects, metaprogramming is facilitated through application modification tools, which can perform modifications on an application regardless of whether the application's programming language enables metaprogramming capabilities. Some operating systems may protect an application loaded into memory by restricting access to the loaded application. This protection mechanism can be circumvented by utilizing operating system's, processor's, and/or other low level features or commands to unprotect the loaded application. For example, a self-modifying application may modify the in-memory image of itself. To do so, the application can obtain the in-memory address of its code. The application may then change the operating system's or platform's protection on this memory range allowing it to modify the code (i.e. insert anticipatory Instruction Sets 526, etc.). In addition to a self-modifying application, one application can utilize similar technique to modify another application. Linux mprotect command or similar commends of other operating systems can be used to change protection (i.e. unprotect, etc.) for a region of memory, for example. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, modifying execution and/or functionality of Application Program 18 can be

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implemented through native capabilities of dynamic, interpreted, and/or scripting programming languages and/or platforms. Most of these languages and/or platforms can perform functionalities at runtime that static programming languages may perform during compilation. Dynamic, interpreted, and/or scripting languages provide native functionalities such as self-modification of code, dynamic code, extending the application, adding new code, extending objects and definitions, and/or other functionalities that can modify an application's execution and/or functionality at runtime. Examples of dynamic, interpreted, and/or scripting languages include Lisp, Perl, PHP, JavaScript, Ruby, Python, Smalltalk, Tcl, VBScript, and/or others. Similar functionalities can also be provided in languages such as Java, C, and/or others using reflection. Reflection includes the ability of an application to examine and modify the structure and behavior of the application at runtime. For example, JavaScript can modify its own code as it runs by utilizing Function object constructor as follows:

```
myFunc=new Function(arg1, arg2, argN, functionBody);
```

The sample code above causes a new function object to be created with the specified arguments and body. The body and/or arguments of the new function object may include new instruction sets (i.e. anticipatory Instruction Sets 526, etc.). The new function can be invoked as any other function in the original code. In another example, JavaScript can utilize eval method that accepts a string of JavaScript statements (i.e. anticipatory Instruction Sets 526, etc.) and execute them as if they were within the original code. An example of how eval method can be used to modify an application includes the following JavaScript code:

```
anticipatoryInstr = 'Object1.moveForward(32);';
if (anticipatoryInstr != "" && anticipatoryInstr != null)
{
        eval(anticipatoryInstr);
}
```

In the sample code above, Artificial Intelligence Unit 110 may generate anticipatory Instruction Set 526 (i.e. 'Object1.moveForward(32)' for moving an object forward 32 units, etc.) and save it in anticipatoryInstr variable, which eval method can then execute. Lisp is another example of dynamic, interpreted, and/or scripting language that includes similar capabilities as previously described JavaScript. For example, Lisp's compile command can create a function at runtime, eval command may parse and evaluate an expression at runtime, and exec command may execute a given instruction set (i.e. string, etc.) at runtime. In another example, dynamic as well as some non-dynamic languages may provide macros, which combine code introspection and/or eval capabilities. In some aspects, macros can access inner workings of the compiler, interpreter, virtual machine, runtime environment/engine, and/or other components of the computing platform enabling the definition of language-like constructs and/or generation of a complete program or sections thereof. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, modifying execution and/or functionality of Application Program 18 can be implemented through dynamic code, dynamic class loading, reflection, and/or other native functionalities of a programming language or platform. In static applications or static programming, a class can be defined and/or loaded at compile time. Conversely, in dynamic applications or dynamic programming, a class can be loaded into a running environment at runtime. For example, Java Runtime Environment (JRE) may not require that all classes be loaded at compile time and class loading can occur when a class is first referenced at runtime. Dynamic class

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loading enables inclusion or injection of on-demand code and/or functionalities at runtime. System provided or custom class loaders may enable loading of classes into the running environment. Custom class loaders can be created to enable custom functionalities such as, for example, specifying a remote location from which a class can be loaded. In addition to dynamic loading of a pre-defined class, a class can also be created at runtime. In some aspects, a class source code can be created at runtime. A compiler such as javac, com.sun.tools.javac.Main, javax.tools, javax.tools.JavaCompiler, and/or other packages can then be utilized to compile the source code. Javac, com.sun.tools.javac.Main, javax.tools, javax.tools.JavaCompiler, and/or other packages may include an interface to invoke Java compiler from within a running application. A Java compiler may accept source code in a file, string, object (i.e. Java String, StringBuffer, CharSequence, etc.) and/or other source, and may generate Java bytecode (i.e. class file, etc.). Once compiled, a class loader can then load the compiled class into the running environment. In other aspects, a tool such as Javaassist (i.e. Java programming assistant) can be utilized to enable an application to create or modify a class at runtime. Javassist may include a Java library that provides functionalities to create and/or manipulate Java bytecode of an application as well as reflection capabilities. Javassist may provide source-level and bytecode-level APIs. Using the source-level API, a class can be created and/or modified using only source code, which Javassist may compile seamlessly on the fly. Javassist source-level API can therefore be used without knowledge of Java bytecode specification. Bytecode-level API enables creating and/or editing a class bytecode directly. In yet other aspects, similar functionalities to the aforementioned ones may be provided in tools such as Apache Commons BCEL (Byte Code Engineering Library), ObjectWeb ASM, CGLIB (Byte Code Generation Library), and/or others. Once a dynamic code or class is created and loaded, reflection in high-level programming languages such as Java and/or others can be used to manipulate or change the runtime behavior of an application. Examples of reflective programming languages and/or platforms include Java, JavaScript, Smalltalk, Lisp, Python, .NET Common Language Runtime (CLR), Tcl, Ruby, Perl, PHP, Scheme, PL/SQL, and/or others. Reflection can be used in an application to access, examine, modify, and/or manipulate a loaded class and/or its elements. Reflection in Java can be implemented by utilizing a reflection API such as java.lang.Reflect package. The reflection API provides functionalities such as, for example, loading or reloading a class, instantiating a new instance of a class, determining class and instance methods, invoking class and instance methods, accessing and manipulating a class, fields, methods and constructors, determining the modifiers for fields, methods, classes, and interfaces, and/or other functionalities. The above described dynamic code, dynamic class loading, reflection, and/or other functionalities are similarly provided in the .NET platform through its tools such as, for example, System.CodeDom.Compiler namespace, System.Reflection.Emit namespace, and/or other native or other .NET tools. Other platforms in addition to Java and .NET may provide similar tools and/or functionalities. In some designs, dynamic code, dynamic class loading, reflection, and/or other functionalities can be used to facilitate modification of an application by inserting or injecting instruction sets (i.e. anticipatory Instruction Sets 526, etc.) into a running application. For example, an existing or dynamically created class comprising VSADO Unit 100 functionalities can be loaded into a running application through manual, automatic, or dynamic instrumentation. Once the class is created and loaded, an instance of VSADO Unit 100 class may be constructed. The instance of VSADO Unit 100 can then take or exert control of the application and/or implement alternate instruction sets (i.e. anticipatory Instruction Sets 526, etc.) at any point in the application's execution. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

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In a further example, modifying execution and/or functionality of Application Program 18 can be implemented through independent tools that can be utilized across different platforms. Such tools provide instrumentation and/or other capabilities on more than one platform or computing system and may facilitate application modification or insertion of instruction sets (i.e. anticipatory Instruction Sets 526, etc.). Examples of these tools include Pin, DynamoRIO, DynInst, Kprobes, KernInst, OpenPAT, DTrace, SystemTap, and/or others. In some aspects, Pin and/or any of its elements, methods, and/or techniques can be utilized for dynamic instrumentation. Pin can perform instrumentation by taking control of an application after it loads into memory. Pin may insert itself into the address space of an executing application enabling it to take control. Pin JIT compiler can then compile and implement alternate code (i.e. anticipatory Instruction Sets 526, etc.). Pin provides an extensive API for instrumentation at several abstraction levels. Pin supports two modes of instrumentation, JIT mode and probe mode. JIT mode uses a just-in-time compiler to insert instrumentation and recompile program code while probe mode uses code trampolines for instrumentation. Pin was designed for architecture and operating system independence. In other aspects, KernInst and/or any of its elements, methods, and/or techniques can be utilized for dynamic instrumentation. KernInst includes an instrumentation framework designed for dynamically inserting code into a running kernel of an operating system. KernInst implements probe-based dynamic instrumentation where code can be inserted, changed, and/or removed at will. Kerninst API enables client tools to construct their own tools for dynamic kernel instrumentation to suit variety of purposes such as insertion of alternate instruction sets (i.e. anticipatory Instruction Sets 526, etc.). Client tools can communicate with KernInst over a network (i.e. internet, wireless network, LAW, WAN, etc). Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, modifying execution and/or functionality of Application Program 18 can be implemented through utilizing operating system's native tools or capabilities such as Unix ptrace command. Ptrace includes a system call that may enable one process to control another allowing the controller to inspect and manipulate the internal state of its target. Ptrace can be used to modify a running application such as modifying an application with alternate instruction sets (i.e. anticipatory Instruction Sets 526, etc.). By attaching to an application using the ptrace call, the controlling application can gain extensive control over the operation of its target. This may include manipulation of its instruction sets, execution path, file descriptors, memory, registers, and/or other components. Ptrace can single-step through the target's code, observe and intercept system calls and their results, manipulate the target's signal handlers, receive and send signals on the target's behalf, and/or perform other operations within the target application. Ptrace's ability to write into the target application's memory space enables the controller to modify the running code of the target application. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, modifying execution and/or functionality of Application Program 18 can be implemented through utilizing just-in-time (JIT) compiling. JIT compilation (also known as dynamic translation, dynamic compilation, etc.) includes compilation performed during an application's execution (i.e. runtime, etc.). A code can be compiled when it is about to be executed, and it may be cached and reused later without the need for additional compilation. In some aspects, a JIT compiler can convert source code or byte code into machine code. In other aspects, a JIT compiler can convert source code into byte code. JIT compiling may be performed directly in memory. For example, JIT compiler can output machine code directly into memory and immediately execute it.

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Platforms such as Java, .NET, and/or others may implement JIT compilation as their native functionality. Platform independent tools for custom system design may include JIT compilation functionalities as well. In some aspects, JIT compilation includes redirecting application's execution to a JIT compiler from a specific entry point. For example, Pin can insert its JIT compiler into the address space of an application. Once execution is redirected to it, JIT compiler may receive alternate instruction sets (i.e. anticipatory Instruction Sets 526, etc.) immediately before their compilation. The JIT compiled instruction sets can be stored in memory or another repository from where they may be retrieved and executed. Alternatively, for example, JIT compiler can create a copy of the original application code or a segment thereof, and insert alternate code (i.e. anticipatory Instruction Sets 526, etc.) before compiling the modified code copy. In some aspects, JIT compiler may include a specialized memory such as fast cache memory dedicated to JIT compiler functionalities from which the modified code can be fetched rapidly. JIT compilation and/or any compilation in general may include compilation, interpretation, or other translation into machine code, bytecode, and/or other formats or types of code. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, modifying execution and/or functionality of Application Program 18 can be implemented through dynamic recompilation. Dynamic recompilation includes recompiling an application or part thereof during execution. An application can be modified with alternate features or instruction sets that may take effect after recompilation. Dynamic recompilation may be practical in various types of applications including object oriented, event driven, forms based, and/or other applications. In a typical windows-based application, most of the action after initial startup occurs in response to user or system events such as moving the mouse, selecting a menu option, typing text, running a scheduled task, making a network connection, and/or other events when an event handler is called to perform an operation appropriate for the event. Generally, when no events are being generated, the application is idle. For example, when an event occurs and an appropriate event handler is called, instrumentation can be implemented in the application's source code to insert alternate instruction sets (i.e. anticipatory Instruction Sets 526, etc.) at which point the modified source code can be recompiled and/or executed. In some aspects, the state of the application can be saved before recompiling its modified source code so that the application may continue from its prior state. Saving the application's state can be achieved by saving its variables, data structures, objects, location of its current instruction, and/or other necessary information in environmental variables, memory, or other repositories where they can be accessed once the application is recompiled. In other aspects, application's variables, data structures, objects, address of its current instruction, and/or other necessary information can be saved in a repository such as file, database, or other repository accessible to the application after recompilation of its source code. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, modifying execution and/or functionality of Application Program 18 can be implemented through modifying or redirecting Application Program's 18 execution path. Generally, an application can be loaded into memory and the flow of execution proceeds from one instruction set to the next until the end of the application. An application may include a branching mechanism that can be driven by keyboard or other input devices, system events, and/or other computing system components or events that may impact the execution path. The execution path can also be altered by an external application through acquiring control of execution and/or redirecting execution to a function, routine/subroutine, or an alternate code segment at any point in the application's

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execution. A branch, jump, or other mechanism can be utilized to implement the redirected execution. For example, a jump instruction can be inserted at a specific point in an application's execution to redirect execution to an alternate code segment. A jump instruction set may include, for example, an unconditional branch, which always results in branching, or a conditional branch, which may or may not result in branching depending on a condition. When executing an application, a computer may fetch and execute instruction sets in sequence until it encounters a branch instruction set. If the instruction set is an unconditional branch, or it is conditional and the condition is satisfied, the computer may fetch its next instruction set from a different instruction set sequence or code segment as specified by the branch instruction set. After the execution of the alternate code segment, control may be redirected back to the original jump point or to another point in the application. For example, modifying an application can be implemented by redirecting execution of an application to alternate instruction sets (i.e. anticipatory Instruction Sets 526, etc.). Alternate instruction sets can be pre-compiled, pre-interpreted, or otherwise pre-translated and ready for execution. Alternate instruction sets can also be JIT compiled, JIT interpreted, or otherwise JIT translated before execution. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

In a further example, modifying execution and/or functionality of Application Program 18 can be implemented through assembly language. Assembly language instructions may be directly related with the architecture's machine instructions as previously described. Assembly language can, therefore, be a powerful tool for implementing direct hardware (i.e. processor registers, memory, etc.) access and manipulations as well as access and manipulations of specialized processor features or instructions. Assembly language can also be a powerful tool for implementing low-level embedded systems, real-time systems, interrupt handlers, self or dynamically modifying code, and/or other applications. Specifically, for instance, self or dynamically modifying code that can be used to facilitate modifying of an application can be seamlessly implemented using assembly language. For example, using assembly language, instruction sets can be dynamically created and loaded into memory similar to the ones that a compiler may generate. Furthermore, using assembly language, memory space of a loaded application can be accessed to modify (including rewrite, overwrite, etc.) original instruction sets or to insert jumps or branches to alternate code elsewhere in memory. Some operating systems may implement protection from changes to applications loaded into memory. Operating system's, processor's, or other low level features or commands can be used to unprotect the protected locations in memory before the change as previously described. Alternatively, a pointer that may reside in a memory location where it could be readily altered can be utilized where the pointer may reference alternate code. In one example, assembly language can be utilized to write alternate code (i.e. anticipatory Instruction Sets 526, etc.) into a location in memory outside a running application's memory space. Assembly language can then be utilized to redirect the application's execution to the alternate code by inserting a jump or branch into the application's in-memory code, by redirecting program counter, or by other technique. In another example, assembly language can be utilized to overwrite or rewrite the entire or part of an application's in-memory code with alternate code. In some aspects, high-level programming languages can call an external assembly language program to facilitate application modification as previously described. In yet other aspects, relatively low-level programming languages such as C may allow embedding assembly language directly in their source code such as, for example, using asm keyword of C. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

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In a further example, modifying execution and/or functionality of Application Program 18 can be implemented through binary rewriting. Binary rewriting tools and/or techniques may modify an application's executable. In some aspects, modification can be minor such as in the case of optimization where the original executable's functionality is kept. In other aspects, modification may change the application's functionality such as by inserting alternate code (i.e. anticipatory Instruction Sets 526, etc.). Examples of binary rewriting tools include SecondWrite, ATOM, DynamoRIO, Purify, Pin, EEL, DynInst, PLTO, and/or others. Binary rewriting may include disassembly, analysis, and/or modification of target application. Since binary rewriting works directly on machine code executable, it is independent of source language, compiler, virtual machine (if one is utilized), and/or other higher level abstraction layers. Also, binary rewriting tools can perform application modifications without access to original source code. Binary rewriting tools include static rewriters, dynamic rewriters, minimally-invasive rewriters, and/or others. Static binary rewriters can modify an executable when the executable is not in use (i.e. not running). The rewritten executable may then be executed including any new or modified functionality. Dynamic binary rewriters can modify an executable during its execution, thereby enabling modification of an application's functionality at runtime. In some aspects, dynamic rewriters can be used for instrumentation or selective modifications such as insertion of alternate code (i.e. anticipatory Instruction Sets 526, etc.), and/or for other runtime transformations or modifications. For example, some dynamic rewriters can be configured to intercept an application's execution at indirect control transfers and insert instrumentation or other application modifying code. Minimally-invasive rewriters may keep the original machine code to the greatest extent possible. They support limited modifications such as insertion of jumps into and out of instrumented code. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

Referring to Fig. 29, in a further example, modifying execution and/or functionality of Processor 11 can be implemented through modification of processor registers, memory, or other computing system components. In some aspects, modifying execution and/or functionality of Processor 11 can be implemented by redirecting Processor's 11 execution to alternate instruction sets (i.e. anticipatory Instruction Sets 526, etc.). In one example, Program Counter 211 may hold or point to a memory address of the next instruction set that will be executed by Processor 11. Artificial Intelligence Unit 110 may generate anticipatory Instruction Sets 526 and store them in Memory 12 as previously described. Modification Interface 130 may then change Program Counter 211 to point to the location in Memory 12 where anticipatory Instruction Sets 526 are stored. The anticipatory Instruction Sets 526 can then be fetched from the location in Memory 12 pointed to by the modified Program Counter 211 and loaded into Instruction Register 212 for decoding and execution. Once anticipatory Instruction Sets 526 are executed, Modification Interface 130 may change Program Counter 211 to point to the last instruction set before the redirection or to any other instruction set. In other aspects, anticipatory Instruction Sets 526 can be loaded directly into Instruction Register 212. As previously described, examples of other processor or computing system components that can be used during an instruction cycle include memory address register (MAR), memory data register (MDR), data registers, address registers, general purpose registers (GPRs), conditional registers, floating point registers (FPRs), constant registers, special purpose registers, machine-specific registers, Register Array 214, Arithmetic Logic Unit 215, control unit, and/or other circuits or components. Any of the aforementioned processor registers, memory, or other computing system components can be accessed and/or modified to facilitate the disclosed functionalities. In some embodiments, processor interrupt may be issued to facilitate such access and/or modification. In some

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designs, modifying execution and/or functionality of Processor 11 can be implemented in a program, combination of programs and hardware, or purely hardware system. Dedicated hardware may be built to perform modifying execution and/or functionality of Processor 11 with marginal or no impact to computing overhead. Other platforms, tools, and/or techniques may provide equivalent or similar functionalities as the above described ones.

Referring to Figs. 30A-30B, in a further example, modifying execution and/or functionality of Logic Circuit 250 can be implemented through modification of inputs and/or outputs of Logic Circuit 250. While Processor 11 includes any type of logic circuit, Logic Circuit 250 is described separately herein to offer additional detail on its functioning. Logic Circuit 250 comprises the functionality for performing logic operations using the circuit's inputs and producing outputs based on the logic operations performed as previously described. In one example, Logic Circuit 250 may perform some logic operations using four input values and produce two output values. Modifying execution and/or functionality of Logic Circuit 250 can be implemented by replacing its input values with anticipatory input values (i.e. anticipatory Instruction Sets 526, etc.). Artificial Intelligence Unit 110 may generate anticipatory input values as previously described. Modification Interface 130 can then transmit the anticipatory input values to Logic Circuit 250 through the four hardwired connections as shown in Fig. 30A. Modification Interface 130 may use Switches 251 to prevent delivery of any input values that may be sent to Logic Circuit 250 from its usual input source. As such, VSADO Unit 100 may cause Logic Circuit 250 to perform its logic operations using the four anticipatory input values, thereby implementing autonomous Device 98 operation. In another example, Logic Circuit 250 may perform some logic operations using four input values and produce two output values. Modifying execution and/or functionality of Logic Circuit 250 can be implemented by replacing its output values with anticipatory output values (i.e. anticipatory Instruction Sets 526, etc.). Artificial Intelligence Unit 110 may generate anticipatory output values (i.e. anticipatory Instruction Sets 526, etc.) as previously described. Modification Interface 130 can then transmit the anticipatory output values through the two hardwired connections as shown in Fig. 30B. Modification Interface 130 may use Switches 251 to prevent delivery of any output values that may be sent by Logic Circuit 250. As such, VSADO Unit 100 may bypass Logic Circuit 250 and transmit the two anticipatory output values to downstream elements, thereby implementing autonomous Device 98 operation. In a further example, instead of or in addition to modifying input and/or output values of Logic Circuit 250, the execution and/or functionality of Logic Circuit 250 may be modified by modifying values or signals in one or more Logic Circuit's 250 internal components such as registers, memories, buses, and/or others (i.e. similar to the previously described modifying of Processor 11 components, etc.). In some designs, modifying execution and/or functionality of Logic Circuit 250 can be implemented in a program, combination of programs and hardware, or purely hardware system. Dedicated hardware may be built to perform modifying execution and/or functionality of Logic Circuit 250 with marginal or no impact to computing overhead. Any of the elements and/or techniques for modifying execution and/or functionality of Logic Circuit 250 can similarly be implemented with Processor 11 and/or other processing elements.

In some embodiments, VSADO Unit 100 may directly modify the functionality of an actuator (previously described, not shown). For example, Logic Circuit 250 or other processing element may control an actuator that enables Device 98 to perform mechanical, physical, and/or other operations. An actuator may receive one or more input values or control signals from Logic Circuit 250 or other processing element directing the actuator to perform specific operations. Modifying functionality of an actuator can be implemented by replacing its input values with anticipatory input values (i.e. anticipatory Instruction Sets 526, etc.) as previously described with respect to replacing

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input values of Logic Circuit 250. Specifically, for instance, Artificial Intelligence Unit 110 may generate anticipatory input values as previously described. Modification Interface 130 can then transmit the anticipatory input values to the actuator. Modification Interface 130 may use Switches 251 to prevent delivery of any input values that may be sent to the actuator from its usual input source. As such, VSADO Unit 100 may cause the actuator to perform its operations using the anticipatory input values, thereby implementing autonomous Device 98 operation.

One of ordinary skill in art will recognize that Figs. 30A-30B depict one of many implementations of Logic Circuit 250 and that any number of input and/or output values can be utilized in alternate implementations. One of ordinary skill in art will also recognize that Logic Circuit 250 may include any number and/or combination of logic components to implement any logic operations.

Other additional techniques or elements can be utilized as needed for modifying execution and/or functionality of Application Program 18, Processor 11, Logic Circuit 250, and/or other processing elements, or some of the disclosed techniques or elements can be excluded, or a combination thereof can be utilized in alternate embodiments.

Referring to Fig. 31, the illustration shows an embodiment of a method 6100 for learning and/or using visual surrounding for autonomous device operation. The method can be used on a computing device or system to enable learning of a device's operation in various visual surroundings and enable autonomous device operation in similar visual surroundings. Method 6100 may include any action or operation of any of the disclosed methods such as method 6200, 6300, 6400, 6500, 6600, and/or others. Additional steps, actions, or operations can be included as needed, or some of the disclosed ones can be optionally omitted, or a different combination or order thereof can be implemented in alternate embodiments of method 6100.

At step 6105, a first digital picture is received. A digital picture (i.e. Digital Picture 525, etc.) may include a depiction of a device's (i.e. Device's 98, etc.) visual surrounding. A digital picture may include a depiction of a remote device's (i.e. Remote Device's 97, etc.) visual surrounding. In some embodiments, a digital picture may include a collection of color encoded pixels or dots. A digital picture comprises any type or form of digital picture such as JPEG, GIF, TIFF, PNG, PDF, and/or other digitally encoded picture. In other embodiments, a stream of digital pictures (i.e. motion picture, video, etc.) may include one or more digital pictures. A stream of digital pictures comprises any type or form of digital motion picture such as MPEG, AVI, FLV, MOV, RM, SWF, WMV, DivX, and/or other digitally encoded motion picture. In some aspects, a digital picture may include or be substituted with a stream of digital pictures, and vice versa. Therefore, the terms digital picture and stream of digital pictures may be used interchangeably herein depending on context. One or more digital pictures can be captured by a picture capturing apparatus (i.e. Picture Capturing Apparatus 90, etc.) such as a still or motion picture camera, or other picture capturing apparatus. In some aspects, a picture capturing apparatus may be part of a device whose visual surrounding is being used for VSADO functionalities. In other aspects, a picture capturing apparatus may be part of a remote device, accessible via a network, whose visual surrounding is being used for VSADO functionalities. Picture capturing apparatus may be provided in any other device, system, process, or configuration. In some embodiments, capturing and/or receiving may be responsive to a triggering object, action, event, time, and/or other stimulus. Receiving comprises any action or operation by or for a Picture Capturing Apparatus 90, Digital Picture 525, and/or other disclosed elements.

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