

AO 120 (Rev. 08/10)

TO: Mail Stop 8 Director of the U.S. Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450	REPORT ON THE FILING OR DETERMINATION OF AN ACTION REGARDING A PATENT OR TRADEMARK
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In Compliance with 35 U.S.C. § 290 and/or 15 U.S.C. § 1116 you are hereby advised that a court action has been
filed in the U.S. District Court _____ for the District of Delaware _____ on the following

☐ Trademarks or ☒ Patents. (☐ the patent action involves 35 U.S.C. § 292.);

DOCKET NO.	DATE FILED	U.S. DISTRICT COURT for the District of Delaware
PLAINTIFF Tigo Energy Inc.		DEFENDANT SMA Solar Technology America LLC
PATENT OR TRADEMARK NO.	DATE OF PATENT OR TRADEMARK	HOLDER OF PATENT OR TRADEMARK
1 8,823,218	Sept. 2, 2014	Tigo Energy Inc.
2 8,933,321	Jan. 13, 2015	Tigo Energy Inc.
3 9,584,021	Feb. 28, 2017	Tigo Energy Inc.
4 9,966,848	May 8, 2018	Tigo Energy Inc.
5 10,256,770	Apr. 9, 2019	Tigo Energy Inc.
6 10,333,405	Jun. 25, 2019	Tigo Energy Inc.

In the above—entitled case, the following patent(s)/ trademark(s) have been included:

DATE INCLUDED	INCLUDED BY ☐ Amendment ☐ Answer ☐ Cross Bill ☐ Other Pleading	
PATENT OR TRADEMARK NO.	DATE OF PATENT OR TRADEMARK	HOLDER OF PATENT OR TRADEMARK
1		
2		
3		
4		
5		

In the above—entitled case, the following decision has been rendered or judgement issued:

DECISION/JUDGEMENT

CLERK	(BY) DEPUTY CLERK	DATE
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Copy 1—Upon initiation of action, mail this copy to Director Copy 3—Upon termination of action, mail this copy to Director
Copy 2—Upon filing document adding patent(s), mail this copy to Director Copy 4—Case file copy



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	ISSUE DATE	PATENT NO.	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/392,960	05/08/2018	9966848	108144-201503/US (TGY015)	3397

64494 7590 04/18/2018
GREENBERG TRAURIG, LLP (SV)
c/o: Greenberg Traurig LLP - Chicago Office
77 West Wacker Drive, Suite 3100
Intellectual Property Department
Chicago, IL 60601

ISSUE NOTIFICATION

The projected patent number and issue date are specified above.

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b) (application filed on or after May 29, 2000)

The Patent Term Adjustment is 0 day(s). Any patent to issue from the above-identified application will include an indication of the adjustment on the front page.

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (<http://pair.uspto.gov>).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Application Assistance Unit (AAU) of the Office of Data Management (ODM) at (571)-272-4200.

APPLICANT(s) (Please see PAIR WEB site <http://pair.uspto.gov> for additional applicants):

Mordechay AVRUTSKY, Alfei Menashe, ISRAEL;
Tigo Energy, Inc., Los Gatos, CA;

The United States represents the largest, most dynamic marketplace in the world and is an unparalleled location for business investment, innovation, and commercialization of new technologies. The USA offers tremendous resources and advantages for those who invest and manufacture goods here. Through SelectUSA, our nation works to encourage and facilitate business investment. To learn more about why the USA is the best country in the world to develop technology, manufacture products, and grow your business, visit SelectUSA.gov.



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/392,960	12/28/2016	Mordechay AVRUTSKY	108144-201503/US (TGY015)	3397
64494	7590	04/06/2018	EXAMINER	
GREENBERG TRAURIG, LLP (SV)			GBLENDE, JEFFREY A	
c/o: Greenberg Traurig LLP - Chicago Office			ART UNIT	
77 West Wacker Drive, Suite 3100			PAPER NUMBER	
Intellectual Property Department			2838	
Chicago, IL 60601			NOTIFICATION DATE	
			DELIVERY MODE	
			04/06/2018	
			ELECTRONIC	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

gtipmail@gtlaw.com
clairt@gtlaw.com
rupickd@gtlaw.com

Response to Rule 312 Communication	Application No. 15/392,960	Applicant(s) AVRUTSKY, MORDECHAY
	Examiner JEFFREY GBLENDE	Art Unit 2838

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

1. ☒ The amendment filed on 14 March 2018 under 37 CFR 1.312 has been considered, and has been:

a) ☒ entered.

b) ☐ entered as directed to matters of form not affecting the scope of the invention.

c) ☐ disapproved because the amendment was filed after the payment of the issue fee.

Any amendment filed after the date the issue fee is paid must be accompanied by a petition under 37 CFR 1.313(c)(1) and the required fee to withdraw the application from issue.

d) ☐ disapproved. See explanation below.

e) ☐ entered in part. See explanation below.

/JEFFREY GBLENDE/
Primary Examiner, Art Unit 2838

App. Ser. No.: 15/392,960

PATENT
Attorney Docket No.: 108144-201503/US (TGY015)**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor:	Mordechay Avrutsky	Examiner:	Jeffrey Gblende
Serial No.:	15/392,960	Group Art Unit:	2838
Filed:	December 28, 2016	Confirmation:	3397
Title:	Systems and Methods for Enhanced Efficiency Auxiliary Power Supply Module		

AMENDMENT UNDER 37 CFR 1.312

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir/Madam:

Please kindly enter the following amendments and consider the following
remarks/arguments:

Amendments to the Claims are reflected in the list of claims that begins on page 2 of this paper;

Remarks/Arguments begin on page 7 of this paper.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor:	Mordechay Avrutsky	Examiner:	Jeffrey Gblende
Serial No.:	15/392,960	Group Art Unit:	2838
Filed:	December 28, 2016	Confirmation:	3397
Title:	Systems and Methods for Enhanced Efficiency Auxiliary Power Supply Module		

AMENDMENT UNDER 37 CFR 1.312

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir/Madam:

Please kindly enter the following amendments and consider the following
remarks/arguments:

Amendments to the Claims are reflected in the list of claims that begins on page 2 of this paper;

Remarks/Arguments begin on page 7 of this paper.

CLAIMS

1. (Original) An apparatus, comprising:
a first stage power converter to generate a first output from a direct current input;
a second stage power converter connected to the first stage power converter to convert the first output to a second output;
a micro-controller;
a power absorption circuit; and
a control circuit coupled with the first stage power converter and the second stage power converter, the control circuit powered by the first output to control the second stage power converter, wherein:
when the second stage power converter is in a first mode of not providing the second output to power the micro-controller, the control circuit monitors a voltage of the first output powering the power absorption circuit and signals the second stage power converter to remain in the first mode during a time period in which the voltage is below a threshold; and
when the voltage is above the threshold, the control circuit signals the second stage power converter to enter a second mode of providing the second output to power the micro-controller.
2. (Original) The apparatus of claim 1, wherein the power absorption circuit comprises a capacitor coupled to the first output.
3. (Original) The apparatus of claim 2, wherein the voltage above the threshold allows the capacitor to power the second stage to operate the micro-controller for at least a predetermined period of time in absence of power input from the first stage during the predetermined period of time.
4. (Original) The apparatus of claim 1, wherein the power absorption circuit comprises a switchable load.
5. (Currently Amended) The apparatus of claim 4, wherein the control circuit signals the

switchable load to be in a first state to consume the first output when the second stage power converter is in the first mode and to be in a second state not to consume the first output when the voltage is above the threshold.

6. (Original) The apparatus of claim 5, wherein, when the switchable load is in the second state, the voltage above the threshold is sufficient to power the second state to operate the micro-controller for at least a predetermined period of time during which the direct current input to the first stage power converter has diminishing power.
7. (Original) The apparatus of claim 5, wherein the switchable load comprises:
a resistive device; and
a switching device connected to the resistive device, wherein the switching device is turned on in the first state to connect the resistive device to the first output and turned off in the second state to disconnect the resistive device from the first output.
8. (Original) The apparatus of claim 5, wherein the control circuit delays a first signal instructing the switching device to change from the first state to the second state to generate a second delayed signal that instructs the second stage to change from the first mode to the second mode.
9. (Original) The apparatus of claim 8, wherein the control circuit comprises:
a hysteresis amplifier coupled to the first output to generate the first signal; and
a delay circuit coupled to the first signal to generate the second delayed signal.
10. (Original) A method to supply power, the method comprising:
providing a first stage power converter to generate a first output from a direct current input, wherein the first stage power converter is coupled to a power absorption circuit;
providing a second stage power converter connected to the first stage power converter to convert the first output to a second output, wherein the second stage power converter is coupled to a micro-controller;
powering, by the first output, a control circuit that is coupled with the first stage power

converter and the second stage power converter; and
controlling, by the control circuit powered by the first output, the second stage power converter, by:

when the second stage power converter is in a first mode of not providing the second output to power the micro-controller, monitoring a voltage of the first output powering the power absorption circuit and signaling the second stage power converter to remain in the first mode during a time period in which the voltage is below a threshold; and
when the voltage is above the threshold, signaling the second stage power converter to enter a second mode of providing the second output to power the micro-controller.

11. (Original) The method of claim 10, further comprising:
providing, in the power absorption circuit, a capacitor coupled to the first output.
12. (Original) The method of claim 11, wherein the voltage above the threshold allows the capacitor to power the second stage to operate the micro-controller for at least a predetermined period of time in absence of power input from the first stage during the predetermined period of time.
13. (Original) The method of claim 10, further comprising:
providing, in the power absorption circuit, a switchable load.
14. (Currently Amended) The method of claim 13, wherein the control circuit signals the switchable load to be in a first state to consume the first output when the second stage power converter is in the first mode and to be in a second state not to consume the first output when the voltage is above the threshold.
15. (Original) The method of claim 14, wherein, when the switchable load is in the second state, the voltage above the threshold is sufficient to power the second state to operate the micro-controller for at least a predetermined period of time during which the direct current input to the first stage power converter has diminishing power.

16. (Original) The method of claim 14, further comprising:
providing, in the switchable load:
a resistive device; and
a switching device connected to the resistive device, wherein the switching device
is turned on in the first state to connect the resistive device to the first
output and turned off in the second state to disconnect the resistive device
from the first output.
17. (Original) The method of claim 14, further comprising:
delaying, by the control circuit, a first signal instructing the switching device to change
from the first state to the second state to generate a second delayed signal that
instructs the second stage to change from the first mode to the second mode.
18. (Original) The method of claim 17, further comprising:
providing, in the control circuit, a hysteresis amplifier coupled to the first output to
generate the first signal; and
providing a delay circuit coupled to the first signal to generate the second delayed signal.
19. (Original) A power supply for use in a solar electric production system, the power supply
comprising:
a first stage power converter having:
an input connected to a first voltage from a photovoltaic panel; and
an output providing a second voltage different from the first voltage from the
photovoltaic panel;
a second stage power converter connected to the output of the first stage power converter,
the second stage power converter supplying power at a third voltage to a micro-
controller of the photovoltaic panel; and
a control circuit coupled to the first stage power converter and the second stage power
converter and powered by the output of the first stage power converter, wherein
after the first stage power converter is turned on, the control circuit instructs the
second stage power converter to:
not supply the power at the third voltage to the micro-controller before a

determination that the output of the first stage power converter has been stable for a predetermined period of time; and
supply the power at the third voltage to the micro-controller after the determination that the output of the first stage power converter has been stable for the predetermined period of time.

20. (Original) The power supply of claim 19, wherein the first stage power converter is a first step-down converter, and the second stage power converter is a second step-down converter.

REMARKS

The above amendments correct typographical errors and/or remove informality.
Authorization is hereby provided to charge any fees which may be required for the present filing,
or credit any overpayment, to Deposit Account No. 50-2638.

Respectfully submitted,

Date: March, 14, 2018

/Lehua Wang/
Lehua Wang
Reg. No. 48023

GREENBERG TRAURIG, LLP
CUSTOMER NUMBER **64494**
Phone: (650) 328-8500
Fax: (650) 328-8508

Electronic Patent Application Fee Transmittal

Application Number:	15392960			
Filing Date:	28-Dec-2016			
Title of Invention:	SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE			
First Named Inventor/Applicant Name:	Mordechay AVRUTSKY			
Filer:	John Patrick Ward/YIJIE SHEN			
Attorney Docket Number:	108144-201503/US (TGY015)			
Filed as Small Entity				
Filing Fees for Utility under 35 USC 111(a)				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
UTILITY APPL ISSUE FEE	2501	1	480	480

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				480

Electronic Acknowledgement Receipt

EFS ID:	32051826
Application Number:	15392960
International Application Number:	
Confirmation Number:	3397
Title of Invention:	SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE
First Named Inventor/Applicant Name:	Mordechay AVRUTSKY
Customer Number:	64494
Filer:	John Patrick Ward/YIJIE SHEN
Filer Authorized By:	John Patrick Ward
Attorney Docket Number:	108144-201503/US (TGY015)
Receipt Date:	14-MAR-2018
Filing Date:	28-DEC-2016
Time Stamp:	15:45:48
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	DA
Payment was successfully received in RAM	\$ 480
RAM confirmation Number	031518INTEFSW00002298502638
Deposit Account	502638
Authorized User	YIJIE SHEN

The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:

37 CFR 1.16 (National application filing, search, and examination fees)

37 CFR 1.17 (Patent application and reexamination processing fees)

Page 15 of 135 SMA Solar Technology America, LLC, Ex. 1006

37 CFR 1.19 (Document supply fees)
37 CFR 1.20 (Post Issuance fees)
37 CFR 1.21 (Miscellaneous fees and charges)

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	108144-201503_IssueFeeTransmittal.PDF	128784	no	2
			c14e6ce71c20c7a0c217f78c98a4a43bfc49b1c		
Warnings:					
Information:					
2	Miscellaneous Incoming Letter	108144-201503_CommentsonStatementofReasonsforAllowance.pdf	85358	no	1
			6a9b47b861b00774bd199e7143d7283ad908c1bf		
Warnings:					
Information:					
3		108144-201503_312Amendment.pdf	103684	yes	7
			1a1f336574a2fc13a831eec5808a0d366becfb25		
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment after Notice of Allowance (Rule 312)		1	1	
	Claims		2	6	
	Applicant Arguments/Remarks Made in an Amendment		7	7	
Warnings:					
Information:					
4	Fee Worksheet (SB06)	fee-info.pdf	30738	no	2
			7a388f2915bcf5afb129d75231e40e95c4ac14d0		
Warnings:					
Information:					
Total Files Size (in bytes):			348564		

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

PART B—FEE(S) TRANSMITTAL

Complete and send this form, together with the applicable fee(s), to: Mail

Mail Stop ISSUE FEE
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, Virginia 22314-1450
 (571)-273-2885

or Fax

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note Use Block 1 for any change of address)

Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

Certificate of Mailing or Transmission

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, EFS-Web transmitted, or facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

YIJIE SHEN	(Depositor's Name)
/YIJIE SHEN/	(S-signature)
3/14/2018	(Date)

64494
 GREENBERG TRAUIG, LLP (SV)
 c/o: Greenberg Traurig LLP - Chicago Office
 77 West Wacker Drive, Suite 3100
 Intellectual Property Department
 Chicago, IL 60601

APPLICATION NO	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO	CONFIRMATION NO
15/392,960	12/28/2016	Mordechay AVRUTSKY	108144-201503/US (TGY015)	3397

TITLE OF INVENTION:

APPLN: TYPE	ENTITY STATUS	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	SMALL	\$480	\$0	\$0	\$480	03/19/2018

EXAMINER	ART UNIT	CLASS-SUBCLASS

1. Change of correspondence address or indication of "Fee Address" (37CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
- ☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. **Use of a Customer Number is required.**

2. For printing on the patent front page, list
- (1) The names of up to 3 registered patent attorneys or agents OR, alternatively,
- (2) The name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered attorney or agent. If no name is listed, no name will be printed

1. Greenberg Traurig, LLP

2.

3.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

1a. NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

TIGO ENERGY, INC.

LOS GATOS, CALIFORNIA

Please check the appropriate assignee categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are submitted:

- ☒ Issue Fee
- ☐ Publication Fee (No small entity discount permitted)
- ☐ Advance Order - # of Copies

4b. Payment of Fees(s): (Please first reapply and previously paid fee shown above)

- ☐ A check is enclosed
- ☐ Payment by credit card. Form PTO-2038 is attached
- ☒ The director is hereby authorized to charge the required fee(s), any deficiency, or credits any overpayment, to Deposit Account Number 502638 (enclose an extra copy of this form)

5. Change of Entity Status (from status indicated above)

- ☐ Applicant certifying micro entity status. See 37 CFR 1.29
- ☐ Applicant asserting small entity status. See 37 CFR 1.27
- ☐ Applicant changing to regular undiscounted fee status.

NOTE: Absent a valid certification of Micro Entity Status (see forms PTO/SB/15A and 15B), issue fee payment in the micro entity amount will not be accepted at the risk of application abandonment.

NOTE: If the application was previously under micro entity status, checking this box will be taken to be a notification of loss of entitlement to micro entity status.

NOTE: Checking this box will be taken to be notification of loss of entitlement to small or micro entity status, as applicable.

NOTE: This form must be signed in accordance with 37 CFR 1.31 and 1.33. See 37 CFR 1.4 for signature requirements and certifications.

Authorized Signature /John P. Ward/

Date 3/14/2018

Typed or printed name John P. Ward

Registration No. 40216

Page 18 of 135

SMA Solar Technology America, LLC, Ex 1006

OMB Clearance and PRA Burden Statement for PTOL-85 Part B

The Paperwork Reduction Act (PRA) of 1995 requires Federal agencies to obtain Office of Management and Budget approval before requesting most types of information from the public. When OMB approves an agency request to collect information from the public, OMB (i) provides a valid OMB Control Number and expiration date for the agency to display on the instrument that will be used to collect the information and (ii) requires the agency to inform the public about the OMB Control Number's legal significance in accordance with 5 CFR 1320.5(b).

The information collected by PTOL-85 Part B is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 30 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450. Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Privacy Act Statement

The **Privacy Act of 1974 (P.L. 93-579)** requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether disclosure of these records is required by the Freedom of Information Act.
2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.
4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (*i.e.*, GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspection or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor:	Mordechay Avrutsky	Examiner:	Jeffrey Gblende
Serial No.:	15/392,960	Group Art Unit:	2838
Filed:	December 28, 2016	Confirmation:	3397
Title:	Systems and Methods for Enhanced Efficiency Auxiliary Power Supply Module		

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir/Madam:

Applicant respectfully provides the following comments on the Statement of Reasons for Allowance that was provided in the Notice of Allowability mailed in the subject application.

Applicant assumes that the claims are to be interpreted based on the features recited in the claims rather than any paraphrases provided in the Statement of Reasons for Allowance, and the Statement of Reasons for Allowance is to be taken in light of the exact features recited in the claims. The claims should not be interpreted to include the features that are not explicitly recited in the claims. Any paraphrases in the Statement of Reasons for Allowance should not be used in a way to narrow the scope of the claims.

Respectfully submitted,

Date: March 14, 2018

/John P. Ward/
John P. Ward
Reg. No. 40216

GREENBERG TRAURIG, LLP
CUSTOMER NUMBER **64494**
Phone: (650) 328-8500
Fax: (650) 328-8508



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

NOTICE OF ALLOWANCE AND FEE(S) DUE

64494 7590 12/19/2017
GREENBERG TRAURIG, LLP (SV)
c/o: Greenberg Traurig LLP - Chicago Office
77 West Wacker Drive, Suite 3100
Intellectual Property Department
Chicago, IL 60601

EXAMINER

GBLENDE, JEFFREY A

ART UNIT

PAPER NUMBER

2838

DATE MAILED: 12/19/2017

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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15/392,960

12/28/2016

Mordechay AVRUTSKY

108144-201503/US

3397

TITLE OF INVENTION: SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE (TGY015)

APPLN. TYPE	ENTITY STATUS	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional

SMALL

\$480

\$0

\$0

\$480

03/19/2018

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the ENTITY STATUS shown above. If the ENTITY STATUS is shown as SMALL or MICRO, verify whether entitlement to that entity status still applies.

If the ENTITY STATUS is the same as shown above, pay the TOTAL FEE(S) DUE shown above.

If the ENTITY STATUS is changed from that shown above, on PART B - FEE(S) TRANSMITTAL, complete section number 5 titled "Change in Entity Status (from status indicated above)".

For purposes of this notice, small entity fees are 1/2 the amount of undiscounted fees, and micro entity fees are 1/2 the amount of small entity fees.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Maintenance fees are due in utility patents issuing on applications filed on or after Dec. 12, 1980. It is patentee's responsibility to ensure timely payment of maintenance fees when due. More information is available at www.uspto.gov/PatentMaintenanceFees.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: **Mail** **Mail Stop ISSUE FEE**
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450
or Fax **(571)-273-2885**

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)

Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

64494 7590 12/19/2017
GREENBERG TRAURIG, LLP (SV)
c/o: Greenberg Traurig LLP - Chicago Office
77 West Wacker Drive, Suite 3100
Intellectual Property Department
Chicago, IL 60601

Certificate of Mailing or Transmission

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

(Depositor's name)
(Signature)
(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/392,960	12/28/2016	Mordechay AVRUTSKY	108144-201503/US (TGY015)	3397

TITLE OF INVENTION: SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE

APPLN. TYPE	ENTITY STATUS	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	SMALL	\$480	\$0	\$0	\$480	03/19/2018

EXAMINER	ART UNIT	CLASS-SUBCLASS
GBLENDE, JEFFREY A	2838	323-282000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. **Use of a Customer Number is required.**

2. For printing on the patent front page, list

(1) The names of up to 3 registered patent attorneys or agents OR, alternatively,

1 _____

(2) The name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

2 _____

3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☐ Corporation or other private group entity ☐ Government

4a. The following fee(s) are submitted:

- ☐ Issue Fee
☐ Publication Fee (No small entity discount permitted)
☐ Advance Order - # of Copies _____

4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above)

- ☐ A check is enclosed.
☐ Payment by credit card. Form PTO-2038 is attached.
☐ The director is hereby authorized to charge the required fee(s), any deficiency, or credits any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)

- ☐ Applicant certifying micro entity status. See 37 CFR 1.29
☐ Applicant asserting small entity status. See 37 CFR 1.27
☐ Applicant changing to regular undiscounted fee status.

NOTE: Absent a valid certification of Micro Entity Status (see forms PTO/SB/15A and 15B), issue fee payment in the micro entity amount will not be accepted at the risk of application abandonment.

NOTE: If the application was previously under micro entity status, checking this box will be taken to be a notification of loss of entitlement to micro entity status.

NOTE: Checking this box will be taken to be a notification of loss of entitlement to small or micro entity status, as applicable.

NOTE: This form must be signed in accordance with 37 CFR 1.31 and 1.33. See 37 CFR 1.4 for signature requirements and certifications.

Authorized Signature _____

Date _____

Typed or printed name _____

Registration No. _____



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
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Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
15/392,960	12/28/2016	Mordechay AVRUTSKY	108144-201503/US (TGY015)	3397
64494 7590 12/19/2017 GREENBERG TRAUIG, LLP (SV) c/o: Greenberg Traurig LLP - Chicago Office 77 West Wacker Drive, Suite 3100 Intellectual Property Department Chicago, IL 60601			EXAMINER GBLENDE, JEFFREY A	
			ART UNIT 2838	PAPER NUMBER

DATE MAILED: 12/19/2017

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b) (Applications filed on or after May 29, 2000)

The Office has discontinued providing a Patent Term Adjustment (PTA) calculation with the Notice of Allowance.

Section 1(h)(2) of the AIA Technical Corrections Act amended 35 U.S.C. 154(b)(3)(B)(i) to eliminate the requirement that the Office provide a patent term adjustment determination with the notice of allowance. See Revisions to Patent Term Adjustment, 78 Fed. Reg. 19416, 19417 (Apr. 1, 2013). Therefore, the Office is no longer providing an initial patent term adjustment determination with the notice of allowance. The Office will continue to provide a patent term adjustment determination with the Issue Notification Letter that is mailed to applicant approximately three weeks prior to the issue date of the patent, and will include the patent term adjustment on the patent. Any request for reconsideration of the patent term adjustment determination (or reinstatement of patent term adjustment) should follow the process outlined in 37 CFR 1.705.

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

OMB Clearance and PRA Burden Statement for PTOL-85 Part B

The Paperwork Reduction Act (PRA) of 1995 requires Federal agencies to obtain Office of Management and Budget approval before requesting most types of information from the public. When OMB approves an agency request to collect information from the public, OMB (i) provides a valid OMB Control Number and expiration date for the agency to display on the instrument that will be used to collect the information and (ii) requires the agency to inform the public about the OMB Control Number's legal significance in accordance with 5 CFR 1320.5(b).

The information collected by PTOL-85 Part B is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450. Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether disclosure of these records is required by the Freedom of Information Act.
2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.
4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspection or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

Notice of Allowability	Application No. 15/392,960	Applicant(s) AVRUTSKY, MORDECHAY	
	Examiner JEFFREY GBLENDE	Art Unit 2838	AIA (First Inventor to File) Status No

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the application filed on 12/28/2016.
☐ A declaration(s)/affidavit(s) under **37 CFR 1.130(b)** was/were filed on _____.
2. ☐ An election was made by the applicant in response to a restriction requirement set forth during the interview on _____; the restriction requirement and election have been incorporated into this action.
3. ☒ The allowed claim(s) is/are 1-20. As a result of the allowed claim(s), you may be eligible to benefit from the **Patent Prosecution Highway** program at a participating intellectual property office for the corresponding application. For more information, please see http://www.uspto.gov/patents/init_events/pph/index.jsp or send an inquiry to PPHfeedback@uspto.gov.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

Certified copies:

- a) ☐ All b) ☐ Some *c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|---|
| 1. ☒ Notice of References Cited (PTO-892) | 5. ☒ Examiner's Amendment/Comment |
| 2. ☒ Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____ | 6. ☐ Examiner's Statement of Reasons for Allowance |
| 3. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 7. ☐ Other _____. |
| 4. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____. | |

/JEFFREY GBLENDE/
Primary Examiner, Art Unit 2838

Examiner's Comment

Allowable Subject Matter

1. Claims 1-20 are allowed.
2. The following is an examiner's statement of reasons for allowance:

Regarding claim 1, the prior art fails to teach or disclose an apparatus, comprising: a first stage power converter to generate a first output from a direct current input; a second stage power converter connected to the first stage power converter to convert the first output to a second output; a micro-controller; a power absorption circuit; and a control circuit coupled with the first stage power converter and the second stage power converter, the control circuit powered by the first output to control the second stage power converter, wherein: when the second stage power converter is in a first mode of not providing the second output to power the micro-controller, the control circuit monitors a voltage of the first output powering the power absorption circuit and signals the second stage power converter to remain in the first mode during a time period in which the voltage is below a threshold; and when the voltage is above the threshold, the control circuit signals the second stage power converter to enter a second mode of providing the second output to power the micro-controller.

Regarding claim 10, the prior art fails to teach or disclose a method to supply power, the method comprising: providing a first stage power converter to generate a first output from a direct current input, wherein the first stage power converter is coupled to a power absorption circuit; providing a second stage power converter connected to the first stage power converter to convert the first output to a second output, wherein the

Art Unit: 2838

second stage power converter is coupled to a micro-controller; powering, by the first output, a control circuit that is coupled with the first stage power converter and the second stage power converter; and controlling, by the control circuit powered by the first output, the second stage power converter, by: when the second stage power converter is in a first mode of not providing the second output to power the micro-controller, monitoring a voltage of the first output powering the power absorption circuit and signaling the second stage power converter to remain in the first mode during a time period in which the voltage is below a threshold; and when the voltage is above the threshold, signaling the second stage power converter to enter a second mode of providing the second output to power the micro-controller.

Regarding claim 19, the prior art fails to teach or disclose a power supply for use in a solar electric production system, the power supply comprising: a first stage power converter having: an input connected to a first voltage from a photovoltaic panel; and an output providing a second voltage different from the first voltage from the photovoltaic panel; a second stage power converter connected to the output of the first stage power converter, the second stage power converter supplying power at a third voltage to a micro-controller of the photovoltaic panel; and a control circuit coupled to the first stage power converter and the second stage power converter and powered by the output of the first stage power converter, wherein after the first stage power converter is turned on, the control circuit instructs the second stage power converter to: not supply the power at the third voltage to the micro-controller before a determination that the output of the first stage power converter has been stable for a predetermined period of time; and supply the power at the third voltage to the micro-controller after the determination

Art Unit: 2838

that the output of the first stage power converter has been stable for the predetermined period of time.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure:

Ishino (US Patent 7397151) discloses a power supply unit and portable apparatus using the same.

Inn et al. (US 2006/0158165) discloses a dual mode buck regulator with improved transition between LDO and PWM operation.

Pai (US Patent 7411316) discloses a dual-input power converter and control methods thereof.

Hart et al. (US Patent 7301313) discloses multiple voltage regulators for use with a single load.

Daniels (US 2003/0048003) discloses a method for simultaneous output ramp up of multiple regulators.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JEFFREY GBLENDE whose telephone number is

Art Unit: 2838

(571)270-5472. The examiner can normally be reached on Monday - Friday 8:00am - 5:00pm EST.

Examiner interviews are available via telephone, in-person, and video conferencing using a USPTO supplied web-based collaboration tool. To schedule an interview, applicant is encouraged to use the USPTO Automated Interview Request (AIR) at <http://www.uspto.gov/interviewpractice>.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Monica Lewis can be reached on 571-272-1838. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/JEFFREY GBLENDE/
Primary Examiner, Art Unit 2838

Notice of References Cited	Application/Control No. 15/392,960		Applicant(s)/Patent Under Reexamination AVRUTSKY, MORDECHAY	
	Examiner JEFFREY GBLENDE		Art Unit 2838	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	CPC Classification	US Classification
*	A	US-7,397,151 B2	07-2008	Ishino; Tsutomu	H02M3/1584	307/150
*	B	US-2006/0158165 A1	07-2006	Inn; Bruce L.	G05F1/575	323/280
*	C	US-7,411,316 B2	08-2008	Pai; Chung-Lung	H02M1/10	307/81
*	D	US-7,301,313 B1	11-2007	Hart; Frank P.	G06F1/263	307/74
*	E	US-2003/0048003 A1	03-2003	Daniels, David G.	H02J1/00	307/42
	F	US-				
	G	US-				
	H	US-				
	I	US-				
	J	US-				
	K	US-				
	L	US-				
	M	US-				

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	CPC Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

App. Ser. No.: 15/392,960

Attorney Docket No.: 108144-201503/US (TGY015)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor:	Mordechay Avrutsky	Examiner:	Monica Lewis
Serial No.:	15/392,960	Group Art Unit:	2838
Filed:	December 28, 2016	Confirmation:	3397
Title:	Systems and Methods for Enhanced Efficiency Auxiliary Power Supply Module		

NOTICE OF RELATED APPLICATIONS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir/Madam:

Applicant is hereby providing notice that the subject patent application is related to the following U.S. patent applications. Applicant respectfully requests the Examiner to consider the related applications and the prosecution histories of these related applications, such as the current claims, Office Actions and Notices of Allowance, etc.

1.	Title:	Systems and Methods for Enhanced Efficiency Auxiliary Power Supply Module	
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Serial No.:	12/787,205	Filed on:	May 25, 2010
Inventor(s):	Mordechay AVRUTSKY	Docket No.:	108144-201501/US (TGY015)
Examiner:	Jeffrey Gblende	Art Unit:	2838
Patent No.:	9,143,036	Issue Date:	September 22, 2015

2.	Title:	Systems and Methods for Enhanced Efficiency Auxiliary Power Supply Module	
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Serial No.:	14/822,227	Filed on:	August 10, 2015
Inventor(s):	Mordechay AVRUTSKY	Docket No.:	108144-201502/US (TGY015)
Examiner:	Jeffrey Gblende	Art Unit:	2838
Status:	Notice of Allowance Mailed --	Status Date:	November 16, 2016

App. Ser. No.: 15/392,960

Attorney Docket No.: 108144-201503/US (TGY015)

Application Received in Office of
Publications

Respectfully submitted,

Date: July 21, 2017

/Lehua Wang/
Lehua Wang
Reg. No. 48,023

GREENBERG TRAURIG, LLP
CUSTOMER NUMBER **64494**
Phone: (650) 328-8500
Fax: (650) 328-8508

/JEFFREY A GLENDE/

12/08/2017

Doc code: IDS

PTO/SB/08a (01-10)

Doc description: Information Disclosure Statement (IDS) Filed

Approved for use through 07/31/2012. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	15392960
	Filing Date	2016-12-28
	First Named Inventor	MORDECHAY AVRUTSKY
	Art Unit	2838
	Examiner Name	GBLENDE, JEFFREY A
	Attorney Docket Number	108144-201503/US (TGY015)

U.S.PATENTS						Remove
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	8013472		2011-09-06	Adest, et al.	
	2	8035249		2011-10-11	Shaver, II, et al.	
	3	8427009		2013-04-23	Shaver, II, et al.	
	4	8531055		2013-09-10	Adest, et al.	
	5	8587151		2013-11-19	Adest, et al.	
	6	8669675		2014-03-11	Capp, et al.	
	7	8816535		2014-08-26	Adest, et al.	
	8	8947194		2015-02-03	Sella, et al.	

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)

Application Number	15392960
Filing Date	2016-12-28
First Named Inventor	MORDECHAY AVRUTSKY
Art Unit	2838
Examiner Name	GBLENDE, JEFFREY A
Attorney Docket Number	108144-201503/US (TGY015)

9	9112379	2015-08-18	Sella, et al.
10	9401599	2016-07-26	Har-Shai, et al.
11	9438035	2016-09-06	Capp, et al.
12	9590526	2017-03-07	Adest, et al.

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	15392960
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Art Unit	2838
Examiner Name	GBLENDE, JEFFREY A
Attorney Docket Number	108144-201503/US (TGY015)

1	
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Examiner Signature	/JEFFREY A GBLENDE/	Date Considered	12/08/2017
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¹ See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. ² Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). ³ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁴ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁵ Applicant is to place a check mark here if English language translation is attached.

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	15392960
Filing Date	2016-12-28
First Named Inventor	MORDECHAY AVRUTSKY
Art Unit	2838
Examiner Name	GBLENDE, JEFFREY A
Attorney Docket Number	108144-201503/US (TGY015)

CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

OR

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Lehua Wang/	Date (YYYY-MM-DD)	2017-11-02
Name/Print	Lehua Wang	Registration Number	48023

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

Bibliographic Data

Application No: 15392960

Foreign Priority claimed: ☐ Yes ☒ No

35 USC 119 (a-d) conditions met: ☐ Yes ☒ No ☐ Met After Allowance

Verified and Acknowledged:

Examiner's Signature

Initials

Title:

SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY
AUXILIARY POWER SUPPLY MODULE

FILING or 371(c) DATE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.
12/28/2016	323	2838	108144-201503/US (TGY015)
RULE			

APPLICANTS

Tigo Energy, Inc., Los Gatos, CA, UNITED STATES

INVENTORS

Mordechay AVRUTSKY, Alfei Menashe, ISRAEL

CONTINUING DATA

This application is a PRO of 61275734 09/02/2009

This application is a PRO of 61336743 01/26/2010

This application is a CON of 12787205 05/25/2010PAT 9143036

This application is a CON of 14822227 08/10/2015PAT 9584021

FOREIGN APPLICATIONS

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Chicago, IL 60601

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Search Notes 	Application/Control No. 15392960	Applicant(s)/Patent Under Reexamination AVRUTSKY, MORDECHAY
	Examiner JEFFREY GBLENDE	Art Unit 2838

CPC- SEARCHED		
Symbol	Date	Examiner
See attached EAST search history	12/8/2017	JG

CPC COMBINATION SETS - SEARCHED		
Symbol	Date	Examiner

US CLASSIFICATION SEARCHED			
Class	Subclass	Date	Examiner

* See search history printout included with this form or the SEARCH NOTES box below to determine the scope of the search.

SEARCH NOTES		
Search Notes	Date	Examiner
Inventor name search	12/8/2017	JG
See attached EAST search history	12/8/2017	JG

INTERFERENCE SEARCH			
US Class/ CPC Symbol	US Subclass / CPC Group	Date	Examiner
	Interference search history attached	12/8/2017	JG

	/J.G./ Primary Examiner.Art Unit 2838
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Issue Classification 	Application/Control No. 15392960	Applicant(s)/Patent Under Reexamination AVRUTSKY, MORDECHAY	
	Examiner JEFFREY GBLENDE	Art Unit 2838	

CPC						
Symbol					Type	Version
H02M		3	/	158	F	2013-01-01
H02M		3	/	07	I	2013-01-01
H02M		1	/	36	A	2013-01-01
H02M		2001	/	007	A	2013-01-01
H02M		2001	/	008	A	2013-01-01
Y02E		10	/	56	A	2013-01-01
H02M		1	/	08	I	2013-01-01
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CPC Combination Sets				
Symbol	Type	Set	Ranking	Version

NONE		Total Claims Allowed:	
		20	
(Assistant Examiner) /JEFFREY GBLENDE/ Primary Examiner.Art Unit 2838	(Date) 12/08/2017	O.G. Print Claim(s)	O.G. Print Figure
(Primary Examiner)	(Date)	1	3

Issue Classification 	Application/Control No. 15392960	Applicant(s)/Patent Under Reexamination AVRUTSKY, MORDECHAY	
	Examiner JEFFREY GBLENDE	Art Unit 2838	

US ORIGINAL CLASSIFICATION						INTERNATIONAL CLASSIFICATION													
CLASS		SUBCLASS				CLAIMED					NON-CLAIMED								
						H	0	2	M	3 / 158 (2006.01.01)									
CROSS REFERENCE(S)																			
CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)																		

NONE		Total Claims Allowed:	
		20	
(Assistant Examiner) /JEFFREY GBLENDE/ Primary Examiner.Art Unit 2838	(Date) 12/08/2017	O.G. Print Claim(s)	O.G. Print Figure
(Primary Examiner)	(Date)	1	3

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	51	("20050017701" "20050040800" "20050057214" "20050057215" "20060001406" "20060174939" "20060176031" "20060185727" "20070001653" "20070273351" "20070278861" "20080100233" "20080121272" "20080122449" "20080122518" "20080179949" "20080191560" "20080191675" "20080303503" "20100013452" "20100117614" "20110031946" "20110050190" "20110148388" "20150349644" "4719553" "5235266" "5268832" "5604430" "5903138" "5923158" "6275016" "6448489" "6650031" "6844739" "6894911" "6984970" "7061214" "7202653" "7248946" "7256566" "7276886" "7518346" "7560915" "7595616" "7605498" "7719140" "7821281" "8004866" "9143036" "9584021").PN.	US-PGPUB; USPAT	OR	OFF	2017/11/28 11:26
S2	12	("8013472" "8035249" "8427009" "8531055" "8587151" "8669675" "8816535" "8947194" "9112379" "9401599" "9438035" "9590526").PN.	US-PGPUB; USPAT	OR	OFF	2017/11/28 11:33
S3	43	((("AVRUTSKY") near3 ("Mordechay"))).INV.	US-PGPUB; USPAT; USOCR	OR	OFF	2017/11/28 11:51
S4	60613	(G05F1/00 G05F1/10 G05F1/12 G05F1/46 G05F1/455 G05F1/45 G05F1/445 H02M5/2573 G05F1/66 H02M1/081 H02M5/293 H02M7/12 H05B39/048 B23K11/24 G05F1/40 G05F1/42 G05F1/44 G05F1/462 G05F1/52 G05F1/56 H02M3/10 H02M3/125 H02M3/13 H02M3/135 H02M3/145 H02M3/15 H02M3/155 G05F1/562 G05F1/565 G05F1/575 H02M2001/0045 H02M3/1563 H02M3/156).CPC.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2017/11/28 14:17
S5	55340	(H02M3/1582 H02M3/1584 H02M3/285 H02M3/33561 H02M7/49 H02J3/46 H02J3/38 H02M1/045 H02M7/006 H02M7/06 H02M7/068 H02M7/153 H02M7/10 H02M1/088 H02M7/103 H02M7/106 H02M7/19 H02M7/08 H02M7/17 H02M2001/007 H02M7/493 H02M7/53806 H02M7/5381).CPC.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2017/11/28 14:18

S6	4280	S4 and S5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2017/11/28 14:18
S7	135269	first near2 (regulator or converter or (power near (stage or module or circuit or unit)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2017/11/28 14:18
S8	110718	second near2 (regulator or converter or (power near (stage or module or circuit or unit)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2017/11/28 14:19
S9	8539264	controller or (control near (unit or module or circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2017/11/28 14:23
S10	288026	((output near voltage) or feed\$1back\$1) with (threshold or reference)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2017/11/28 14:24
S11	251	S7 same S8 same S10 same (select\$5 or choos\$4 or pick\$4 or enabl\$4 or "on")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2017/11/28 14:25
S12	4	S6 and S11	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2017/11/28 14:25
S13	39805	((output near voltage) or feed\$1back\$1) with (threshold or reference) with (below or small\$4 or less)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2017/11/28 15:07
S14	204	S7 same S8 same S13	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO;	OR	OFF	2017/11/28 15:09

			DERWENT; IBM_TDB			
S15	142	S14 not S11	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2017/11/28 15:09
S16	2586	S7 with S8 with series	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2017/11/28 15:24
S17	359	S16 same (select\$5 or choos\$4 or pick\$4 or enabl\$4 or "on")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2017/11/28 15:24
S18	123	S10 and S17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2017/11/28 15:24

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S19	8246	(G05F1/00 G05F1/10 G05F1/12 G05F1/46 G05F1/455 G05F1/45 G05F1/445 H02M5/2573 G05F1/66 H02M1/081 H02M5/293 H02M7/12 H05B39/048 B23K11/24 G05F1/40 G05F1/42 G05F1/44 G05F1/462 G05F1/52 G05F1/56 H02M3/10 H02M3/125 H02M3/13 H02M3/135 H02M3/145 H02M3/15 H02M3/155 G05F1/562 G05F1/565 G05F1/575 H02M2001/0045 H02M3/1563 H02M3/156).CPC.	US- PGPUB	OR	OFF	2017/11/29 12:17
S20	6810	(H02M3/1582 H02M3/1584 H02M3/285 H02M3/33561 H02M7/49 H02J3/46 H02J3/38 H02M1/045 H02M7/006 H02M7/06 H02M7/068 H02M7/153 H02M7/10 H02M1/088 H02M7/103 H02M7/106 H02M7/19 H02M7/08 H02M7/17 H02M2001/007 H02M7/493 H02M7/53806 H02M7/5381).CPC.	US- PGPUB	OR	OFF	2017/11/29 12:19
S21	40075	first near2 (regulator or converter or (power near (stage or module or circuit or unit)))	US- PGPUB	OR	OFF	2017/11/29 12:19
S22	33360	second near2 (regulator or converter or (power near (stage or module or circuit or unit)))	US- PGPUB	OR	OFF	2017/11/29 12:19

S23	85544	((output near voltage) or feed\$1back\$1) with (threshold or reference)	US-PGPUB	OR	OFF	2017/11/29 12:19
S24	1528320	controller or (control near (unit or module or circuit))	US-PGPUB	OR	OFF	2017/11/29 12:19
S25	19	(S21 same S22 same S23 same S24 same (select\$5 or choos\$4 or pick\$4 or enabl\$4 or "on")).clm.	US-PGPUB	OR	OFF	2017/11/29 12:21

12/ 8/ 2017 11:44:39 AM
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Doc code: IDS

PTO/SB/08a (03-15)

Doc description: Information Disclosure Statement (IDS) Filed

Approved for use through 07/31/2016. OMB 0651-0031

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	Filing Date	2016-12-28
	First Named Inventor	MORDECHAY AVRUTSKY
	Art Unit	2838
	Examiner Name	LEWIS, MONICA
	Attorney Docket Number	108144-201503/US (TGY015)

U.S.PATENTS						Remove
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
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	2	5235266		1993-08-10	Schaffrin	
	3	5268832		1993-12-07	Kandatsu	
	4	5604430		1997-02-18	Decker, et al.	
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	15392960
Filing Date	2016-12-28
First Named Inventor	MORDECHAY AVRUTSKY
Art Unit	2838
Examiner Name	LEWIS, MONICA
Attorney Docket Number	108144-201503/US (TGY015)

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10	6844739	2005-01-18	Kasai, et al.
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12	6984970	2006-01-10	Capel
13	7061214	2006-06-13	Mayega, et al.
14	7202653	2007-04-10	Pai
15	7248946	2007-07-24	Bashaw, et al.
16	7256566	2007-08-14	Bhavaraju, et al.
17	7276886	2007-10-02	Kinder, et al.
18	7518346	2009-04-14	Prexl, et al.
19	7560915	2009-07-14	Ito, et al.

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

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Art Unit	2838
Examiner Name	LEWIS, MONICA
Attorney Docket Number	108144-201503/US (TGY015)

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21	7605498	2009-10-20	Ledenev, et al.
22	7719140	2010-05-18	Ledenev, et al.
23	7821281	2010-10-26	Chen
24	8004866	2011-08-23	Bucella, et al.
25	9143036	2015-09-22	Avrutsky
26	9584021	2017-02-28	Avrutsky

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	1	20050017701		2005-01-27	Hsu, Chih-Yuan	
	2	20050040800		2005-02-24	Sutardja, Sehat	

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	15392960
Filing Date	2016-12-28
First Named Inventor	MORDECHAY AVRUTSKY
Art Unit	2838
Examiner Name	LEWIS, MONICA
Attorney Docket Number	108144-201503/US (TGY015)

3	20050057214	2005-03-17	Matan, Stefan
4	20050057215	2005-03-17	Matan, Stefan
5	20060001406	2006-01-05	Matan; Stefan
6	20060174939	2006-08-10	Matan; Stefan
7	20060176031	2006-08-10	Dustin D. Forman, et al.
8	20060185727	2006-08-24	Matan; Stefan
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12	20080100233	2008-05-01	Negoi; Andy C.
13	20080121272	2008-05-29	David A. Besser, et al.

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Examiner Name	LEWIS, MONICA
Attorney Docket Number	108144-201503/US (TGY015)

14	20080122449	2008-05-29	David A. Besser, et al.
15	20080122518	2008-05-29	David A. Besser, et al.
16	20080179949	2008-07-31	David A. Besser, et al.
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18	20080191675	2008-08-14	David A. Besser, et al.
19	20080303503	2008-12-11	Wolfs; Peter
20	20100013452	2010-01-21	BENJAMIM TANG, et al.
21	20100117614	2010-05-13	Justin P. Bandholz, et al.
22	20110031946	2011-02-10	Patrick K. Egan, et al.
23	20110050190	2011-03-03	AVRUTSKY; Mordechay
24	20110148388	2011-06-23	Alfio Zanchi, et al.

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Attorney Docket Number	108144-201503/US (TGY015)

25	20150349644	2015-12-03	AVRUTSKY; Mordechay
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FOREIGN PATENT DOCUMENTS

Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ² i	Kind Code ⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T ⁵
	1	2005262278	AU		2005-07-12	Central Queensland University et al.		
	2	19961705	DE		2001-07-05	SMA Regelsysteme GmbH		
	3	4232356	DE		1994-03-31	Inst Solare Energieversorgungs		
	4	1388774	EP		2004-02-11	CIT Alcatel		
	5	2249147	ES		2006-03-16	Fundacion Robotiker		
	6	2003012569	WO		2003-02-13	Stichting Energie et al.		

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NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
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1	ALONSO, R. et al., "A New Distributed Converter Interface for PV Panels," 20th European Photovoltaic Solar Energy Conference, Barcelona, Spain, pp. 2288-2291, June 6-10, 2005.
2	ALONSO, R. et al., "Experimental Results of Intelligent PV Module for Grid-Connected PV Systems," 21st European Photovoltaic Solar Energy Conference, Dresden, Germany, pp. 2297-2300, September 4-8, 2006.
3	BASSO, Tim, "IEEE Standard for Interconnecting Distributed Resources With the Electric Power System," IEEE PES Meeting, June 9, 2004.
4	Boostbuck.com, "The Four Boostbuck Topologies," located at http://www.boostbuck.com/TheFourTopologies.html , 2003.
5	ENSLIN, Johan H.R., et al., "Integrated Photovoltaic Maximum Power Point Tracking Converter," IEEE Transactions on Industrial Electronics, Vol. 44, No. 6, pp. 769-773, December 1997.
6	GAUTAM, Nalin K. et al., "An Efficient Algorithm to Simulate the Electrical Performance of Solar Photovoltaic Arrays," Energy, Vol. 27, No. 4, pp. 347-361, 2002.
7	International Patent Application No. PCT/US2010/046272, International Search Report and Written Opinion, March 31, 2011.
8	LINARES, Leonor et al., "Improved Energy Capture in Series String Photovoltaics via Smart Distributed Power Electronics," 24th Annual IEEE Applied Power Electronics Conference and Exposition, pp. 904-910, February 15, 2009.
9	NORDMANN, T. et al., "Performance of PV Systems Under Real Conditions," European Workshop on Life Cycle Analysis and Recycling of Solar Modules, The "Waste" Challenge, Brussels, Belgium, March 18-19, 2004.
10	PALMA, L. et al., "A Modular Fuel Cell, Modular DC-DC Converter Concept for High Performance and Enhanced Reliability," 38th IEEE Power Electronics Specialists Conference (PESC'07), pp. 2633-2638, June 17, 2007.
11	QUASCHNING, V. et al., "Cost Effectiveness of Shadow Tolerant Photovoltaic Systems," Euronsun 96, pp. 819-824, September 16, 1996.

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12	ROMAN, Eduardo, et al., "Intelligent PV Module for Grid-Connected PV Systems," IEEE Transactions on Industrial Electronics, Vol. 53, No. 4, pp. 1066-1073, August 2006.
13	URIARTE, S. et al., "Energy Integrated Management System for PV Applications," 20th European Photovoltaic Solar Energy Conference, June 6, 2005.
14	WALKER, G. R. et al., "Cascaded DC-DC Converter Connection of Photovoltaic Modules," 33rd IEEE Power Electronics Specialists Conference (PESC'02), Vol. 1, pp. 24-29, 2002.
15	WALKER, Geoffrey R. et al., "Cascaded DC-DC Converter Connection of Photovoltaic Modules," IEEE Transactions on Power Electronics, Vol. 19, No. 4, pp. 1130-1139, July 2004.

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Examiner Signature	/JEFFREY A GBLENDE/	Date Considered	12/08/2017
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☒ A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Lehua Wang/	Date (YYYY-MM-DD)	2017-07-21
Name/Print	Lehua Wang	Registration Number	48023

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	15392960
	Filing Date	2016-12-28
	First Named Inventor	MORDECHAY AVRUTSKY
	Art Unit	2838
	Examiner Name	GBLENDE, JEFFREY A
	Attorney Docket Number	108144-201503/US (TGY015)

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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	8013472		2011-09-06	Adest, et al.	
	2	8035249		2011-10-11	Shaver, II, et al.	
	3	8427009		2013-04-23	Shaver, II, et al.	
	4	8531055		2013-09-10	Adest, et al.	
	5	8587151		2013-11-19	Adest, et al.	
	6	8669675		2014-03-11	Capp, et al.	
	7	8816535		2014-08-26	Adest, et al.	
	8	8947194		2015-02-03	Sella, et al.	

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Examiner Name	GBLENDE, JEFFREY A
Attorney Docket Number	108144-201503/US (TGY015)

9	9112379	2015-08-18	Sella, et al.
10	9401599	2016-07-26	Har-Shai, et al.
11	9438035	2016-09-06	Capp, et al.
12	9590526	2017-03-07	Adest, et al.

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Signature	/Lehua Wang/	Date (YYYY-MM-DD)	2017-11-02
Name/Print	Lehua Wang	Registration Number	48023

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Electronic Acknowledgement Receipt

EFS ID:	30840140
Application Number:	15392960
International Application Number:	
Confirmation Number:	3397
Title of Invention:	SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE
First Named Inventor/Applicant Name:	Mordechay AVRUTSKY
Customer Number:	64494
Filer:	John Patrick Ward/Junchao Zhang
Filer Authorized By:	John Patrick Ward
Attorney Docket Number:	108144-201503/US (TGY015)
Receipt Date:	02-NOV-2017
Filing Date:	28-DEC-2016
Time Stamp:	15:52:41
Application Type:	Utility under 35 USC 111(a)

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	1	4719553		1988-01-12	Hinckley	
	2	5235266		1993-08-10	Schaffrin	
	3	5268832		1993-12-07	Kandatsu	
	4	5604430		1997-02-18	Decker, et al.	
	5	5903138		1999-05-11	Hwang, et al.	
	6	5923158		1999-07-13	Kurokami, et al.	
	7	6275016		2001-08-14	Ivanov	
	8	6448489		2002-09-10	Kimura, et al.	

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9	6650031		2003-11-18	Goldack	
10	6844739		2005-01-18	Kasai, et al.	
11	6894911		2005-05-17	Telefus, et al.	
12	6984970		2006-01-10	Capel	
13	7061214		2006-06-13	Mayega, et al.	
14	7202653		2007-04-10	Pai	
15	7248946		2007-07-24	Bashaw, et al.	
16	7256566		2007-08-14	Bhavaraju, et al.	
17	7276886		2007-10-02	Kinder, et al.	
18	7518346		2009-04-14	Prexl, et al.	
19	7560915		2009-07-14	Ito, et al.	

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	15392960
Filing Date	2016-12-28
First Named Inventor	MORDECHAY AVRUTSKY
Art Unit	2838
Examiner Name	LEWIS, MONICA
Attorney Docket Number	108144-201503/US (TGY015)

20	7595616	2009-09-29	Prexl, et al.
21	7605498	2009-10-20	Ledenev, et al.
22	7719140	2010-05-18	Ledenev, et al.
23	7821281	2010-10-26	Chen
24	8004866	2011-08-23	Bucella, et al.
25	9143036	2015-09-22	Avrutsky
26	9584021	2017-02-28	Avrutsky

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U.S.PATENT APPLICATION PUBLICATIONS

Remove

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20050017701		2005-01-27	Hsu, Chih-Yuan	
	2	20050040800		2005-02-24	Sutardja, Sehat	

**INFORMATION DISCLOSURE
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Filing Date		2016-12-28
First Named Inventor	MORDECHAY AVRUTSKY	
Art Unit	2838	
Examiner Name	LEWIS, MONICA	
Attorney Docket Number	108144-201503/US (TGY015)	

3	20050057214	2005-03-17	Matan, Stefan
4	20050057215	2005-03-17	Matan, Stefan
5	20060001406	2006-01-05	Matan; Stefan
6	20060174939	2006-08-10	Matan; Stefan
7	20060176031	2006-08-10	Dustin D. Forman, et al.
8	20060185727	2006-08-24	Matan; Stefan
9	20070001653	2007-01-04	Xu; Peng
10	20070273351	2007-11-29	Matan; Stefan
11	20070278861	2007-12-06	Chih-Hong Lou, et al.
12	20080100233	2008-05-01	Negoi; Andy C.
13	20080121272	2008-05-29	David A. Besser, et al.

**INFORMATION DISCLOSURE
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First Named Inventor	MORDECHAY AVRUTSKY	
Art Unit	2838	
Examiner Name	LEWIS, MONICA	
Attorney Docket Number	108144-201503/US (TGY015)	

14	20080122449		2008-05-29	David A. Besser, et al.	
15	20080122518		2008-05-29	David A. Besser, et al.	
16	20080179949		2008-07-31	David A. Besser, et al.	
17	20080191560		2008-08-14	David A. Besser, et al.	
18	20080191675		2008-08-14	David A. Besser, et al.	
19	20080303503		2008-12-11	Wolfs; Peter	
20	20100013452		2010-01-21	BENJAMIM TANG, et al.	
21	20100117614		2010-05-13	Justin P. Bandholz, et al.	
22	20110031946		2011-02-10	Patrick K. Egan, et al.	
23	20110050190		2011-03-03	AVRUTSKY; Mordechay	
24	20110148388		2011-06-23	Alfio Zanchi, et al.	

**INFORMATION DISCLOSURE
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Application Number	15392960
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Art Unit	2838
Examiner Name	LEWIS, MONICA
Attorney Docket Number	108144-201503/US (TGY015)

25	20150349644	2015-12-03	AVRUTSKY; Mordechay
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FOREIGN PATENT DOCUMENTS

Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ²	Kind Code ⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T ⁵
	1	2005262278	AU		2005-07-12	Central Queensland University et al.		
	2	19961705	DE		2001-07-05	SMA Regelsysteme GmbH		
	3	4232356	DE		1994-03-31	Inst Solare Energieversorgungs		
	4	1388774	EP		2004-02-11	CIT Alcatel		
	5	2249147	ES		2006-03-16	Fundacion Robotiker		
	6	2003012569	WO		2003-02-13	Stichting Energie et al.		

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NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
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**INFORMATION DISCLOSURE
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Application Number	15392960
Filing Date	2016-12-28
First Named Inventor	MORDECHAY AVRUTSKY
Art Unit	2838
Examiner Name	LEWIS, MONICA
Attorney Docket Number	108144-201503/US (TGY015)

1	ALONSO, R. et al., "A New Distributed Converter Interface for PV Panels," 20th European Photovoltaic Solar Energy Conference, Barcelona, Spain, pp. 2288-2291, June 6-10, 2005.
2	ALONSO, R. et al., "Experimental Results of Intelligent PV Module for Grid-Connected PV Systems," 21st European Photovoltaic Solar Energy Conference, Dresden, Germany, pp. 2297-2300, September 4-8, 2006.
3	BASSO, Tim, "IEEE Standard for Interconnecting Distributed Resources With the Electric Power System," IEEE PES Meeting, June 9, 2004.
4	Boostbuck.com, "The Four Boostbuck Topologies," located at http://www.boostbuck.com/TheFourTopologies.html , 2003.
5	ENSLIN, Johan H.R., et al., "Integrated Photovoltaic Maximum Power Point Tracking Converter," IEEE Transactions on Industrial Electronics, Vol. 44, No. 6, pp. 769-773, December 1997.
6	GAUTAM, Nalin K. et al., "An Efficient Algorithm to Simulate the Electrical Performance of Solar Photovoltaic Arrays," Energy, Vol. 27, No. 4, pp. 347-361, 2002.
7	International Patent Application No. PCT/US2010/046272, International Search Report and Written Opinion, March 31, 2011.
8	LINARES, Leonor et al., "Improved Energy Capture in Series String Photovoltaics via Smart Distributed Power Electronics," 24th Annual IEEE Applied Power Electronics Conference and Exposition, pp. 904-910, February 15, 2009.
9	NORDMANN, T. et al., "Performance of PV Systems Under Real Conditions," European Workshop on Life Cycle Analysis and Recycling of Solar Modules, The "Waste" Challenge, Brussels, Belgium, March 18-19, 2004.
10	PALMA, L. et al., "A Modular Fuel Cell, Modular DC-DC Converter Concept for High Performance and Enhanced Reliability," 38th IEEE Power Electronics Specialists Conference (PESC'07), pp. 2633-2638, June 17, 2007.
11	QUASCHNING, V. et al., "Cost Effectiveness of Shadow Tolerant Photovoltaic Systems," Euronsun 96, pp. 819-824, September 16, 1996.

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Examiner Name	LEWIS, MONICA
Attorney Docket Number	108144-201503/US (TGY015)

12	ROMAN, Eduardo, et al., "Intelligent PV Module for Grid-Connected PV Systems," IEEE Transactions on Industrial Electronics, Vol. 53, No. 4, pp. 1066-1073, August 2006.
13	URIARTE, S. et al., "Energy Integrated Management System for PV Applications," 20th European Photovoltaic Solar Energy Conference, June 6, 2005.
14	WALKER, G. R. et al., "Cascaded DC-DC Converter Connection of Photovoltaic Modules," 33rd IEEE Power Electronics Specialists Conference (PESC'02), Vol. 1, pp. 24-29, 2002.
15	WALKER, Geoffrey R. et al., "Cascaded DC-DC Converter Connection of Photovoltaic Modules," IEEE Transactions on Power Electronics, Vol. 19, No. 4, pp. 1130-1139, July 2004.

If you wish to add additional non-patent literature document citation information please click the Add button

EXAMINER SIGNATURE

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. ² Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). ³ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁴ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁵ Applicant is to place a check mark here if English language translation is attached.

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Application Number	15392960
Filing Date	2016-12-28
First Named Inventor	MORDECHAY AVRUTSKY
Art Unit	2838
Examiner Name	LEWIS, MONICA
Attorney Docket Number	108144-201503/US (TGY015)

CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

OR

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Lehua Wang/	Date (YYYY-MM-DD)	2017-07-21
Name/Print	Lehua Wang	Registration Number	48023

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether the Freedom of Information Act requires disclosure of these records.
2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.
4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspections or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor:	Mordechay Avrutsky	Examiner:	Monica Lewis
Serial No.:	15/392,960	Group Art Unit:	2838
Filed:	December 28, 2016	Confirmation:	3397
Title:	Systems and Methods for Enhanced Efficiency Auxiliary Power Supply Module		

NOTICE OF RELATED APPLICATIONS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir/Madam:

Applicant is hereby providing notice that the subject patent application is related to the following U.S. patent applications. Applicant respectfully requests the Examiner to consider the related applications and the prosecution histories of these related applications, such as the current claims, Office Actions and Notices of Allowance, etc.

1.	Title:	Systems and Methods for Enhanced Efficiency Auxiliary Power Supply Module	
	Serial No.:	12/787,205	Filed on: May 25, 2010
	Inventor(s):	Mordechay AVRUTSKY	Docket No.: 108144-201501/US (TGY015)
	Examiner:	Jeffrey Gblende	Art Unit: 2838
	Patent No.:	9,143,036	Issue Date: September 22, 2015
2.	Title:	Systems and Methods for Enhanced Efficiency Auxiliary Power Supply Module	
	Serial No.:	14/822,227	Filed on: August 10, 2015
	Inventor(s):	Mordechay AVRUTSKY	Docket No.: 108144-201502/US (TGY015)
	Examiner:	Jeffrey Gblende	Art Unit: 2838
	Status:	Notice of Allowance Mailed --	Status Date: November 16, 2016

Application Received in Office of
Publications

Respectfully submitted,

Date: July 21, 2017

/Lehua Wang/
Lehua Wang
Reg. No. 48,023

GREENBERG TRAURIG, LLP
CUSTOMER NUMBER **64494**
Phone: (650) 328-8500
Fax: (650) 328-8508

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor:	Mordechay Avrutsky	Examiner:	Monica Lewis
Serial No.:	15/392,960	Group Art Unit:	2838
Filed:	December 28, 2016	Confirmation:	3397
Title:	Systems and Methods for Enhanced Efficiency Auxiliary Power Supply Module		

TRANSMITTAL FOR INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir/Madam:

According to 37 C.F.R. §1.98(d), copies of the Foreign Patent Documents and Non-Patent Literature Documents listed in the Information Disclosure Statement Form (SB08) filed herewith are not required for the Information Disclosure Statement, because these documents have been submitted in U.S. Pat. App. Ser. No. 14/822,227, which is relied upon in the present application for an earlier effective filing date under 35 U.S.C. §120.

Consideration of the references listed in the Information Disclosure Statement Forms (SB08) filed herewith is respectfully requested.

Respectfully submitted,

Date: July 21, 2017

/Lehua Wang/
Lehua Wang
Reg. No. 48,023

GREENBERG TRAURIG, LLP
CUSTOMER NUMBER **64494**
Phone: (650) 328-8500
Fax: (650) 328-8508

Electronic Acknowledgement Receipt

EFS ID:	29857747
Application Number:	15392960
International Application Number:	
Confirmation Number:	3397
Title of Invention:	SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE
First Named Inventor/Applicant Name:	Mordechay AVRUTSKY
Customer Number:	64494
Filer:	John Patrick Ward/Junchao Zhang
Filer Authorized By:	John Patrick Ward
Attorney Docket Number:	108144-201503/US (TGY015)
Receipt Date:	21-JUL-2017
Filing Date:	28-DEC-2016
Time Stamp:	15:38:08
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Form (SB08)	108144-201503_IDS.PDF	1009452	no	10
			51f5af07daf19d15d5dca11e997be1d755718b0a		

Warnings:

Information:					
2	Information Disclosure Statement (IDS) Form (SB08)	108144-201503_RelatedApps. pdf	76632 267498c59e28c076a79ee54f0c38019bfc831a98	no	2
Warnings:					
Information:					
This is not an USPTO supplied IDS fillable form					
3	Transmittal Letter	108144-201503_IDSTransmittal .pdf	75242 f4c7a6d0373f064015e2d3f8685435c2ca91468b	no	1
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This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503. <u>New Applications Under 35 U.S.C. 111</u> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application. <u>National Stage of an International Application under 35 U.S.C. 371</u> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course. <u>New International Application Filed with the USPTO as a Receiving Office</u> If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.					



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APPLICATION NUMBER	FILING OR 371(C) DATE	FIRST NAMED APPLICANT	ATTY. DOCKET NO./TITLE
15/392,960	12/28/2016	Mordechay AVRUTSKY	108144-201503/US (TGY015)

CONFIRMATION NO. 3397

POA ACCEPTANCE LETTER



64494
GREENBERG TRAURIG, LLP (SV)
c/o: Greenberg Traurig LLP - Chicago Office
77 West Wacker Drive, Suite 3100
Intellectual Property Department
Chicago, IL 60601

Date Mailed: 06/14/2017

NOTICE OF ACCEPTANCE OF POWER OF ATTORNEY

This is in response to the Power of Attorney filed 06/08/2017.

The Power of Attorney in this application is accepted. Correspondence in this application will be mailed to the above address as provided by 37 CFR 1.33.

Questions about the contents of this notice and the requirements it sets forth should be directed to the Office of Data Management, Application Assistance Unit, at (571) 272-4000 or (571) 272-4200 or 1-888-786-0101.

/byemane/

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POWER OF ATTORNEY TO PROSECUTE APPLICATIONS BEFORE THE USPTO

I hereby revoke all previous powers of attorney given in the application identified in the attached statement under 37 CFR 3.73(c).

I hereby appoint:



Practitioners associated with Customer Number:

64494

OR

Practitioner(s) named below (if more than ten patent practitioners are to be named, then a customer number must be used).

Name	Registration Number

Name	Registration Number

As attorney(s) or agent(s) to represent the undersigned before the United States Patent and Trademark Office (USPTO) in connection with any and all patent applications assigned only to the undersigned according to the USPTO assignment records or assignments documents attached to this form in accordance with 37 CFR 3.73(c).

Please change the correspondence address for the application identified in the attached statement under 37 CFR 3.73(c) to:



The address associated with Customer Number:

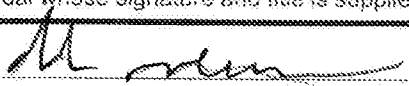
64494

OR

☐	Firm or Individual Name			
	Address			
	City	State	Zip	
	Country			
	Telephone	Email		

Assignee Name and Address: Tigo Energy, Inc.
420 Blossom Hill Road
Los Gatos CA 95032**A copy of this form, together with a statement under 37 CFR 3.73(c) (Form PTO/AIA/96 or equivalent) is required to be Filed in each application in which this form is used. The statement under 37 CFR 3.73(c) may be completed by one of The practitioners appointed in this form, and must identify the application in which this Power of Attorney is to be filed.****SIGNATURE of Assignee of Record**

The individual whose signature and title is supplied below is authorized to act on behalf of the assignee

Signature		Date	May 20, 2016
Name	Danit Neeman	Telephone	408-402-0802
Title	Chief Financial Officer		

This collection of information is required by 37 CFR 1.31, 1.32 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
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4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (*i.e.*, GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspection or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

Electronic Acknowledgement Receipt

EFS ID:	29436667
Application Number:	15392960
International Application Number:	
Confirmation Number:	3397
Title of Invention:	SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE
First Named Inventor/Applicant Name:	Mordechay AVRUTSKY
Customer Number:	64494
Filer:	John Patrick Ward/YIJIE SHEN
Filer Authorized By:	John Patrick Ward
Attorney Docket Number:	108144-201503/US (TGY015)
Receipt Date:	08-JUN-2017
Filing Date:	28-DEC-2016
Time Stamp:	15:04:25
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Assignee showing of ownership per 37 CFR 3.73	108144-201503_OwnershipStatement.PDF	124380 b2bb0e76a8fea09af14eebc720c5a02d42fe4a1f	no	3

Warnings:

Information:					
2	Power of Attorney	108144-201503_POA.PDF	1520611	no	2
			16013dac75fdac3034828c08fc49d9a29ccc15dc		
Warnings:					
Information:					
			Total Files Size (in bytes):		
			1644991		
This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503. <u>New Applications Under 35 U.S.C. 111</u> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application. <u>National Stage of an International Application under 35 U.S.C. 371</u> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course. <u>New International Application Filed with the USPTO as a Receiving Office</u> If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.					

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STATEMENT UNDER 37 CFR 3.73(c)Applicant/Patent Owner: Tigo Energy, Inc.Application No./Patent No.: 15392960Filed/Issue Date: 12/28/2016Titled: SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULETigo Energy, Inc., a corporation

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that, for the patent application/patent identified above, it is (choose **one** of options 1, 2, 3 or 4 below):

1. ☒ The assignee of the entire right, title, and interest.
2. ☐ An assignee of less than the entire right, title, and interest (check applicable box):
- ☐ The extent (by percentage) of its ownership interest is _____%. Additional Statement(s) by the owners holding the balance of the interest must be submitted to account for 100% of the ownership interest.
- ☐ There are unspecified percentages of ownership. The other parties, including inventors, who together own the entire right, title and interest are:

Additional Statement(s) by the owner(s) holding the balance of the interest must be submitted to account for the entire right, title, and interest.

3. ☐ The assignee of an undivided interest in the entirety (a complete assignment from one of the joint inventors was made). The other parties, including inventors, who together own the entire right, title, and interest are:

Additional Statement(s) by the owner(s) holding the balance of the interest must be submitted to account for the entire right, title, and interest.

4. ☐ The recipient, via a court proceeding or the like (e.g., bankruptcy, probate), of an undivided interest in the entirety (a complete transfer of ownership interest was made). The certified document(s) showing the transfer is attached.

The interest identified in option 1, 2 or 3 above (not option 4) is evidenced by either (choose **one** of options A or B below):

- A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 041209, Frame 0996, or for which a copy thereof is attached.
- B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:
1. From: _____ To: _____
- The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.
2. From: _____ To: _____
- The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

[Page 1 of 2]

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

STATEMENT UNDER 37 CFR 3.73(c)

3. From: _____ To: _____

The document was recorded in the United States Patent and Trademark Office at
 Reel _____, Frame _____, or for which a copy thereof is attached.

4. From: _____ To: _____

The document was recorded in the United States Patent and Trademark Office at
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The document was recorded in the United States Patent and Trademark Office at
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6. From: _____ To: _____

The document was recorded in the United States Patent and Trademark Office at
 Reel _____, Frame _____, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet(s).

☒ As required by 37 CFR 3.73(c)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

/John P. Ward/

Signature

John P. Ward

Printed or Typed Name

June 8, 2017

Date

40216

Title or Registration Number

[Page 2 of 2]

Privacy Act Statement

The **Privacy Act of 1974 (P.L. 93-579)** requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether disclosure of these records is required by the Freedom of Information Act.
2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.
4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (*i.e.*, GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
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United States Patent and Trademark Office
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www.uspto.gov

APPLICATION NUMBER	FILING OR 371(C) DATE	FIRST NAMED APPLICANT	ATTY. DOCKET NO./TITLE
15/392,960	12/28/2016	Mordechay AVRUTSKY	108144-201503/US (TGY015)

CONFIRMATION NO. 3397

PUBLICATION NOTICE



OC000000090785200

64494
GREENBERG TRAURIG, LLP (SV)
c/o: Greenberg Traurig LLP - Chicago Office
77 West Wacker Drive, Suite 3100
Intellectual Property Department
Chicago, IL 60601

Title:SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE

Publication No.US-2017-0110966-A1

Publication Date:04/20/2017

NOTICE OF PUBLICATION OF APPLICATION

The above-identified application will be electronically published as a patent application publication pursuant to 37 CFR 1.211, et seq. The patent application publication number and publication date are set forth above.

The publication may be accessed through the USPTO's publically available Searchable Databases via the Internet at www.uspto.gov. The direct link to access the publication is currently <http://www.uspto.gov/patft/>.

The publication process established by the Office does not provide for mailing a copy of the publication to applicant. A copy of the publication may be obtained from the Office upon payment of the appropriate fee set forth in 37 CFR 1.19(a)(1). Orders for copies of patent application publications are handled by the USPTO's Office of Public Records. The Office of Public Records can be reached by telephone at (571) 272-3150 or (800) 972-6382, by facsimile at (571) 273-3250, by mail addressed to the United States Patent and Trademark Office, Office of Public Records, Alexandria, VA 22313-1450 or via the Internet.

In addition, information on the status of the application, including the mailing date of Office actions and the dates of receipt of correspondence filed in the Office, may also be accessed via the Internet through the Patent Electronic Business Center at www.uspto.gov using the public side of the Patent Application Information and Retrieval (PAIR) system. The direct link to access this status information is currently <http://pair.uspto.gov/>. Prior to publication, such status information is confidential and may only be obtained by applicant using the private side of PAIR.

Further assistance in electronically accessing the publication, or about PAIR, is available by calling the Patent Electronic Business Center at 1-866-217-9197.

Office of Data Management, Application Assistance Unit (571) 272-4000, or (571) 272-4200, or 1-888-786-0101

PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875						Application or Docket Number 15/392,960			
APPLICATION AS FILED - PART I									
(Column 1)		(Column 2)		SMALL ENTITY		OR OTHER THAN SMALL ENTITY			
FOR	NUMBER FILED	NUMBER EXTRA		RATE(\$)	FEE(\$)		RATE(\$)		
BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A		N/A	70		N/A		
SEARCH FEE (37 CFR 1.16(k), (l), or (m))	N/A	N/A		N/A	300		N/A		
EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A		N/A	360		N/A		
TOTAL CLAIMS (37 CFR 1.16(j))	20	minus 20 =	*	x 40 =	0.00	OR			
INDEPENDENT CLAIMS (37 CFR 1.16(h))	3	minus 3 =	*	x 210 =	0.00				
APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is \$310 (\$155 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).				0.00				
MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))					0.00				
* If the difference in column 1 is less than zero, enter "0" in column 2.				TOTAL	730		TOTAL		
APPLICATION AS AMENDED - PART II									
(Column 1)		(Column 2)		(Column 3)		SMALL ENTITY		OR OTHER THAN SMALL ENTITY	
AMENDMENT A		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE(\$)	ADDITIONAL FEE(\$)		RATE(\$)
	Total (37 CFR 1.16(i))	*	Minus	**	=	x =		OR	x =
	Independent (37 CFR 1.16(h))	*	Minus	***	=	x =		OR	x =
	Application Size Fee (37 CFR 1.16(s))							OR	
	FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR	
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE
AMENDMENT B		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE(\$)	ADDITIONAL FEE(\$)		RATE(\$)
	Total (37 CFR 1.16(i))	*	Minus	**	=	x =		OR	x =
	Independent (37 CFR 1.16(h))	*	Minus	***	=	x =		OR	x =
	Application Size Fee (37 CFR 1.16(s))							OR	
	FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR	
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE
* If the entry in column 1 is less than the entry in column 2, write "0" in column 3. ** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20". *** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3". The "Highest Number Previously Paid For" (Total or Independent) is the highest found in the appropriate box in column 1.									



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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	TOT CLAIMS	IND CLAIMS
15/392,960	12/28/2016	2631	730	108144-201503/US (TGY015)	20	3

CONFIRMATION NO. 3397

FILING RECEIPT



0000000088388194

64494
GREENBERG TRAURIG, LLP (SV)
c/o: Greenberg Traurig LLP - Chicago Office
77 West Wacker Drive, Suite 3100
Intellectual Property Department
Chicago, IL 60601

Date Mailed: 01/11/2017

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please submit a written request for a Filing Receipt Correction. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections**

Inventor(s)

Mordechay AVRUTSKY, Alfei Menashe, ISRAEL;

Applicant(s)

Tigo Energy, Inc., Los Gatos, CA;

Power of Attorney: None

Domestic Priority data as claimed by applicant

This application is a CON of 14/822,227 08/10/2015
which is a CON of 12/787,205 05/25/2010 PAT 9143036
which claims benefit of 61/275,734 09/02/2009
and claims benefit of 61/336,743 01/26/2010

Foreign Applications for which priority is claimed (You may be eligible to benefit from the **Patent Prosecution Highway** program at the USPTO. Please see <http://www.uspto.gov> for more information.) - None.

Foreign application information must be provided in an Application Data Sheet in order to constitute a claim to foreign priority. See 37 CFR 1.55 and 1.76.

Permission to Access Application via Priority Document Exchange: Yes

Permission to Access Search Results: Yes

Applicant may provide or rescind an authorization for access using Form PTO/SB/39 or Form PTO/SB/69 as appropriate.

If Required, Foreign Filing License Granted: 01/09/2017

page 1 of 3

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 15/392,960**

Projected Publication Date: 04/20/2017

Non-Publication Request: No

Early Publication Request: No

**** SMALL ENTITY ****

Title

SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY
MODULE

Preliminary Class

375

Statement under 37 CFR 1.55 or 1.78 for AIA (First Inventor to File) Transition Applications: No

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Since the rights granted by a U.S. patent extend only throughout the territory of the United States and have no effect in a foreign country, an inventor who wishes patent protection in another country must apply for a patent in a specific country or in regional patent offices. Applicants may wish to consider the filing of an international application under the Patent Cooperation Treaty (PCT). An international (PCT) application generally has the same effect as a regular national patent application in each PCT-member country. The PCT process **simplifies** the filing of patent applications on the same invention in member countries, but **does not result** in a grant of "an international patent" and does not eliminate the need of applicants to file additional documents and fees in countries where patent protection is desired.

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Title 37, Code of Federal Regulations, 5.11 & 5.15

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UNITED STATES PATENT APPLICATION
FOR
**SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY
POWER SUPPLY MODULE**

INVENTOR:

MORDECHAY AVRUTSKY

PREPARED BY:

GREENBERG TRAURIG

PHONE: (650) 328-8500

FAX: (650) 328-8508

**SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER
SUPPLY MODULE**

RELATED APPLICATIONS

[0001] The present application is a continuation application of U.S. Pat. App. Ser. No. 14/822,227, filed Aug. 10, 2015 and entitled “Systems and Methods for Enhanced Efficiency Auxiliary Power Supply Module”, which is a continuation application of U.S. Pat. App. Ser. No. 12/787,205, filed May 25, 2010, entitled “Systems and Methods for Enhanced Efficiency Auxiliary Power Supply Module”, and issued as U.S. Pat. No. 9,143,036 on Sep. 22, 2015, which claims the benefit of filing dates of Prov. U.S. App. Ser. No. 61/275,734, filed Sep. 2, 2009 and entitled “System and Method for Enhanced Efficiency Auxiliary Power Supply Module”, and Prov. U.S. App. Ser. No. 61/336,743, filed Jan. 26, 2010 and also entitled “System and Method for Enhanced Efficiency Auxiliary Power Supply Module,” the entire disclosures of which applications are hereby incorporated herein by reference.

FIELD OF THE TECHNOLOGY

[0002] At least some embodiments of this disclosure relate to photovoltaic systems in general, and more particularly but not limited to, enhancing the efficiency for auxiliary power supply modules used in photovoltaic systems.

BACKGROUND

[0003] Typically, power supplies or power supply modules used for photovoltaic systems may provide, for example, 3.3V to be used as a main supply voltage, in most applications when the photovoltaic system needs to maintain low voltage for energy-saving purposes. However, when the power supplies are driving the metal-oxide-semiconductor field-effect transistors (MOSFET) or similar devices or switches contained within the photovoltaic systems, the power supplies or power supply modules of photovoltaic systems may have to provide higher voltage levels such as, for example, 10V to 12V. Keeping the main supply of the control circuit and the overall

power supply module at, for example, 3.3.V may be preferable in order to maintain ideal energy-savings for the operation of photovoltaic systems.

SUMMARY OF THE DESCRIPTION

[0004] Systems and methods in accordance with the present disclosure are described herein. Some embodiments are summarized in this section.

[0005] In one of many embodiments of the disclosure, provided is a power supply for use in a solar electric production system. The power supply includes: a first stage having an input connected to a voltage from a photovoltaic panel and an output providing a first voltage different from the voltage from the photovoltaic panel; and a second stage connected to the output of the first stage, the second stage supplying power at a second voltage to a micro-controller, where the output of the first stage is turned on and stable for a period of time before the second stage is turned on to supply the power at the second voltage to the micro-controller.

[0006] In another embodiment, provided is a power supply for use in a solar electric production system. The power supply includes: a first stage having an input and an output, the input of the first stage to receive power from a photovoltaic panel, the output of the first stage to provide a first voltage different from the voltage received at the input of the first stage; a switchable load; a second stage having an input connected to the output of the first stage, the second stage to supply power at a second voltage to an micro-controller; and a control circuit to provide a signal to the switchable load to switch off the switchable load prior to providing a signal to the second stage to enable output from the second stage to the micro-controller.

[0007] Other embodiments and features of the present disclosure will be apparent from the accompanying drawings and from the detailed description which follows.

BRIEF DESCRIPTION OF THE DRAWINGS

[0008] The embodiments are illustrated by way of example and not limitation in the figures of the accompanying drawings in which like references indicate similar elements.

[0009] **Figure 1** shows an example gate charge vs. gate-to-source voltage diagram

according to an embodiment of the present disclosure.

[0010] **Figure 2** shows a diagram illustrating R_{DSon} behavior as a function of V_{GS} (voltage between gate and source of a FET) according to an embodiment of the present disclosure.

[0011] **Figure 3** shows a schematic description of an auxiliary circuit according to an embodiment of the present disclosure.

[0012] **Figure 4** shows a schematic description of a circuit practicing a system and method according to an embodiment of the present disclosure.

[0013] **Figure 5** shows a schematic description of an example startup behavior of wave forms, according to an embodiment of the present disclosure.

[0014] **Figure 6** shows a power supply according to one embodiment.

DETAILED DESCRIPTION

[0015] The following description and drawings are illustrative and are not to be construed as limiting. Numerous specific details are described to provide a thorough understanding. However, in certain instances, well known or conventional details are not described in order to avoid obscuring the description. References to one or an embodiment in the present disclosure are not necessarily references to the same embodiment; and, such references mean at least one.

[0016] Provided is a system and method for an auxiliary power supply module used in a photovoltaic system to increase the efficiency of the photovoltaic system and reduce maintenance responsibilities of the system, or housekeeping requirements, wherein housekeeping roughly means the energy, power or voltage requirements needed to operate controls and switches of a photovoltaic system that converts sunlight into electricity, as well as to manage and control directly associated tasks, including but not limited to communication operations and so on. This increase in efficiency and power savings may be achieved by designing a photovoltaic system where the efficiency curve is as flat as possible. For example, in order to achieve such efficiency objectives, an internal auxiliary power supply having reduced power consumption may be used to drive the MOSFETs of a photovoltaic system at the lower power end of the efficiency curve in order to ultimately obtain the most efficient power savings possible.

[0017] One application of the present disclosure is to power control circuits for a solar panel using the power generated by the solar panel. For example, a power supply module of one embodiment is used to power the link module (200) using the electricity generated by the photovoltaic panel (101n) described in U.S. Pat. App. Ser. No. 11/875,799, published now as U.S. Pat. App. Pub. No. 2008/0097655. For example, a power supply module of one embodiment is used to power the local management unit (LMU) (101) using the electricity generated by the solar module (102) described in U.S. Pat. App. Ser. No. 12/411,317, now issued as U.S. Pat. No. 7,602,080. In one embodiment, a sun can be shown along with any of the above-referenced **Figures** – illustrate that the sun may be the source of photovoltaic energy absorbed by a solar panel, for instance. The contents of the above discussed patent and publication are incorporated herein by reference.

[0018] **Figure 1** shows an example gate charge vs. gate-to-source voltage diagram according to an embodiment of the present disclosure. Gate charge (Q_G) vs. gate-to-source voltage (V_{GS}) diagram (100) comprises at least one drain-to-source voltage (V_{DS}) curve (102), a gate-to-source voltage (V_{GS}) axis (104), having a unit of measurements of Volts, and a total gate charge (Q_G) axis (106), having a unit of measurement of Coulombs, or capacitive charge, or in the specific case of **Figure 1**, nano-Coulombs (nC).

[0019] **Figure 1** analyzes the gate, drain and source of a typical MOSFET transistor that may be used in a photovoltaic system, for example. According to an embodiment of the present disclosure, driving the gate of the MOSFET relative to the source with a lower voltage value (low V_{GS} values on the gate-to-source voltage axis (104)) may yield energy savings for the associated voltage drive at, for example, saving roughly twice the amount of energy compared to using high-voltage values). Note also that the slope (change in gate-to-source voltage over change in total gate charge) is slightly higher for lower values of V_{GS} as opposed to higher values of V_{GS} , which may mean greater power efficiency overall. Both the lowered voltage of the gate-to-source voltage V_{GS} and the lowered internal gate capacity (which in turn reduces the charge or discharge current across the gate) result in a roughly quadratic savings of power. This power savings allows a photovoltaic system to achieve great reduction in housekeeping

energy supply, by using an “adaptive” gate drive that adapts power consumption based on the amount of gate driving voltage provided. Therefore, as can be seen in **Figure 1**, the reduction in gate charge is linear in relation to the gate-to-source voltage V_{GS} .

[0020] MOSFETs have a threshold input voltage, above which the transistor is substantially in full conduction mode or in saturation mode. For safe operation and full conduction, the minimum gate voltage should be at least 1V above the minimum threshold voltage value. Even if full saturation is not achieved, particularly at the low end of the power range, a loss in switching efficiency may be small, whereas at the same low end of the power range, housekeeping energy may suddenly become an issue. Adjusting the gate drive voltage is required to reduce the auxiliary supply current at low load conditions, whereas the effective resistance between the drain and the source of a MOSFET (R_{DS}) has little to no influence on switching efficiency. For a MOSFET above its turn-on threshold voltage, the total gate charge value versus normal gate charge value is a straight line, as is well known in the art, meaning that the behavior per each load current exhibits constant capacitance. Therefore, the gate drive current may behave according to $\frac{1}{2}CV^2f$, where C = the equivalent capacitance, V = the gate voltage, and f = operating frequency. Thus it is clear that reducing the gate voltage from 11V to 7V may reduce the drive current by more than two orders of magnitude. At the same time that the R_{DSon} (the resistance between the drain and the source of a MOSFET switch (or other FET switch) when fully on) is increased by less than 1.5 times its 12V value. At light load conditions (less than 30 percent of a standard-rated load) the change in R_{DSon} does not affect the overall efficiency of the overall MOSFET transistor or of the photovoltaic system.

[0021] **Figure 2** shows a diagram illustrating R_{DSon} behavior as a function of V_{GS} (voltage between gate and source of a FET) according to an embodiment of the present disclosure. Drain-to-source voltage (V_{DS}) to drain-to-source Current (I_D) diagram (200) comprises at least one gate-to-source voltage (V_{GS}) curve 202, a drain-to-source Current (I_D) axis 204, and a drain-to-source voltage (V_{DS}) axis 206. As can be seen from **Figure 2**, a gate-to-source voltage value (V_{GS}) between 15V to 6V does not affect the resistance significantly up to a 1V drop which is the range of R_{DSon} (the resistance between the drain and the source of a MOSFET when fully on) for the switching area.

The resistance is not effected significantly up to a 1V drop because the MOSFET is in saturation mode, and because $V_{DS} = I \cdot R_{DSon}$ and R_{DSon} is a function of both V_{GS} and V_{DS} , so when V_{GS} changes, the R_{DSon} value is affected. As also can be observed from **Figure 2**, the slope (change in ID over change in VDS) increases for higher values of V_{GS} and begin to decrease and plateau for lower values of V_{GS} . This implies that there is an optimal V_{GS} value to drive the gate voltage at in order to still be efficient and not affect the range of R_{DSon} , which is desired in order to avoid any excessive power consumption from the resistance of R_{DSon} .

[0022] Further details about characteristics of MOSFETs in one embodiment (e.g., used as switches for controlling the output of solar panels) may be provided in IRF8101E/S/SLPbF data sheet from International Rectifier, which is hereby incorporated herein by reference.

[0023] **Figure 3** shows a schematic description of an auxiliary circuit according to an embodiment of the present disclosure. Auxiliary circuit (300) is a circuit designed to control the auxiliary power for a photovoltaic system, for example. In one embodiment, the auxiliary circuit (300) receives in the input terminal (333) electricity from the photovoltaic panel (101n) described in U.S. Pat. App. Ser. No. 11/875,799, or from the solar module (102) described in U.S. Pat. App. Ser. No. 12/411,317. In one embodiment, the auxiliary circuit (300) provides in one output terminal (330) a voltage for control logic, such as the link module (200) described in U.S. Pat. App. Ser. No. 11/875,799 or the controller (109) described in U.S. Pat. App. Ser. No. 12/411,317. In one embodiment, the circuit (300) provides in one output terminal (331) a voltage for driving a switch, such as a switch having characteristics shown in **Figs. 1** and **2**, which can be used as a switch in the link module (200) described in U.S. Pat. App. Ser. No. 11/875,799, or a switch (106) and/or a switch (108) described in U.S. Pat. App. Ser. No. 12/411,317.

[0024] In one embodiment, the auxiliary circuit (300) includes step-down converters, such as a buck converter (301) to drive a first voltage output (331) and a buck converter (311) to drive a second voltage output (330). The auxiliary circuit (300) uses capacitor (326) as a backup power supply in order to keep buck converter (311) running longer than buck converter (301). In other words, capacitor (326) is charged by buck

converter (301) and used by buck converter (311) as a power supply such that the output of buck converter (311) lasts longer than the output of buck converter (301). If the output of buck converter (301) is out or unstable for a little while, buck converter (311) can still keep the auxiliary circuit (300) working properly at 3.3V for this short period of power outage or instability. Thus, the control logic powered by the voltage (330) will have enough power to provide appropriate control signals, until the drive stage powered by the voltage (331) loses the power (e.g., to operate the switch for the solar panel). Utilizing capacitor (326) may be beneficial in situations where the solar panel is temporarily out of power, such as, for example, when the solar panel is covered by a falling leaf or a bird flying by or a brief shadow.

[0025] In one embodiment, auxiliary circuit (300) has input voltage (333), buck converter (301), voltage (332), MOSFET (345), drive voltage (31), resistor (319), resistor (313), capacitor (326), buck converter (311), 3.3V logic supply output (330), diode (322), resistors (323, 324, 325), transistors (320, 321), control block (315), and resistors (341, 342, 343, 340). The buck converter (301) has: MOSFET (302), diode (305), inductor (306), resistors (308, 309, 310), capacitor (307), MOSFET (303), and control (304). The buck converter (311) has: MOSFET (312), diode (325), inductor (327), capacitor (328) and control (314). Various known converters can be used to replace the voltage conversion portion of the buck converters (301 and 311), such MOSFET (302), diode (305), inductor (306) and capacitor (307) in the buck converter (301), or MOSFET (312), diode (325), inductor (327) and capacitor (328) in the buck converter (311). Therefore, the details shown in buck converter (303) and buck converter (311) are merely illustrative, and other types of buck converters known in the art may be used. In one embodiment, control block 315 has: control circuit (318), and transistors (317, 316). The control circuits 304, 314 and 318 may be interconnected via one or more control signals or connected to one source, such as a micro-controller, although this embodiment is not explicitly shown in **Figure 3**. In one embodiment, the control circuit (318) controls the operations of the control (304) and control (314).

[0026] In auxiliary circuit (300), drive voltage (331) is controlled by a serial MOSFET (345) and resistor (319). Before enabling the serial MOSFET (345), transistor (321) is to be turned on by meeting at least the following two conditions: (i) 3.3V logic supply

(330), for example, is on and higher than at least, for example, 2.6V. (ii) At this particular voltage level, the threshold formed by Zener diode (322) and resistors (324) and (325), as well as when the V_{BE} (base-emitter voltage) of transistor (321) (if transistor (321) comprises a bipolar transistor instead of a MOSFET) activates transistor (321), thus making the control circuit (318) operative and able to control transistor (316) to change from open collector to conductive or on (if transistor (316) is a bipolar transistor, for example). Resistor (323) further enables the 3.3V logic supply (330), for example, to be used in operation with transistor (320) (transistor (320) shown as being a bipolar transistor in **Figure 3**). According to an embodiment of the present disclosure, a MOSFET is used instead of a bipolar transistor for some or all of the bipolar transistors (e.g. (321), (316), (320)) shown in **Figure 3** and throughout the present disclosure. Voltages and/or resistors can be adjusted for the use of MOSFETs, instead of bipolar transistors, to properly accommodate such substitution. In one embodiment, the enable circuit (320, 321, 322, 323, 324, 325, 345, 319) connects the output of buck converter (301) to its load (e.g., MOSFET switches, see description below for **Figure 4**) only when the 3.3V output is stable and above a 2.6V threshold, for example.

[0027] Transistor (316) and control circuit (318) are both internal to control block (315), the control circuit (318) being naturally defined by default in the “open drain” state when turned on. When the above-described two conditions are met, transistor (321) enables MOSFET (345) to turn on the drive voltage to power the V_i for the drive stage output (331). That is, in other words, drive stage output (331) may not actually receive a drive voltage when the control circuit is not operating. The control circuit may disable the drive voltage during other conditions, such as a low output voltage from buck converter (301) sensed by resistors (340) and (341) and connected to control circuit (318). Buck converter (301) may yield a wide range of voltage for the input voltage (333). Buck converter (301) also is controlled by control (304) and feedback from transistor (303). Resistors (342) and (343) connect the input voltage (333) to transistor (302), and the node in between the middle of resistors (342) and (343) may be directly connected to the control circuit (318) of control block (315). Low logic voltage may shut-down the voltage for the drive stage output (331). A wide operation range may be easily achieved by buck converter (301). The buck converter (301) may also be turned

on when the input voltage (333) is above 6.5V, for example (at turn on, buck converter (301) is not required to regulate anything, and may just have an output voltage that is higher than 5V, for example).

[0028] In one embodiment, transistor (316), transistor (320), transistor (321), diode (322), resistor (323), resistor (324), resistor (325), resistor (319) and transistor (345) are configured to ensure that transistor (345) is turned on only when voltage supply (330) is high enough and when control circuit (318) is working properly (e.g., ready and/or sending the proper signals). Transistor (321) will turn on when (I) voltage supply (330) is at an adequate level and (II) when control circuit (318) is working properly (e.g., ready and/or sending proper signals). First, transistor (321) may turn on only when voltage supply (330) is on and higher than, for example, 2.6V. At this voltage level the threshold formed by diode (322) and resistors (324) and (325), as well as the V_{BE} (base-emitter) voltage of transistor (321) activates transistor (321), if transistor (320) is not turned on. Second, if control circuit (318) is operative or working properly, it will provide a high voltage to the base or gate of transistor (316) to turn on transistor (316). Transistor (316) may be the internal part of control circuit (318) with an open collector or open drain. When transistor (316) is on, transistor (320) is off, which prevents transistor (321) from being turned off if the voltage supply (330) is high enough (e.g., above 2.6V). When transistor (316) is turned off because control circuit (318) is not working properly for lack of power or simply not ready, and when logic supply (330) is high enough, resistor (323) is able to turn transistor (320) on. Once transistor (320) is on, the gate or base of transistor (321) is connected to the ground via transistor (320); and thus transistor (321) turns off, which in turn turns off transistor (345).

[0029] When the above described two conditions (I and II) are both met, transistor (321) is turned on to allow transistor (345) to turn on the drive voltage to drive the “Vi to drive stage” output (331). When transistor (321) turns off, the resistor (319) is not sufficient to turn on transistor (345) and thus the “Vi to drive stage” output (331) is disabled by the transistor (345).

[0030] In one embodiment, when (a) voltage supply (330) becomes high enough, at for example above 2.6V and (b) control (318) is not working properly to turn on transistor (316), the voltage level after dropping past resistor (323) is high enough to

feed into the base or gate of transistor (320), which then is able to turn transistor (320) on or put it in saturation mode. Transistor (320) may be turned on only if transistor (316) is off, which indicates that the control circuit (318) is not ready. In such a case where the transistor (320) is turned on in saturation mode, the base or gate of transistor (320) is connected to ground via the transistor (316) in saturation mode. When transistor (320) is on, a voltage near a ground value is sent to the gate or base of transistor (321) via transistor (320), therefore turning transistor (321) and transistor (345) off (since the control circuit (318) is not be ready, as indicated by the state of transistor (316)). In one embodiment, transistor (316) serves as a switch to toggle transistor (320). If transistor (316) is turned off, as discussed above, transistor (320) may be on if the voltage supply (330) is high enough. Likewise, if transistor (316) is turned on, a ground value is fed into the base or gate of transistor (320), therefore turning transistor (320) off. In one embodiment, transistor (316) is controlled by control circuit (318) to indicate the readiness of the control circuit (318). When ready, control circuit (318) provides a high enough voltage to the base or gate of transistor (316) to turn it on.

[0031] When transistor (321) is on, transistor (345) is on if the output (332) from the converter (301) is high enough. Likewise, if transistor (321) is off, transistor (345) is off even if the output (332) is high enough (for the lack of a sufficient voltage drop across bias resistor (319)). In one embodiment, transistor (345) is always off until the voltage drop across resistor (319) increases to turn transistor (345) on (e.g., when output (332) is high enough and transistor (321) is on for a connection to ground). In one embodiment, the transistor (345) is a PMOS transistor or a PNP bipolar junction type of transistor. In one embodiment, a sufficient voltage drop across resistor (319) from logic supply (332) is provided by transistor (321) turning on and therefore connecting the gate of transistor (345) to ground.

[0032] In one embodiment, the input voltage of buck converter (301) is between the input voltage (333) and the ground located at the bottom of the diode (305) and the capacitor (307). The output voltage of buck converter (301) is between the voltage (332) and the same ground located at the bottom of the diode (305) and the capacitor (307). Changing the duty cycle of MOSFET (302) changes the output voltage of buck

converter (301). Control circuit (304) of buck converter (301) controls the duty cycle of buck converter (301) by controlling the gate of MOSFET (302). The input voltage of buck converter (311) is between the voltage on the node just above the capacitor (326) and the ground located at the bottom of the diode (325) and the capacitor (328). The output voltage of buck converter (311) is between the voltage on (330) and the same ground located at the bottom of diode (325) and the capacitor (328). Like buck converter (303), the control circuit (314) of buck converter (311) controls the duty cycle of buck converter (311) by controlling the gate of MOSFET (312).

[0033] The buck converter (311) is operative from 5V to 20V, for example. Thus, if input loss conditions are experienced, buck converter (311) is the last unit to turn off running from bypass capacitor (326) while buck converter (301) cannot regulate the circuit/system or is in off mode. The buck converter (311) also runs off control circuit (314), the control circuit (314) working in tandem with transistor (314) to effectively control buck converter in a feedback mechanism. Furthermore, in one embodiment, the auxiliary circuit (300) is to identify low power conditions and reduce buck converter (301) voltage from 11V to 7V, for example, by transistor (317), which may in turned be controlled by transistor (303), transistor (303) being able to control the buck converter (301) set point or ideal operating point by means of control circuit (304). When the control circuit identifies low power conditions, it may set transistor (317) on, which results in transistor (303) going off and voltage (332) being set to a low operation drive voltage (typically 7V, for example, which is the lowest power consuming mode voltage of the overall control circuit). MOSFET (303) also effectively serves as a switch to selectively lower the voltage output of buck converter (303) from 11V to 7V, for example. For most low voltage MOSFETS, two levels of drive voltage may be enough, but additional levels are possible.

[0034] One of the problematic aspects of operating a controller in a solar power environment is the unreliability of the power source during the early morning hours as well as during late evening hours, when faint blue light allows the panel to generate a high voltage, but presents barely enough current to properly operate. As a result, circuits often create a "false start" in which the voltages appear to be there, but as soon as the controller becomes active there is a drop or brown out or power outage, leading

to the undesirable occurrence of unstable or even meta-stable modes, which may occur more frequently for small processors.

[0035] **Figure 4** shows a schematic description of a circuit practicing a system and method according to an embodiment of the present disclosure. Circuit (400) is another circuit that may be used as an auxiliary power supply or power supply module for a photovoltaic system, for example. In one embodiment, circuit (400) uses control logic to ensure that solar power is strong enough to enable output from the buck converter (402). Preload resistor P1 (405) is used to emulate the load of buck converter (402). If the output of buck converter (401) can support the load, the preload resistor P1 (405) is disconnected and the output of the buck converter (402) is connected to its load. Circuit (400) therefore can be used to address the false start problem that occurs when the solar panel is able to provide enough output voltage when it is not connected to the output, but not enough to sustain even a small current provided by the buck converter (402) to its load, such as a control logic, a control circuit, a micro-controller, etc.

[0036] In one embodiment, when the intermediate housekeeping voltage V_{buck1} (i.e., the voltage to operate an auxiliary microcontroller unit or MCU) is turned on by buck converter (401), V_{buck1} is initially used to power a preload resistor P1 (405), by turning on the switch (404), typically a FET or bipolar transistor controlled by one of the outputs of delay circuit (403) (also supplied by V_{buck1} but not shown here for clarity). The other output of delay circuit (403) suppresses at the same time buck converter (402). After the housekeeping voltage V_{buck1} is above a predefined threshold for the full delay period of delay circuit (403), the switch 404, and thus the preload resistor P1, is turned off to provide power for the load of the buck converter (402) and the 3.3V logic is permitted to turn on and output from buck converter (402) in accordance with the enable signal from the delay (403).

[0037] Here, in **Figure 4**, V_{buck1} is shown having an ideal range of 10-11V, for example, but this range has been merely provided for exemplary purposes and should not be taken to limit the disclosure to any particular range. This approach provides a smooth turn-on of the logic voltage. Typically, the load of resistor P1 (405) should exceed the load of buck converter (402) (buck converter's load being the MCU which is not shown), so that activating buck converter (402) does not crash the housekeeping

voltage V_{buck1} . Otherwise, the system of the circuit (400) might experience turn-on attempts which could not be completed, and as a result, the MCU could get stuck due to meta-stability effects of the MCU in some cases and some of the time.

[0038] In one embodiment, the loading of this approach is slightly unpredictable; and positive hysteresis is introduced in order to prevent oscillations. For instance, amplifier (406) may be used as a VHV (very high voltage) monitor in order to perform positive hysteresis to ultimately avoid oscillations. The reason this hysteresis modification is used is that at very low light conditions (where the sunlight is very dim for instance), solar panels can provide voltages that are high enough for turning on, but cannot ultimately provide sufficient power for the circuit that is to be powered by the buck converter (402). Therefore, identifying a simple threshold for voltage V_{buck1} does not stop unsuccessful turn-on attempts, if no hysteresis is employed in delay circuit (403) (meaning the delay starts only if V_{buck1} is, for example, at least 1.2 times the minimal input voltage of buck converter (402)). One way to prevent such attempts is to permit the system of circuit (400) to turn on only when it can stay on with the available given power. Power is measured by measuring the voltage on resistor P1 (405) (plus any losses from the switch (404)) as represented by V_{buck1} . In one embodiment, the delay circuit (403) can be omitted from the circuit (400), since this approach can also work with zero delay because of positive hysteresis. The above-described approach improves the performance of an electronic device mounted on a panel and a device having a MCU or a device similar to a MCU. The buck converter (401) part should be able to accept any variation in voltage during turn-on because it may not be sensitive.

[0039] In the above **Figures 3 – 4**, the voltage conversion section of the buck converters (e.g. 401, 402, 301, or 311) can be replaced with step-down converters or other converters known in the art.

[0040] In one embodiment, the features illustrated in **Figure 3** can also be used in the circuit in **Figure 4**. For example, the enable circuit (320, 321, 322, 323, 324, 325, 345, and 319) in **Figure 3** can be used in the circuit of **Figure 4** to control the output of the voltage V_{buck1} to a drive stage via MOSFET (345). For example, a capacitor can be connected to the input of the buck converter 402 in **Figure 4**, in a way as the capacitor 326 connected to the input of the buck converter 311 in **Figure 3**. For

example, the control block 318 used to control the enable circuit in **Figure 3** can also be used in the circuit of **Figure 4**; and the circuit (317, 303, 308-310) and control block (304) in **Figure 3**, used to reduce output voltage of the buck converter 301 when the input power from the solar panel is low, can also be used to control the output voltage of the buck converter (401) in **Figure 4**. For examples, the resistors 340-343 connected to input and output of the buck converter 301 in **Figure 3** to detect low power conditions in **Figure 3** can also be used for the buck converter 401 in **Figure 4**.

[0041] One application of the present disclosure is using the high voltages from buck converter (e.g. 301 or 401) (e.g. V_{buck1}) to control MOSFET switches used in a solar panel energy system and the low voltage from buck converter (e.g. 311 or 402) can be used to power control circuits for a solar panel using the power generated by the solar panel. Examples of the MOSFET switches can be found in at least U.S. Pat. App. Ser. No. 12/366,597, published as U.S. Pat. App. Pub. No. 2009/0133736, and U.S. Pat. App. Ser. No. 12/411,317, now issued as U.S. Pat. No. 7,602,080, all the contents of which are incorporated herein by reference. Examples of the control circuits can be found in at least U.S. Pat. App. Ser. No. 11/875,799, published now as U.S. Pat. App. Pub. No. 2008/0097655, and U.S. Pat. App. Ser. No. 12/411,317, now issued as U.S. Pat. No. 7,602,080, the contents of which are incorporated herein by reference.

[0042] **Figure 5** shows a schematic description of an example startup behavior of wave forms, according to an embodiment of the present disclosure. Wave forms (500), where the x-axis is time, may be generated from running the circuit (400) shown in **Figure 4**, above, through simulations. Curve (501) may represent the V_{in} (or input voltage value) from the solar panel into buck converter (401). Curve (502) may represent the output of buck converter (401), which is V_{buck1} as shown in **Figure 4**. Curve (503) may represent the power used from the panel. Note in **Figure 5** the drop of power at dotted line (505) when the photovoltaic system is turned on. At dotted line (505), the second stage is turned on and therefore the system is turned on. Curve (504) may represent the output of delay circuit (403) controlling switch (404) and curve (506) may represent the V_{B2E} value coming out of delay circuit (403) ultimately used to control buck converter (402). Also, Curve (504) may show that the preload resistor P1 (405) may be turned off after V_{buck1} passes a designated threshold level and a delay

time has passed.

[0043] It is clear that many modifications and variations of this embodiment may be made by one skilled in the art without departing from the spirit of the novel art of this disclosure. These modifications and variations do not depart from the broader spirit and scope of the present disclosure, and the examples cited here are to be regarded in an illustrative rather than a restrictive sense.

[0044] The performance of solar modules can vary significantly with temperature. A system capable of measuring temperature can implement methods for controlling the voltage, power output, or the efficiency of one or more strings of solar module controllers using module temperature as a factor. For example, a formula presented by Nalin K. Gautam and N.D. Kaushika in “An efficient algorithm to simulate the electrical performance of solar photovoltaic arrays,” *Energy*, Volume 27, Issue 4, April 2002, pages 347-261, can be used to compute the voltage of a solar module at the maximum power point. Other formulae can also be used.

[0045] Many variations can be applied to the above disclosed systems and methods, without departing from the spirit of the present disclosure. For example, additional components can be added, or components can be replaced. For example, rather than using a capacitor as primary energy storage, an inductor can be used, or a combination of inductor and capacitor. Different types of transistors – such as MOSFET or bipolar transistors – may also be used interchangeably, with appropriate adjustments to voltage and other settings. Also, the balance between hardware and firmware in the micro controllers or processors can be changed, without departing from the spirit of the present disclosure.

[0046] It is clear that many modifications and variations of this embodiment can be made by one skilled in the art without departing from the spirit of the novel art of this disclosure. For example, the systems and method herein disclosed can be applied to energy generating systems besides solar photovoltaics (e.g., windmills, water turbines, hydrogen fuel cells, to name a few). Also, although specific voltage values and thresholds have been mentioned, other reference points for the target voltage can also be used. These modifications and variations do not depart from the broader spirit and scope of the present disclosure, and the examples cited here are to be regarded in an

illustrative rather than a restrictive sense.

[0047] In the foregoing specification, the disclosure has been described with reference to specific exemplary embodiments thereof. It will be evident that various modifications can be made thereto without departing from the broader spirit and scope as set forth in the following claims. The specification and drawings are, accordingly, to be regarded in an illustrative sense rather than a restrictive sense.

CLAIMS

What is claimed is:

1. An apparatus, comprising:
 - a first stage power converter to generate a first output from a direct current input;
 - a second stage power converter connected to the first stage power converter to convert the first output to a second output;
 - a micro-controller;
 - a power absorption circuit; and
 - a control circuit coupled with the first stage power converter and the second stage power converter, the control circuit powered by the first output to control the second stage power converter, wherein:
 - when the second stage power converter is in a first mode of not providing the second output to power the micro-controller, the control circuit monitors a voltage of the first output powering the power absorption circuit and signals the second stage power converter to remain in the first mode during a time period in which the voltage is below a threshold; and
 - when the voltage is above the threshold, the control circuit signals the second stage power converter to enter a second mode of providing the second output to power the micro-controller.
2. The apparatus of claim 1, wherein the power absorption circuit comprises a capacitor coupled to the first output.
3. The apparatus of claim 2, wherein the voltage above the threshold allows the capacitor to power the second stage to operate the micro-controller for at least a predetermined period of time in absence of power input from the first stage during the predetermined period of time.
4. The apparatus of claim 1, wherein the power absorption circuit comprises a

switchable load.

5. The apparatus of claim 4, wherein the control circuit signals the switchable load to be in a first state to consume the first output when the second stage power converter is the first mode and to be in a second state not to consume the first output when the voltage is above the threshold.
6. The apparatus of claim 5, wherein, when the switchable load is in the second state, the voltage above the threshold is sufficient to power the second state to operate the micro-controller for at least a predetermined period of time during which the direct current input to the first stage power converter has diminishing power.
7. The apparatus of claim 5, wherein the switchable load comprises:
a resistive device; and
a switching device connected to the resistive device, wherein the switching device is turned on in the first state to connect the resistive device to the first output and turned off in the second state to disconnect the resistive device from the first output.
8. The apparatus of claim 5, wherein the control circuit delays a first signal instructing the switching device to change from the first state to the second state to generate a second delayed signal that instructs the second stage to change from the first mode to the second mode.
9. The apparatus of claim 8, wherein the control circuit comprises:
a hysteresis amplifier coupled to the first output to generate the first signal; and
a delay circuit coupled to the first signal to generate the second delayed signal.
10. A method to supply power, the method comprising:
providing a first stage power converter to generate a first output from a direct current input, wherein the first stage power converter is coupled to a power absorption circuit;
providing a second stage power converter connected to the first stage power

converter to convert the first output to a second output, wherein the second stage power converter is coupled to a micro-controller; powering, by the first output, a control circuit that is coupled with the first stage power converter and the second stage power converter; and controlling, by the control circuit powered by the first output, the second stage power converter, by:

when the second stage power converter is in a first mode of not providing the second output to power the micro-controller, monitoring a voltage of the first output powering the power absorption circuit and signaling the second stage power converter to remain in the first mode during a time period in which the voltage is below a threshold; and

when the voltage is above the threshold, signaling the second stage power converter to enter a second mode of providing the second output to power the micro-controller.

11. The method of claim 10, further comprising:
providing, in the power absorption circuit, a capacitor coupled to the first output.
12. The method of claim 11, wherein the voltage above the threshold allows the capacitor to power the second stage to operate the micro-controller for at least a predetermined period of time in absence of power input from the first stage during the predetermined period of time.
13. The method of claim 10, further comprising:
providing, in the power absorption circuit, a switchable load.
14. The method of claim 13, wherein the control circuit signals the switchable load to be in a first state to consume the first output when the second stage power converter is the first mode and to be in a second state not to consume the first output when the voltage is above the threshold.
15. The method of claim 14, wherein, when the switchable load is in the second

state, the voltage above the threshold is sufficient to power the second state to operate the micro-controller for at least a predetermined period of time during which the direct current input to the first stage power converter has diminishing power.

16. The method of claim 14, further comprising:
providing, in the switchable load:
 - a resistive device; and
 - a switching device connected to the resistive device, wherein the switching device is turned on in the first state to connect the resistive device to the first output and turned off in the second state to disconnect the resistive device from the first output.
17. The method of claim 14, further comprising:
delaying, by the control circuit, a first signal instructing the switching device to change from the first state to the second state to generate a second delayed signal that instructs the second stage to change from the first mode to the second mode.
18. The method of claim 17, further comprising:
providing, in the control circuit, a hysteresis amplifier coupled to the first output to generate the first signal; and
providing a delay circuit coupled to the first signal to generate the second delayed signal.
19. A power supply for use in a solar electric production system, the power supply comprising:
a first stage power converter having:
 - an input connected to a first voltage from a photovoltaic panel; and
 - an output providing a second voltage different from the first voltage from the photovoltaic panel;
a second stage power converter connected to the output of the first stage power converter, the second stage power converter supplying power at a third

- voltage to a micro-controller of the photovoltaic panel; and
a control circuit coupled to the first stage power converter and the second stage power converter and powered by the output of the first stage power converter, wherein after the first stage power converter is turned on, the control circuit instructs the second stage power converter to:
not supply the power at the third voltage to the micro-controller before a determination that the output of the first stage power converter has been stable for a predetermined period of time; and
supply the power at the third voltage to the micro-controller after the determination that the output of the first stage power converter has been stable for the predetermined period of time.
20. The power supply of claim 19, wherein the first stage power converter is a first step-down converter, and the second stage power converter is a second step-down converter.

ABSTRACT

Provided is a power supply for use in a solar electric production system, including: a first stage having an input connected to a voltage from a photovoltaic panel and an output providing a first voltage different from the voltage from the photovoltaic panel; and a second stage connected to the output of the first stage, the second stage supplying power at a second voltage to a micro-controller, where the output of the first stage is turned on and stable for a period of time before the second stage is turned on to supply the power at the second voltage to the micro-controller.

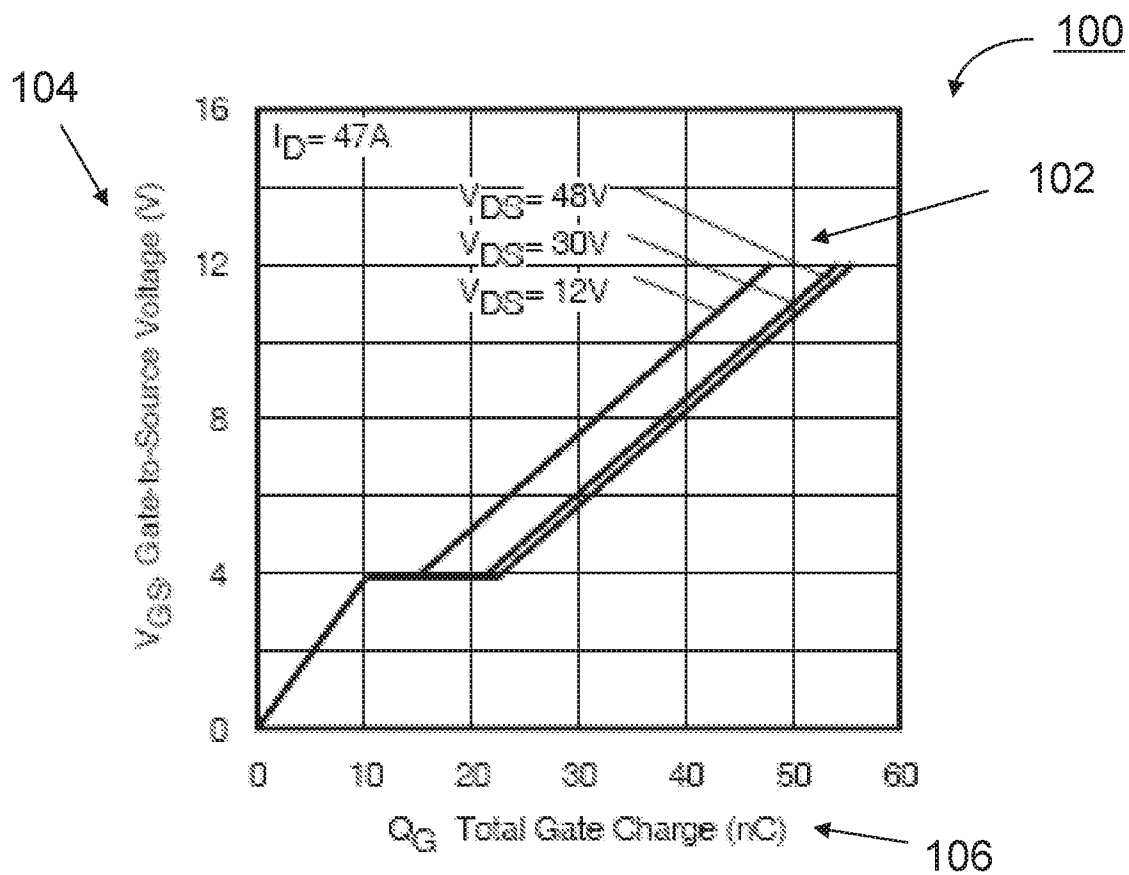


Figure 1

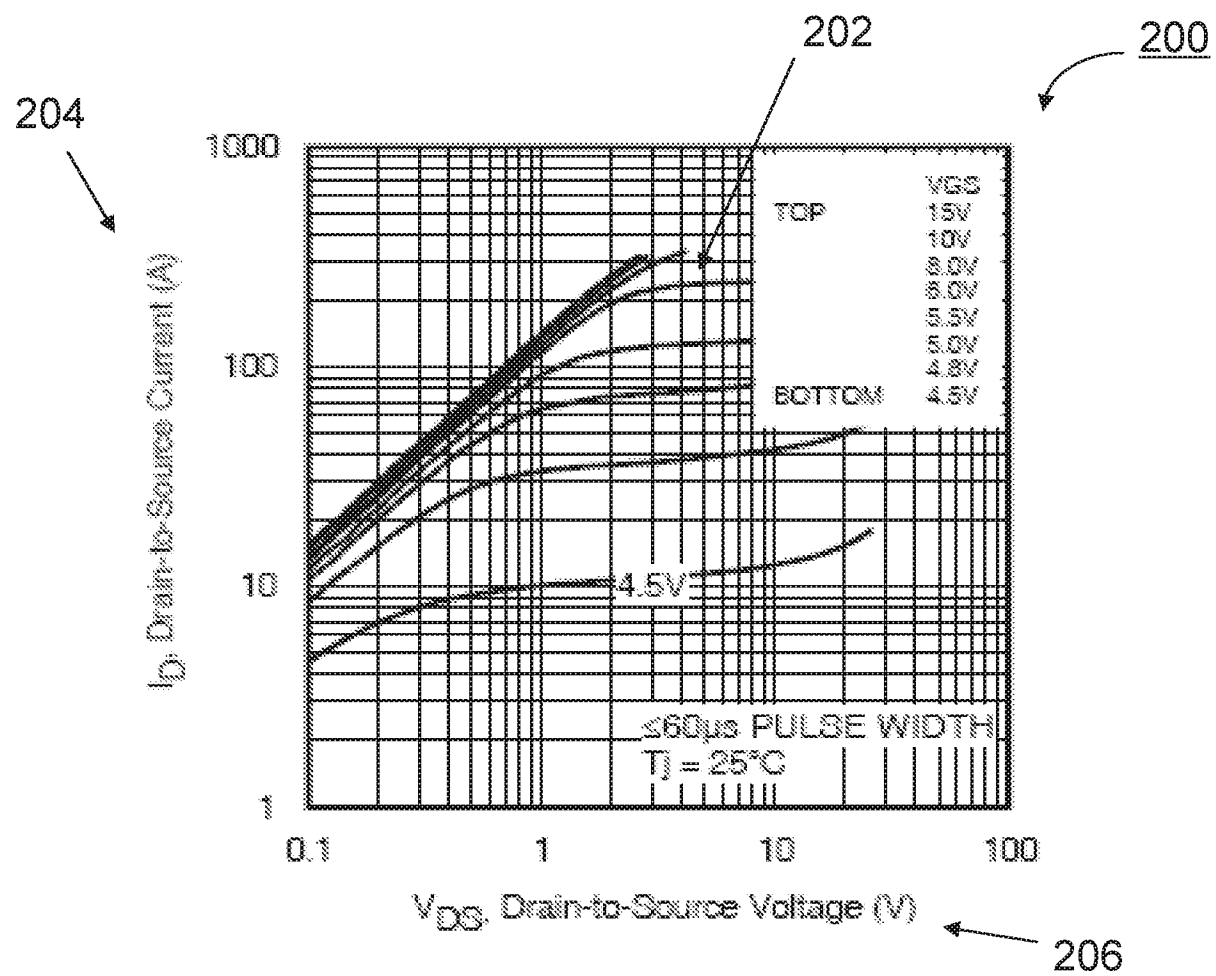


Figure 2

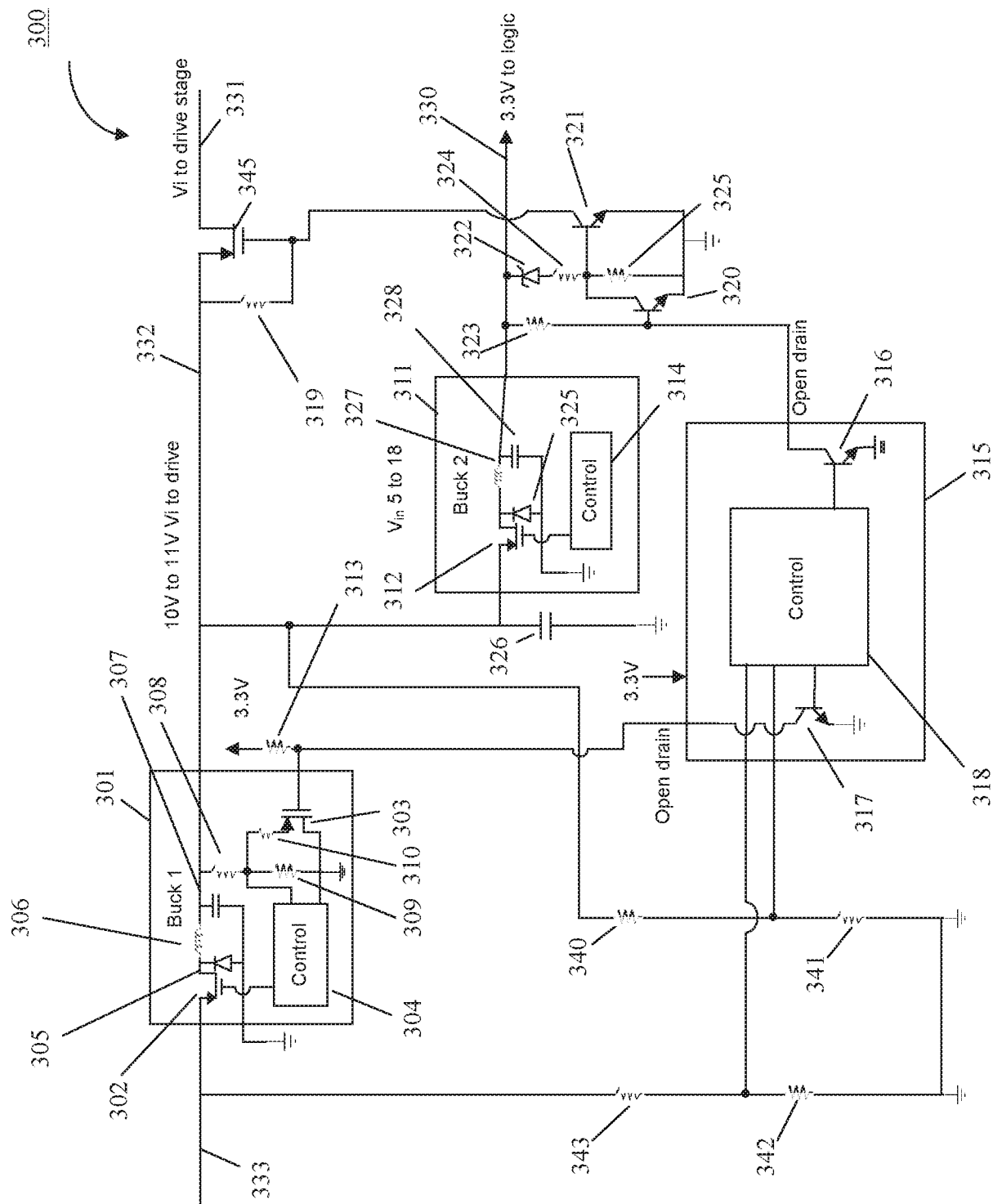


Figure 3

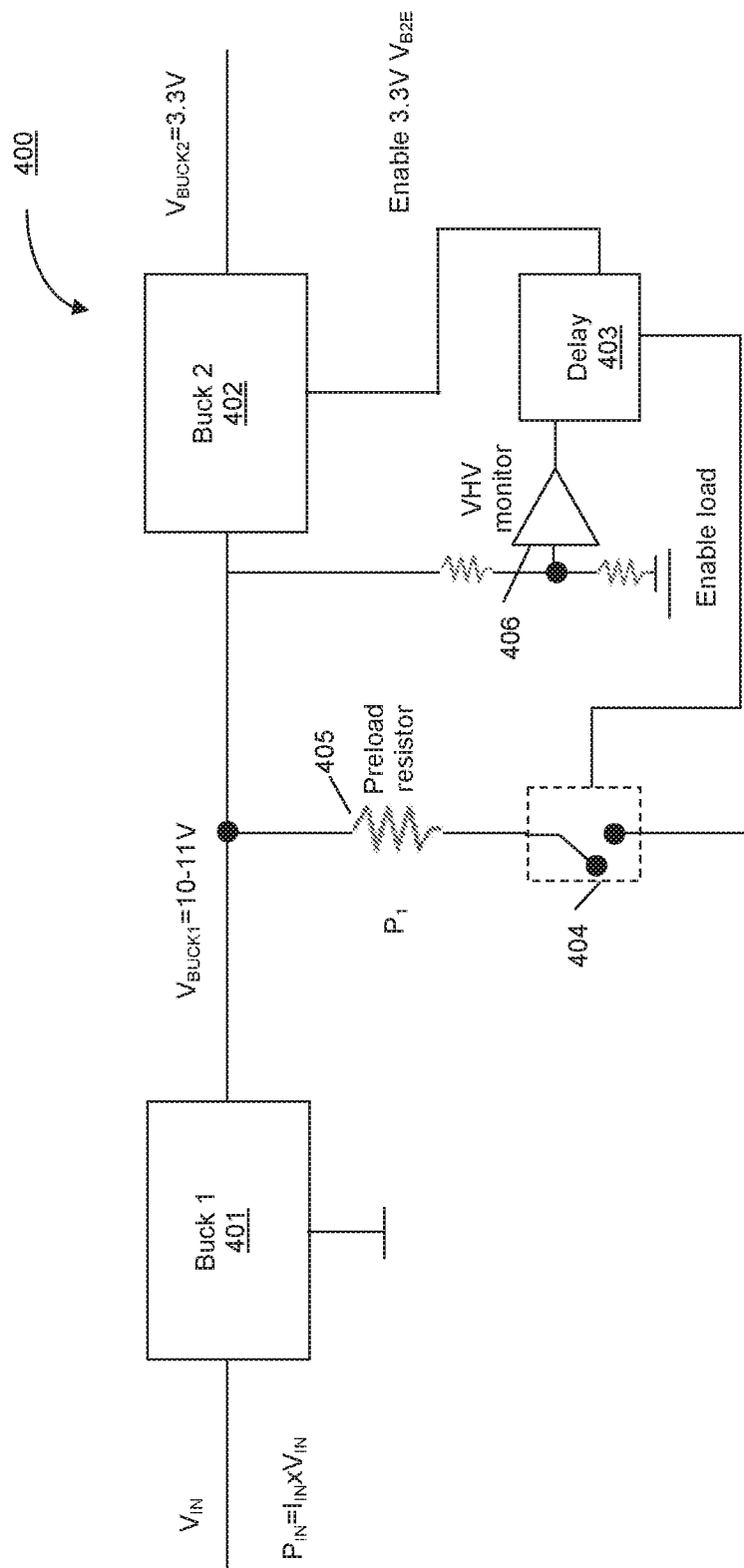


Figure 4

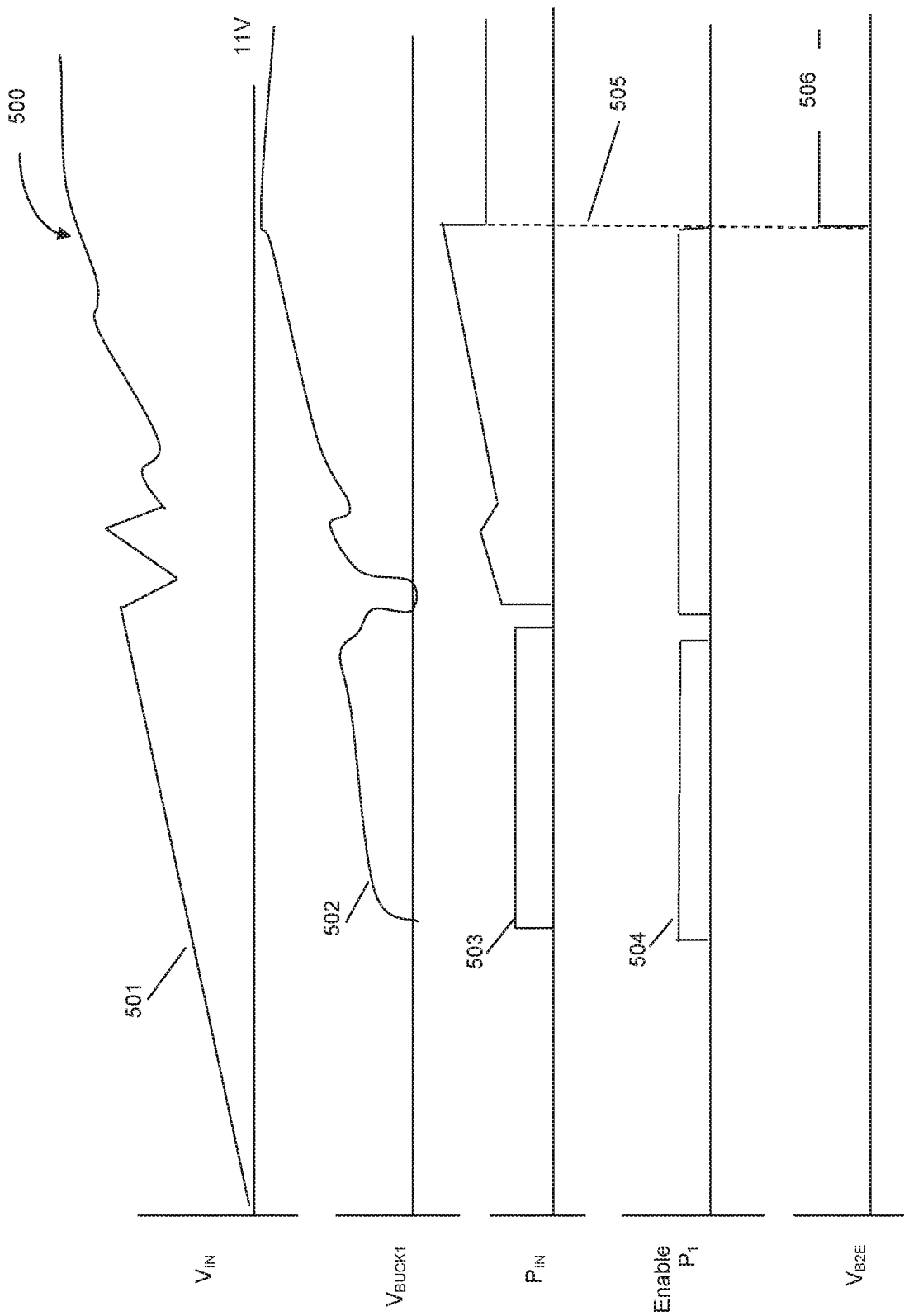


Figure 5

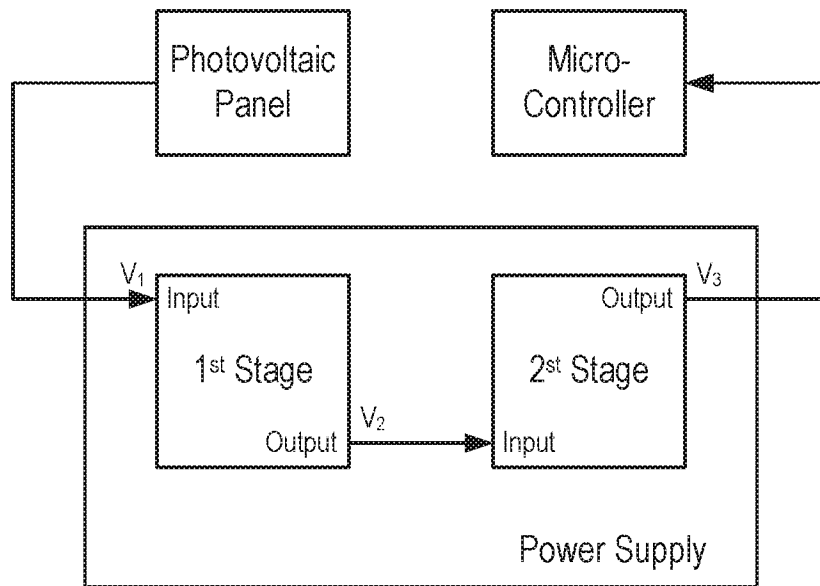


Figure 6

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Application Data Sheet 37 CFR 1.76		Attorney Docket Number	108144-201503/US (TGY015)
		Application Number	
Title of Invention	SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE		
The application data sheet is part of the provisional or nonprovisional application for which it is being submitted. The following form contains the bibliographic data arranged in a format specified by the United States Patent and Trademark Office as outlined in 37 CFR 1.76. This document may be completed electronically and submitted to the Office in electronic format using the Electronic Filing System (EFS) or the document may be printed and included in a paper filed application.			

Secrecy Order 37 CFR 5.2:

☐	Portions or all of the application associated with this Application Data Sheet may fall under a Secrecy Order pursuant to 37 CFR 5.2 (Paper filers only. Applications that fall under Secrecy Order may not be filed electronically.)
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Inventor Information:

Inventor	1				Remove
Legal Name					
Prefix	Given Name	Middle Name	Family Name	Suffix	
	Mordechay		AVRUTSKY		
Residence Information (Select One) US Residency • Non US Residency Active US Military Service					
City	Alfei Menashe		Country of Residence ⁱ	L	
Mailing Address of Inventor:					
Address 1	20 Arbel Street				
Address 2					
City	Alfei Menashe		State/Province		
Postal Code	44851		Country ⁱ	IL	
All Inventors Must Be Listed - Additional Inventor Information blocks may be generated within this form by selecting the Add button.					

Correspondence Information:

Enter either Customer Number or complete the Correspondence Information section below. For further information see 37 CFR 1.33(a).			
☐ An Address is being provided for the correspondence Information of this application.			
Customer Number	64494		
Email Address	GTIPMAIL@GTLAW.COM	Add Email	Remove Email

Application Information:

Title of the Invention	SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE		
Attorney Docket Number	108144-201503/US (TGY015)	Small Entity Status Claimed	☒
Application Type	Nonprovisional		
Subject Matter	Utility		
Total Number of Drawing Sheets (if any)	6	Suggested Figure for Publication (if any)	

Application Data Sheet 37 CFR 1.76		Attorney Docket Number	108144-201503/US (TGY015)
		Application Number	
Title of Invention	SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE		

Filing By Reference:

Only complete this section when filing an application by reference under 35 U.S.C. 111(c) and 37 CFR 1.57(a). Do not complete this section if application papers including a specification and any drawings are being filed. Any domestic benefit or foreign priority information must be provided in the appropriate section(s) below (i.e., "Domestic Benefit/National Stage Information" and "Foreign Priority Information").

For the purposes of a filing date under 37 CFR 1.53(b), the description and any drawings of the present application are replaced by this reference to the previously filed application, subject to conditions and requirements of 37 CFR 1.57(a).

Application number of the previously filed application	Filing date (YYYY-MM-DD)	Intellectual Property Authority or Country

Publication Information:

☐ Request Early Publication (Fee required at time of Request 37 CFR 1.219)

☐ **Request Not to Publish.** I hereby request that the attached application not be published under 35 U.S.C. 122(b) and certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral international agreement, that requires publication at eighteen months after filing.

Representative Information:

Representative information should be provided for all practitioners having a power of attorney in the application. Providing this information in the Application Data Sheet does not constitute a power of attorney in the application (see 37 CFR 1.32). Either enter Customer Number or complete the Representative Name section below. If both sections are completed the customer Number will be used for the Representative Information during processing.

Please Select One:	☒ Customer Number	US Patent Practitioner	☐ Limited Recognition (37 CFR 11.9)
Customer Number	64494		

Domestic Benefit/National Stage Information:

This section allows for the applicant to either claim benefit under 35 U.S.C. 119(e), 120, 121, 365(c), or 386(c) or indicate National Stage entry from a PCT application. Providing benefit claim information in the Application Data Sheet constitutes the specific reference required by 35 U.S.C. 119(e) or 120, and 37 CFR 1.78.

When referring to the current application, please leave the "Application Number" field blank.

Prior Application Status	Pending	Remove	
Application Number	Continuity Type	Prior Application Number	Filing or 371(c) Date (YYYY-MM-DD)
	Continuation of	14822227	2015-08-10

Application Data Sheet 37 CFR 1.76		Attorney Docket Number		108144-201503/US (TGY015)	
		Application Number			
Title of Invention		SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE			

Prior Application Status		Patented		Remove	
Application Number	Continuity Type	Prior Application Number	Filing Date (YYYY-MM-DD)	Patent Number	Issue Date (YYYY-MM-DD)
14822227	Continuation of	12787205	2010-05-25	9143036	2015-09-22
Prior Application Status		Expired		Remove	
Application Number	Continuity Type	Prior Application Number	Filing or 371(c) Date (YYYY-MM-DD)		
12787205	Claims benefit of provisional	61275734	2009-09-02		
Prior Application Status		Expired		Remove	
Application Number	Continuity Type	Prior Application Number	Filing or 371(c) Date (YYYY-MM-DD)		
12787205	Claims benefit of provisional	61336743	2010-01-26		
Additional Domestic Benefit/National Stage Data may be generated within this form by selecting the Add button.					Add

Foreign Priority Information:

This section allows for the applicant to claim priority to a foreign application. Providing this information in the application data sheet constitutes the claim for priority as required by 35 U.S.C. 119(b) and 37 CFR 1.55. When priority is claimed to a foreign application that is eligible for retrieval under the priority document exchange program (PDX)ⁱ the information will be used by the Office to automatically attempt retrieval pursuant to 37 CFR 1.55(i)(1) and (2). Under the PDX program, applicant bears the ultimate responsibility for ensuring that a copy of the foreign application is received by the Office from the participating foreign intellectual property office, or a certified copy of the foreign priority application is filed, within the time period specified in 37 CFR 1.55(g)(1).

			Remove
Application Number	Country ⁱ	Filing Date (YYYY-MM-DD)	Access Code ⁱ (if applicable)
Additional Foreign Priority Data may be generated within this form by selecting the Add button.			Add

Statement under 37 CFR 1.55 or 1.78 for AIA (First Inventor to File) Transition Applications

☐ This application (1) claims priority to or the benefit of an application filed before March 16, 2013 and (2) also contains, or contained at any time, a claim to a claimed invention that has an effective filing date on or after March 16, 2013.

NOTE: By providing this statement under 37 CFR 1.55 or 1.78, this application, with a filing date on or after March 16, 2013, will be examined under the first inventor to file provisions of the AIA.

Application Data Sheet 37 CFR 1.76	Attorney Docket Number	108144-201503/US (TGY015)
	Application Number	
Title of Invention	SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE	

Authorization or Opt-Out of Authorization to Permit Access:

When this Application Data Sheet is properly signed and filed with the application, applicant has provided written authority to permit a participating foreign intellectual property (IP) office access to the instant application-as-filed (see paragraph A in subsection 1 below) and the European Patent Office (EPO) access to any search results from the instant application (see paragraph B in subsection 1 below).

Should applicant choose not to provide an authorization identified in subsection 1 below, applicant **must opt-out** of the authorization by checking the corresponding box A or B or both in subsection 2 below.

NOTE: This section of the Application Data Sheet is **ONLY** reviewed and processed with the **INITIAL** filing of an application. After the initial filing of an application, an Application Data Sheet cannot be used to provide or rescind authorization for access by a foreign IP office(s). Instead, Form PTO/SB/39 or PTO/SB/69 must be used as appropriate.

1. Authorization to Permit Access by a Foreign Intellectual Property Office(s)

A. Priority Document Exchange (PDX) - Unless box A in subsection 2 (opt-out of authorization) is checked, the undersigned hereby **grants the USPTO authority** to provide the European Patent Office (EPO), the Japan Patent Office (JPO), the Korean Intellectual Property Office (KIPO), the State Intellectual Property Office of the People's Republic of China (SIPO), the World Intellectual Property Organization (WIPO), and any other foreign intellectual property office participating with the USPTO in a bilateral or multilateral priority document exchange agreement in which a foreign application claiming priority to the instant patent application is filed, access to: (1) the instant patent application-as-filed and its related bibliographic data, (2) any foreign or domestic application to which priority or benefit is claimed by the instant application and its related bibliographic data, and (3) the date of filing of this Authorization. See 37 CFR 1.14(h)(1).

B. Search Results from U.S. Application to EPO - Unless box B in subsection 2 (opt-out of authorization) is checked, the undersigned hereby **grants the USPTO authority** to provide the EPO access to the bibliographic data and search results from the instant patent application when a European patent application claiming priority to the instant patent application is filed. See 37 CFR 1.14(h)(2).

The applicant is reminded that the EPO's Rule 141(1) EPC (European Patent Convention) requires applicants to submit a copy of search results from the instant application without delay in a European patent application that claims priority to the instant application.

2. Opt-Out of Authorizations to Permit Access by a Foreign Intellectual Property Office(s)

☐ A. Applicant **DOES NOT** authorize the USPTO to permit a participating foreign IP office access to the instant application-as-filed. If this box is checked, the USPTO will not be providing a participating foreign IP office with any documents and information identified in subsection 1A above.

☐ B. Applicant **DOES NOT** authorize the USPTO to transmit to the EPO any search results from the instant patent application. If this box is checked, the USPTO will not be providing the EPO with search results from the instant application.

NOTE: Once the application has published or is otherwise publicly available, the USPTO may provide access to the application in accordance with 37 CFR 1.14.

Application Data Sheet 37 CFR 1.76		Attorney Docket Number	108144-201503/US (TGY015)
		Application Number	
Title of Invention	SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE		

Applicant Information:

Providing assignment information in this section does not substitute for compliance with any requirement of part 3 of Title 37 of CFR to have an assignment recorded by the Office.

Applicant	1	Remove
If the applicant is the inventor (or the remaining joint inventor or inventors under 37 CFR 1.45), this section should not be completed. The information to be provided in this section is the name and address of the legal representative who is the applicant under 37 CFR 1.43; or the name and address of the assignee, person to whom the inventor is under an obligation to assign the invention, or person who otherwise shows sufficient proprietary interest in the matter who is the applicant under 37 CFR 1.46. If the applicant is an applicant under 37 CFR 1.46 (assignee, person to whom the inventor is obligated to assign, or person who otherwise shows sufficient proprietary interest) together with one or more joint inventors, then the joint inventor or inventors who are also the applicant should be identified in this section. Clear		
☒ Assignee	Legal Representative under 35 U.S.C. 117	Joint Inventor
Person to whom the inventor is obligated to assign.		Person who shows sufficient proprietary interest
If applicant is the legal representative, indicate the authority to file the patent application, the inventor is:		
Name of the Deceased or Legally Incapacitated Inventor:		
If the Applicant is an Organization check here. ☒		
Organization Name	Tigo Energy, Inc.	
Mailing Address Information For Applicant:		
Address 1	420 Blossom Hill Road	
Address 2		
City	Los Gatos	State/Province CA
Country	US	Postal Code 95032
Phone Number		Fax Number
Email Address		
Additional Applicant Data may be generated within this form by selecting the Add button. Add		

Assignee Information including Non-Applicant Assignee Information:

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Application Data Sheet 37 CFR 1.76		Attorney Docket Number	108144-201503/US (TGY015)
		Application Number	
Title of Invention	SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE		

Assignee	1			
Complete this section if assignee information, including non-applicant assignee information, is desired to be included on the patent application publication. An assignee-applicant identified in the "Applicant Information" section will appear on the patent application publication as an applicant. For an assignee-applicant, complete this section only if identification as an assignee is also desired on the patent application publication.				
				Remove
If the Assignee or Non-Applicant Assignee is an Organization check here.				☐
Prefix	Given Name	Middle Name	Family Name	Suffix
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Address 2				
City		State/Province		
Country ⁱ		Postal Code		
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Email Address				
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Signature:

NOTE: This Application Data Sheet must be signed in accordance with 37 CFR 1.33(b). However, if this Application Data Sheet is submitted with the **INITIAL** filing of the application and either box A or B is not checked in subsection 2 of the "Authorization or Opt-Out of Authorization to Permit Access" section, then this form must also be signed in accordance with 37 CFR 1.14(c).

This Application Data Sheet **must** be signed by a patent practitioner if one or more of the applicants is a **juristic entity** (e.g., corporation or association). If the applicant is two or more joint inventors, this form must be signed by a patent practitioner, **all** joint inventors who are the applicant, or one or more joint inventor-applicants who have been given power of attorney (e.g., see USPTO Form PTO/AIA/81) on behalf of **all** joint inventor-applicants.

See 37 CFR 1.4(d) for the manner of making signatures and certifications.

Signature	/Lehua Wang/		Date (YYYY-MM-DD)	2016-12-28
First Name	LEHUA	Last Name	WANG	Registration Number
				48,023
Additional Signature may be generated within this form by selecting the Add button.				Add

Application Data Sheet 37 CFR 1.76		Attorney Docket Number	108144-201503/US (TGY015)
		Application Number	
Title of Invention	SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE		

This collection of information is required by 37 CFR 1.76. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 23 minutes to complete, including gathering, preparing, and submitting the completed application data sheet form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether the Freedom of Information Act requires disclosure of these records.
2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.
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6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspections or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

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**DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN
APPLICATION DATA SHEET (37 CFR 1.76)**

Title of Invention	SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE
-----------------------	--

As the below named inventor, I hereby declare that:

This declaration is directed to: ☐ The attached application, or
☒ United States application or PCT international application number 14/822,227
filed on August 10, 2015

The above-identified application was made or authorized to be made by me.

I believe that I am the original inventor or an original joint inventor of a claimed invention in the application.

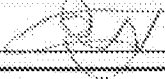
I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) years, or both.

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LEGAL NAME OF INVENTOR

Inventor: MORDECHAY AVRUTSKY Date (Optional): 08/28/2015

Signature: 

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Electronic Patent Application Fee Transmittal

Application Number:

Filing Date:

Title of Invention:

SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER
SUPPLY MODULE

First Named Inventor/Applicant Name:

Mordechay AVRUTSKY

Filer:

John Patrick Ward/YIJIE SHEN

Attorney Docket Number:

108144-201503/US (TGY015)

Filed as Small Entity

Filing Fees for Utility under 35 USC 111(a)

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
UTILITY FILING FEE (ELECTRONIC FILING)	4011	1	70	70
UTILITY SEARCH FEE	2111	1	300	300
UTILITY EXAMINATION FEE	2311	1	360	360

Pages:

Claims:

Miscellaneous-Filing:

Petition:

Patent-Appeals-and-Interference:

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				730

Electronic Acknowledgement Receipt

EFS ID:	27921387
Application Number:	15392960
International Application Number:	
Confirmation Number:	3397
Title of Invention:	SYSTEMS AND METHODS FOR ENHANCED EFFICIENCY AUXILIARY POWER SUPPLY MODULE
First Named Inventor/Applicant Name:	Mordechay AVRUTSKY
Customer Number:	64494
Filer:	John Patrick Ward/YIJIE SHEN
Filer Authorized By:	John Patrick Ward
Attorney Docket Number:	108144-201503/US (TGY015)
Receipt Date:	28-DEC-2016
Filing Date:	
Time Stamp:	18:15:11
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	DA
Payment was successfully received in RAM	\$ 730
RAM confirmation Number	122916INTEFSW00007031502638
Deposit Account	502638
Authorized User	YIJIE SHEN

The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:

37 CFR 1.16 (National application filing, search, and examination fees)

37 CFR 1.17 (Patent application and reexamination processing fees)

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37 CFR 1.19 (Document supply fees)
 37 CFR 1.20 (Post Issuance fees)
 37 CFR 1.21 (Miscellaneous fees and charges)

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		108144-201503US_Description.pdf	104548	yes	23
			ed13bb1f16f94485f4e67e6d2bcd5be276860e99		
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Specification		1	17	
	Claims		18	22	
	Abstract		23	23	
Warnings:					
Information:					
2	Drawings-only black and white line drawings	108144-201503US_Drawings.pdf	211931	no	6
			3864e093753fad49e254a2f053d9931df22a15e3		
Warnings:					
Information:					
3	Application Data Sheet	108144-201503_ADS.PDF	1793358	no	8
			4d72d7307c0bddfa01cac21a1b50d175553be2d1		
Warnings:					
Information:					
4	Oath or Declaration filed	108144-201502_Declaration_Executed.PDF	179168	no	2
			f358bdf8ddf79a49928369fc4c47eb7ad0a451bd		
Warnings:					
Information:					
5	Fee Worksheet (SB06)	fee-info.pdf	35046	no	2
			d976d72d5fd4e3ea43a3ecc03c60785b7bb39019		
Page 134 of 135		SMA Solar Technology America, LLC, Ex. 1006			

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