

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

WESTERN DIGITAL TECHNOLOGIES, INC.,
Petitioner

v.

LONGITUDE LICENSING LTD.,
Patent Owner

IPR2023-01230

U.S. Patent No. 9,207,701

PETITIONER'S UPDATED EXHIBIT LIST

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October 21, 2024

EX1001	U.S. Patent No. 9,207,701 to Hayashi et al. ("701 patent")
EX1002	Prosecution File History of the '701 Patent ("768 app")
EX1003	Declaration of Dr. Harvey J. Stiegler
EX1004	CV of Dr. Harvey J. Stiegler
EX1005	U.S. Patent No. 7,427,888 to Zhang et al. ("Zhang")
EX1006	U.S. Patent No. 5,767,735 to Javanifard et al. ("Javanifard")
EX1007	Prosecution File History of Zhang (App. 10/888667) ("667 app")
EX1008	Charge Pump Circuit Design, Pan et al., 1st Edition, 2006 ("Pan")
EX1009	Prosecution File History of Parent of '701 patent (App. 12/052422) ("422 app")
EX1010	Prosecution File History of Parent of '701 patent (App. 13/912845) ("845 app")
EX1011	Order Granting Western Digital's Motion to Stay (Amazon) (05-16-23)
EX1012	Order Granting Western Digital's Motion to Intervene (Amazon) (05-16-23)
EX1013	Director Vidal Memorandum re: Interim Procedure for Discretionary Denials in AIA Post-Grant Proceedings with Parallel District Court Litigation, June 22, 2022
EX1014	U.S. Patent No. 7,579,902 to Frulio et al. ("Frulio")
EX1015	U.S. Patent No. 7,477,093 to Al-Shamma et al. ("Al-Shamma")
EX1016	U.S. Patent No. 7,046,076 to Daga et al. ("Daga")
EX1017	U.S. Patent No. 6,157,242 to Fukui ("Fukui")
EX1018	U.S. Patent No. 7,505,350 to Lee et al. ("Lee")
EX1019	U.S. Patent No. 6,954,386 to Narui et al. ("Narui")
EX1020	"Charge Pump Circuits with Low Area and High Power Efficiency for Memory Applications," Kang et al., Journal of Semiconductor

Petitioner's Updated Exhibit List
IPR2023-01230 (Patent No. 9,207,701)

	Technology and Science, Vol. 6, No. 4, December 2006
EX1021	U.S. Patent No. 6,034,539 to Hwang (“Hwang”)
EX1022	U.S. App. Pub. No. 2003/0067063A1 to Muff et al. (“Muff”)
EX1023	U.S. Patent No. 6,346,738 to Kim et al. (“Kim”)
EX1024	Declaration of Dr. Sylvia D. Hall-Ellis
EX1025	U.S. Patent No. 7,170,197 to Lopata (“Lopata”)
EX1026	“Charge-Pump Circuits: Power-Consumption Optimization,” Palumbo et al., IEEE Transactions on Circuits and Systems—I: Fundamental Theory and Applications, Vol. 49, No. 11, November 2002 (“Palumbo”)
EX1027	Texas Instruments, Power Management Selection Guide, August 30, 2004, https://web.archive.org/web/20050306072641/http://focus.ti.com:80/lit/ml/slv145d/slv145d.pdf (retrieved July 19, 2024), accessible also through “download” hyperlink in EX1028 (“TI-Guide”)
EX1028	Texas Instruments, DC Converters Selection Guides, 2024, https://web.archive.org/web/20040909150107/http://focus.ti.com:80/analog/docs/analogtechdoc.tsp?navSection=sel_guides&templateId=2&familyId=490 (retrieved July 19, 2024) (“TI DC Converters Selection Guides”)
EX1029	Bruce Jacob deposition transcript, June 21, 2024 (“Jacob Deposition”)
EX1030	U.S. Patent No. 6,927,441 to Pappalardo et al. (“Pappalardo”)
EX1031	U.S. Patent No. 6,060,942 to Oh (“Oh”)
EX1032 (NEW)	Petitioner’s Oral Argument Demonstratives

Respectfully submitted,

Dated: October 21, 2024

/David M. O'Dell/

David M. O'Dell

Lead Counsel for Petitioner

Registration No. 42,044

CERTIFICATE OF SERVICE

The undersigned certifies, in accordance with 37 C.F.R. § 42.6, that service was made on the Patent Owner as detailed below:

Date of service October 21, 2024

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Documents served Petitioner's Updated Exhibit List and EX1032

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