

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC,
Petitioner,

v.

GREENTHREAD LLC,
Patent Owner

Case IPR2023-01244
U.S. Patent No. 11,121,222

PETITIONER'S UPDATED EXHIBIT LIST

EXHIBIT LIST

Exhibit No.	Description
1001	U.S. Patent No. 11,121,222 to Rao (the “’222 Patent”)
1002	Prosecution History of the ‘222 Patent (“the Prosecution History”)
1003	Declaration of Travis Blalock, PhD
1004	Curriculum Vitae of Travis Blalock, PhD
1005	U.S. Patent No. 4,684,971 (“Payne”)
1006	U.S. Patent No. 4,907,058 to Sakai (“Sakai”)
1007	U.S. Patent No. 6,043,114 to Kawagoe, et al., (“Kawagoe”)
1008A	Stanley Wolf and Richard N. Tauber, <i>Silicon Processing For The VLSI Era</i> , Vol 1, Lattice Press (2000) (“Wolf.1”)
1008B	Stanley Wolf and Richard N. Tauber, <i>Silicon Processing For The VLSI Era</i> , Vol. 2, Lattice Press (2000) (“Wolf.2”)
1008C	Stanley Wolf and Richard N. Tauber, <i>Silicon Processing For The VLSI Era</i> , Vol. 3, Lattice Press (2000) (“Wolf.3”)
1008D	Stanley Wolf and Richard N. Tauber, <i>Silicon Processing For The VLSI Era</i> , Vol. 4, Lattice Press (2000) (“Wolf.4”)
1009	U.S. Patent No. 4,160,985 (“Kamins”)
1010	U.S. Patent No. 4,481,522 (“Jastrzebski”)
1011	U.S. Patent Application Publication No. 2003/0042511 (“Rhodes”)
1012	U.S. Patent Application Publication No. 2002/0102783 (“Fujimoto”)
1013	Wang and Agrawal, <i>Single Event Upset: An Embedded Tutorial</i> , 21st Intl Conf on VLSI Design, IEEE 2008 (“Wang”)

Exhibit No.	Description
1014	Publication Declaration of Alyssa G. Resnick for Wolf.1 and Wolf.2 (“Resnick Decl.”)
1015	Publication Declaration of Rachel J. Watters for Wolf.3 and Wolf.4 (“Watters Decl.”)
1016	IPR2020-289, Patent Owner Preliminary Response
1017	U.S. Pat. No. 8,421,195 (the “‘195 Patent”)
1018	U.S. Pat. No. 11,316,014 (the “‘014 Patent”)
1019	Civil Action No. 2:19-cv-00147-JRG, Dkt. 67 (E.D. Tex. Apr. 20, 2019)
1020	Prosecution History of U.S. Pat. No. 8,421,195
1021	Prosecution History of U.S. Pat. No. 11,316,014
1022	U.S. Patent Application Publication No. 2004/0063288 (“Kenney”)
1023	Jaeger, <i>Introduction to Microelectronic Fabrication</i> , Vol. V, Addison-Wesley Modular Series on Solid State Devices (1988) (“Jaeger”)
1024	U.S. District Courts – Case Statistics, obtained at https://www.uscourts.gov/statistics-reports/analysisreports/federal-court-management-statistics , dated March 31, 2023
1025	U.S. Patent No. 4,435,896 (“Parrillo”)
1026	L.C. Parrillo, R.S. Payne et al., <i>Twin-Tub CMOS - A Technology for VLSI Circuits</i> , IEEE 1980 (“Parrillo2”)
1027	U.S. Patent Application Publication No. 2007/0045682 to Hong <i>et al.</i> (“Hong”)
1028	The Oxford American Dictionary and Language Guide, Oxford

Exhibit No.	Description
	University Press (1996)
1029	Defendants' Opening Claim Construction Brief
1030	Patent Owner's Response Claim Construction Brief
1031	Defendants' Reply Claim Construction Brief
1032	Patent Owner's Sur-Reply Claim Construction Brief
1033	Proposed Claim Constructions in the District Court Case
1034	Redacted District Court Case Transfer Order
1035	Civil Action No. 2:19-cv-00147-JRG, Dkt. 105 (E.D. Tex. Jul. 9, 2020)
1036	IPR2020-289, Termination Order
1037	District Court's Tentative Claim Construction Ruling
1038	U.S. Patent Application Publication No. 2003/0183856 to Wieczorek ("Wieczorek")
1039	Publication Declaration of Sylvia Hall-Ellis for Wolf
1040	Law360 Article "Stays Pending IPR In Del. District Courts Are Here To Stay," Kraning and Del Dotto, March 11, 2022.
1041	Reserved
1042	Reserved
1043	Reserved
1044	Reserved
1045	Reserved

Exhibit No.	Description
1046	E-mail from Nicholas T. Matich on October 11, 2023 at 5:31 PM.
1047	E-mail from Nicholas T. Matich on October 12, 2023 at 12:09 AM.

Dated: October 31, 2023

Respectfully submitted,
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CERTIFICATE OF SERVICE

The undersigned certifies that a copy of the foregoing **PETITIONER'S
UPDATED EXHIBIT LIST** and was served on October 31, 2023, via email at the
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