

1.	Epitaxy	(E)
2.	Gate	(G)
3	Dielectric	(D)
4	Ohmic	(O)

$O \rightarrow G$ { gate electrode
does not see
ohmic area }

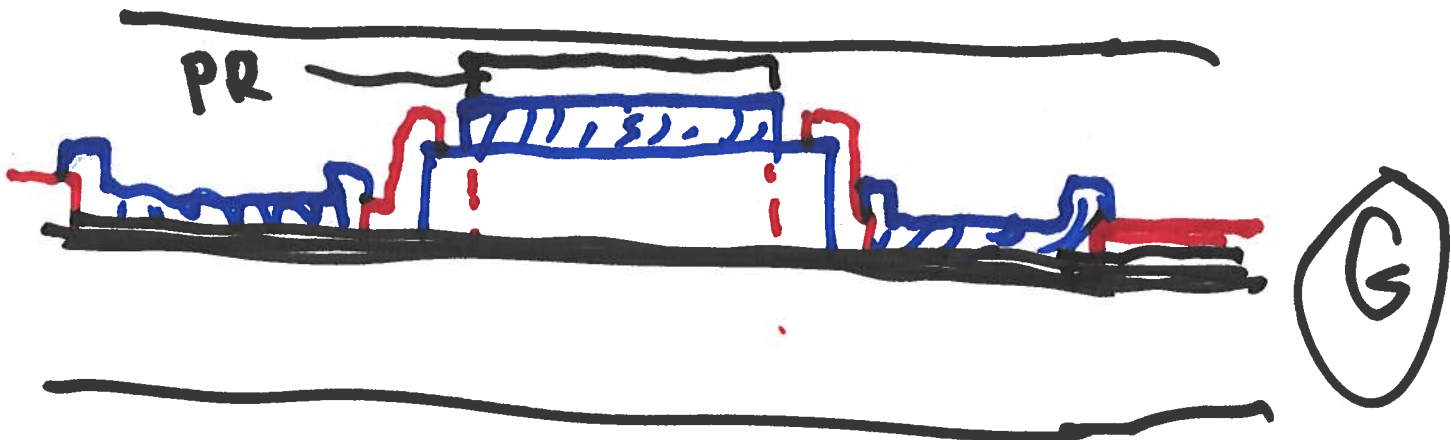
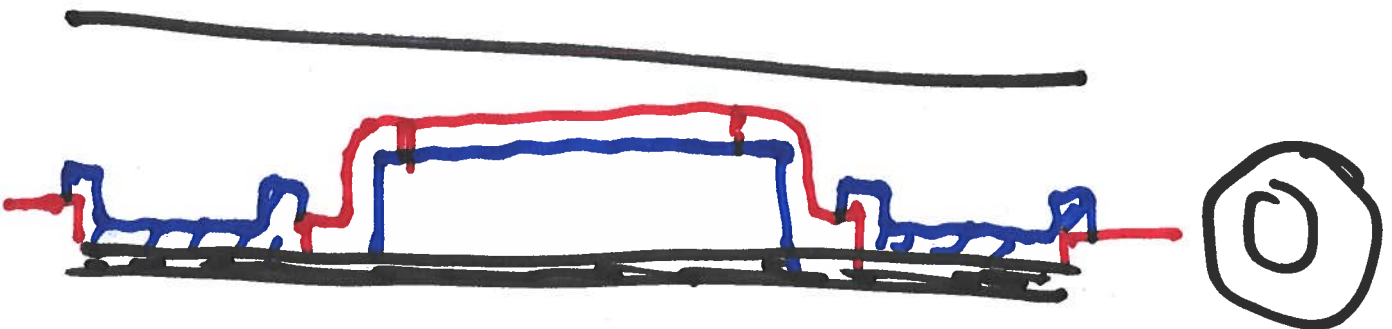
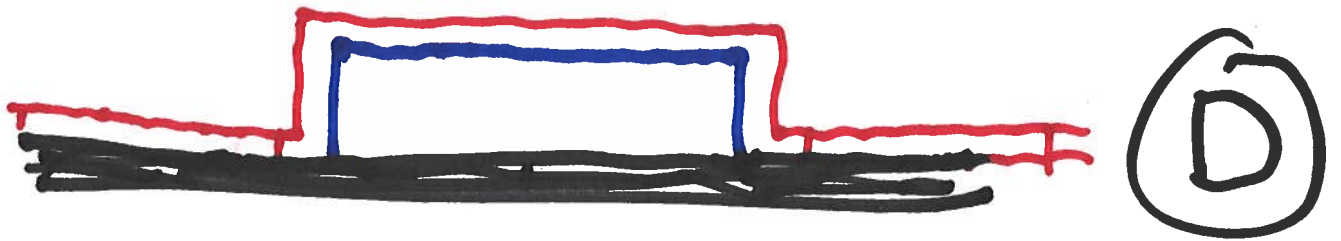
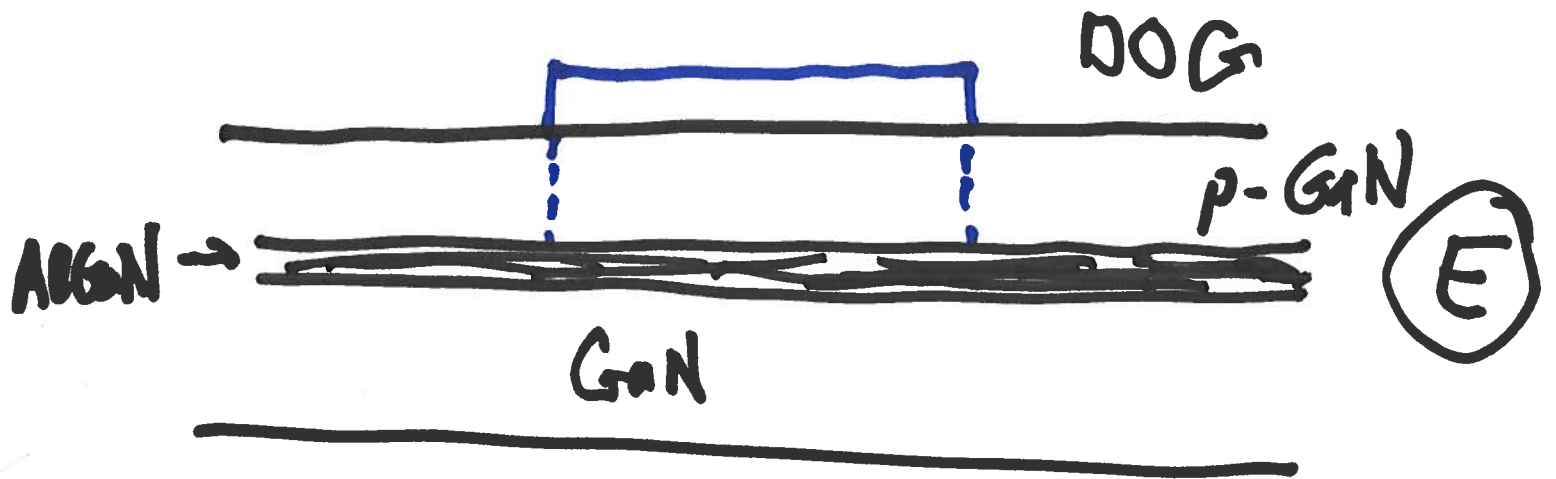
$D \rightarrow O$ Board decision

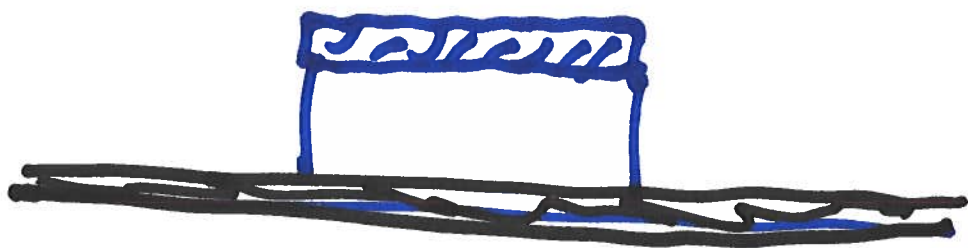
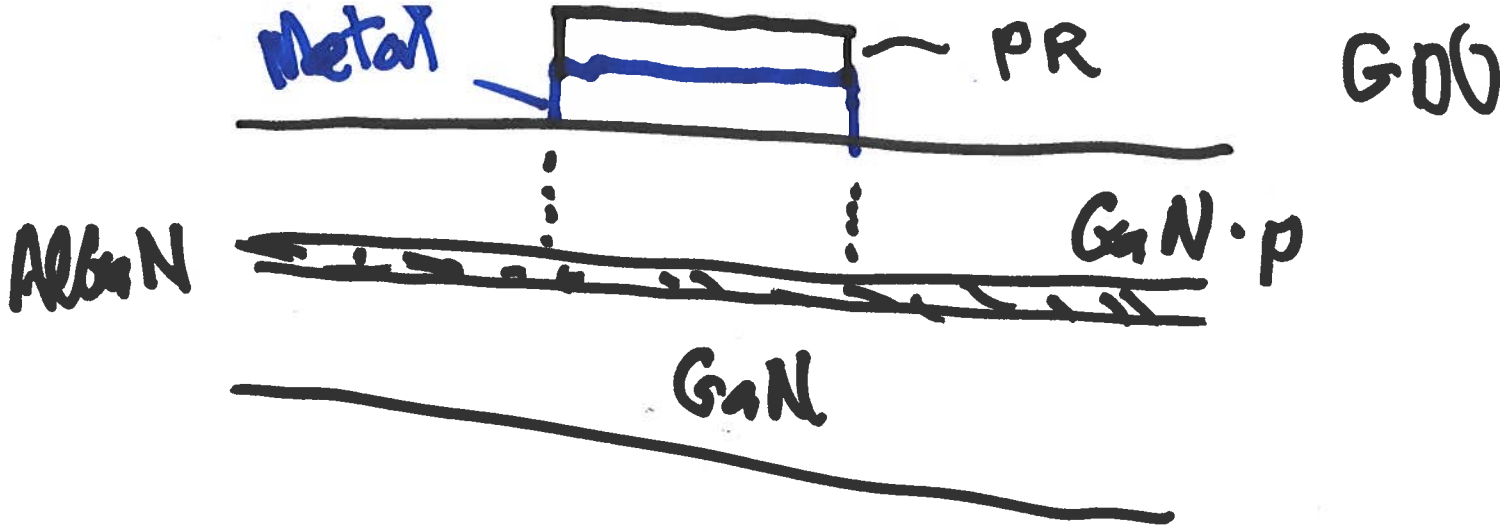
D first encapsulated,
the gate region
to avoid contamination



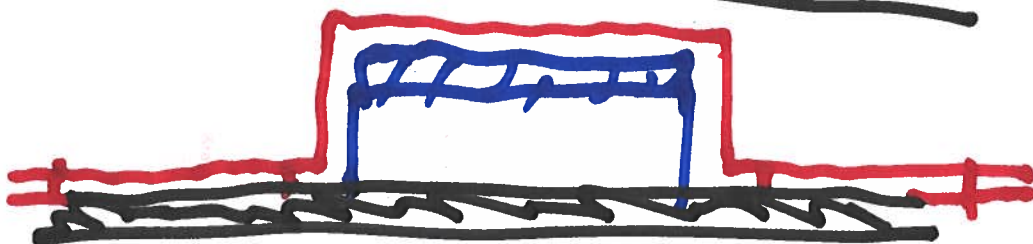
- D O G

- G D O
- O G D

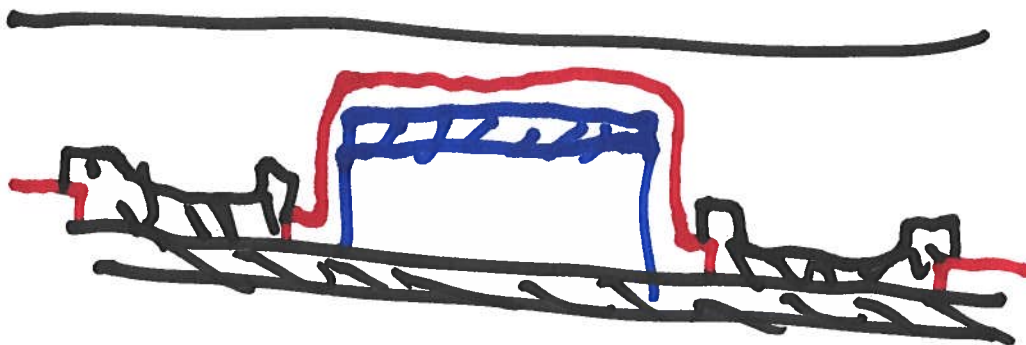




G



D



O

O G D

