

Dongwan-Shin

Jul 9, 2012 | Alumni

[VIEW DETAILS](#)**Communication Synthesis for SoC**

Oct 11, 2023 | PhD Defenses

by Dongwan Shin , University of California,
Irvine April 16, 2004[VIEW DETAILS](#)**Gajski**

May 29, 2012 |

Professor Gajski's Research Research Interests
 Daniel Gajski is particularly interested in
 requirements and specifications of embedded
 systems and the design process that leads from
 an executable specification to the final
 manufacturable blueprint. In order to study...

[VIEW DETAILS](#)**Design of a MP3 Decoder using the
System-On-Chip Environment (SCE)**

Jul 20, 2007 | Technical Reports

Andreas Gerstlauer, Dongwan Shin, Samar
 Abdi, Pramod Chandraiah, Daniel Gajski,
 "Design of a MP3 Decoder using the System-
 On-Chip Environment (SCE)", TR 07-05,
 November 2, 2007 download pdf

[VIEW DETAILS](#)**IEEE Transactions on Very Large
Scale Integration Systems (TVLSI)**

Jun 19, 2008 | Journal Articles

Dongwan Shin, Andreas Gerstlauer, Rainer
 Dömer, Daniel Gajski: An Interactive Design
 Environment for C-Based High-Level Synthesis
 of RTL Processors. IEEE Trans. VLSI Syst.
 (TVLSI) 16(4):466-475 (2008)

[VIEW DETAILS](#)**IEEE Transactions on Computer-
Aided Design of Integrated Circuits
and Systems (TCAD)**

Jun 19, 2007 | Journal Articles

Sudeep Pasricha, Nikil D. Dutt: A Framework for
 Cosynthesis of Memory and Communication
 Architectures for MPSoC. IEEE Trans. on CAD
 of Integrated Circuits and Systems (TCAD)

**Daniel D. Gajski**

Jul 5, 2012 | Faculty, Research, Staff

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Daniel Gajski is particularly interested in
 requirements and specifications of embedded



November 2, 2007 download pdf

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Architectures for MPSoC. IEEE Trans. on CAD of Integrated Circuits and Systems (TCAD) 26(3):408-420 (2007) Partha Biswas, Nikil D. Dutt, Laura Pozzi, Paolo Ienne: Introduction of...

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Daniel Gajski is particularly interested in requirements and specifications of embedded systems and the design process that leads from an executable specification to the final manufacturable blueprint. In order to study the design process, his group is developing new specification languages and modeling guidelines, as well as simulation, synthesis, and verification tools. In order to obtain efficient specifications and design models, they are taxonomizing models of computations, platform architectures and design styles. In order to develop efficient CAD tools, they are studying synthesis algorithms for systems, architectures, processors, controllers, datapaths, and other intellectual properties (IPs). Their further goal is to build proof-of-concept tools and prove our methodology for different application domains and tools on extensive industrial-strength examples.

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EURASIP Journal on Embedded Systems (EJES)

Jun 19, 2008 | Journal Articles

Rainer Dömer, Andreas Gerstlauer, Junyu Peng, Dongwan Shin, Lukai Cai, Haobo Yu, Samar Abdi, Daniel D. Gajski: System-on-Chip Environment: A SpecC-Based Framework for Heterogeneous MPSoC Design. EURASIP J. Emb. Sys. (EJES) 2008 (2008)

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