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International Rectifier Introduces DC Bus Converter Chip Set Re-Defining Distributed Power Architecture for Networking and Communication Systems

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International Rectifier

April 14, 2003- International Rectifier, IR® (NYSE: IRF), today unveiled the first group of products in the family of DC Bus Converter Chip Sets that re-define the underlying distributed power architecture for 48V-input, 150W board-mounted power (BMP) converters used in telecom and networking systems.


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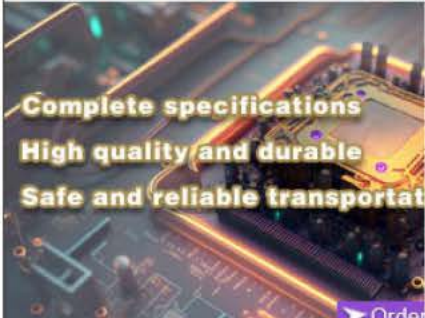
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IR's DC Bus Converter Chip Set architecture establishes benchmarks for overall efficiency, power density and simplicity, delivering more than 96% efficiency at 20A/150Wout in a circuit footprint less than 1.7in2. The Chip Set enables a 53% size reduction when compared to industry-standard quarter-brick form factors and slashes component count in isolated converters from approximately 50 components to 20.

The new Chip Set consists of a single IR2085S control IC and one pair each IRF7493 primary- and IRF6603 secondary-side HEXFET® power MOSFETs, plus the IRF7380 for primary-side bias and an IRF9956 for the secondary-side gate clamp. The Chip Set is designed for the isolated front-end of two-stage distributed power architectures (DPA) with an intermediate bus voltage feeding non-isolated point-of-load (POL) converters.

Two-stage DPA schemes do not require a tightly regulated intermediate bus voltage, since the POL will typically accept a relatively wide input voltage, and the POL provides the needed regulation to the load. In addition, multiple DC Bus Converter Chip Sets can be placed in parallel for higher power requirements.





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Part Number	Package	Voltage Rating(V)	RDS(on) max(mΩ)	Qg typ(nC)	Qgd typ(nC)	Function
IR2085S	SO-8	100V	-	-	-	Primary-side control IC, fixed 50% duty cycle, self-oscillating(1 per chip set)
IRF7493	SO-8	80V	15mΩ @ 10Vgs	31nC	12nC	Primary-side MOSFET used in half-bridge(2 per chip set)
IRF6603	DirectFET™	30V	3.9mΩ @ 10Vgs	39nC	17nC	Secondary-side MOSFET for self-driven sync rectification(2 per chip set)
IRF7380	Dual SO-8	80V	73mΩ @ 10Vgs	15nC	4.5nC	Primary-side MOSFET for primary-side bias(1 per chip set)
IRF9956	Dual SO-8	30V	100mΩ @ 10Vgs	6.9nC	1.8nC	Secondary-side MOSFET for gate clamp bias(1 per chip set)

Pricing and Availability

The DC Bus Converter Chip Set samples and evaluation tools are available now. Production release for the chip set is June 2003. Pricing is US \$7.67 for each chip set in 10,000-unit quantities.

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