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| APPLICATION # | RECEIPT DATE / TIME | ATTORNEY DOCKET # |
|---|---|---|
| **11/569,083** | **06/02/2024 11:13:47 PM Z ET** | **2004P01041WOUS** |

## Title of Invention

INTEGRATED CIRCUIT AND METHOD FOR BUFFERING TO OPTIMIZE BURST LENGTH IN NETWORKS ON CHIPS

## Application Information

| | | | |
|---|---|---|---|
| APPLICATION TYPE | Utility - U.S. National Stage under 35 USC 371 | PATENT # | 8086800 |
| CONFIRMATION # | 4172 | FILED BY | Diana Girgis |
| PATENT CENTER # | 65768261 | FILING DATE | 11/14/2006 |
| CUSTOMER # | 24737 | FIRST NAMED INVENTOR | Andrei Radulescu |
| INTL. APPLICATION # | - | INTL. FILING DATE | - |
| CORRESPONDENCE ADDRESS | - | AUTHORIZED BY | Jennifer Hayes |

## Documents

**TOTAL DOCUMENTS: 1**

| DOCUMENT | PAGES | DESCRIPTION | SIZE (KB) |
|---|---|---|---|
| 2024-06-02 - Statutory Disclaimer in a Patent - 098000-020.pdf | 2 | Statutory disclaimers per Manual of Patent Examining Procedure(MPEP) 1490. | 222 KB |

## Digest

| DOCUMENT | MESSAGE DIGEST(SHA-512) |
|---|---|
| 2024-06-02 - Statutory Disclaimer in a Patent - 098000- | DDBD4B8C8C5F46EEB63886E7874F053447C099036F3B8AD97C0F1872798162472F105E2841D92E5D441758EFCA4A6E45086 |

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Doc Code: STAT.DISCLMR
Document Description: Statutory Disclaimers per Manual of Patent Examining Procedure(MPEP) 1490

PTO/SB/43 (11-23)
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| Name of Patentee | Docket Number (optional) |
|---|---|
| Network System Technologies LLC | 098000-000020 |
| **Patent Number** | **Date Patent Issued** |
| 8,086,800 | Dec. 27, 2011 |

Title of Invention: INTEGRATED CIRCUIT AND METHOD FOR BUFFERING TO OPTIMIZE BURST LENGTH IN NETWORKS ON CHIPS

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- [ ] attorney or agent of record.

| Signature | Date |
|---|---|
| Warren Hurwitz | 05/31/2024 |
| **Name (printed/typed)** | **Practitioner registration number** |
| Warren Hurwitz | |

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