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Luca Benini received the B.S. degree (summa cum laude) in electrical engineering from the University of Bologna, Italy, in 1991, and the M.S. and Ph.D. degrees in electrical engineering from Stanford University in 1994 and 1997, respectively. He is an associate professor in the department of electronics and computer science in the University of Bologna. He also holds visiting researcher positions at Stanford University and the Hewlett-Packard Laboratories, Palo Alto, CA.

Dr. Benini's research interests are in all aspects of computer-aided design of digital circuits, with special emphasis on low-power applications, and in the design of portable systems. On these topics, he published more than 150 papers in international conferences and journals. He is co-author of the book: *Dynamic Power management, Design Techniques and CAD tools*, Kluwer 1998.

Dr. Benini is a member of the technical program committee for several technical conferences, including the *Design Automation Conference*, the *International Symposium on Low Power Design* and the *International symposium on Hardware-Software Codesign*.

Teaching

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