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PRESS

DATE⁰⁵

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The World's Favourite Electronic Systems Design Conference

DATE 04 receives world record submissions

Paris, November 4th, 2003 – DATE, Europe's premier conference and exhibition for electronic design, automation and test, has received all-time record submissions to the conference programme. For the 2004 event, which takes place in Paris at CNIT, Paris La Défense from February 16th to 20th, 780 papers were submitted for review to the 372 member Technical Programme Committee, chaired by Prof. Georges Gielen of the Katholieke Universiteit Leuven, Belgium. The number of papers submitted has increased by 39% over DATE 03 – and is the highest number of papers ever submitted to an electronic design conference anywhere in the world.

The submissions have also come from all over the world – underlining DATE's status as the leading international event in its field. The highest submission from a single country came from the USA (296 papers), there were 313 papers from Europe, and 64 papers from the Asia-Pacific region – demonstrating DATE's high visibility in both established and emerging world markets.

About ten percent of the papers were submitted to the Designers' Forum, which targets industrial designers and addresses application-oriented contributions that discuss the use of innovative design and test methods and tools as well as practical case studies.

Following the DATE programme selection meeting in Leuven, 181 papers have been chosen from the 702 submitted to the main technical tracks – an acceptance rate of 27%. These are supplemented by 52 papers accepted for the Designers' Forum track. A further 81 submissions were accepted as Interactive Presentations – an innovation for DATE 04 (see below).

Special Focus Topics

Ultra Low Power Design

The topic receiving most papers for the 2004 programme is low-power design and DATE 04 will feature a special focus day on Ultra Low Power Design on Wednesday, February 18th. This special day focuses on the power problem and its implications on design techniques and methodologies. It addresses the difficulties ahead and advances strategies and principles for achieving solutions. Major challenges for the successful design of highly integrated systems in nanometer technologies will be covered:

- Ultra-low-power digital design techniques.
- Design methods for power estimation and reduction in embedded systems.
- Dynamic power management techniques.
- Low-power system-level design methodologies (for HW and SW components).

New Trends in Multimedia

The second special focus day on Thursday, February 19th combines special and invited sessions that present the new trends and challenges in the design of Systems-on-Chips and embedded systems for multimedia applications, as well as the platforms, design methodologies and tools that are needed for this. Major challenges for a successful design of future multimedia systems that will be covered:

COMMITTEES

- ▶ Executive Committee
- ▶ Technical Programme Committee

DATE

- ▶ Past Events
- ▶ Future Events

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- ▶ ITC
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- Embedded Multimedia architectures and platforms
- Multimedia processors
- Hardware/Software co-design methodologies
- Techniques to manage timing / QoS and power

New Initiatives for DATE 04

Interactive Presentations

A new initiative at DATE 04 will be Interactive presentations - a new forum for presenters to discuss novel ideas and work in progress, where additional research and discussion are required and interaction with other researchers working in the same area would be of great value.

The Interactive presenters will be given 30 minute time slots in the programme to present their work interactively using a laptop computer, and discuss this work with their colleagues in a vivid face-to-face discussion format.

Executive Track

DATE04 will also host an Executive Track of presentations by company CEOs, CTOs, VPs and other senior executives from CAD vendors, IP suppliers, semiconductor and system houses. The Executive Track takes place on Tuesday February 17th.

Ends

Contacts:

DATE 04 Secretariat – Andrew Porter or Jeremy Kenyon, EDA Exhibitions, London
Tel: +44 20 7681 1000
date@edatld.co.uk

DATE 04 Press Chair – Fred Santamaria
fredmarcom@aol.com

DATE 04 General Chair – Prof. Joan Figueras, Universitat Politècnica de Catalunya, Spain
figueras@eel.upc.es

DATE 04 Programme Chair – Prof. Georges Gielen, Katholieke Universiteit Leuven, Belgium
georges.gielen@esat.kuleuven.ac.be

DATE 04 Publicity Chair – Prof. Bernard Courtois, TIMA-CMP, Grenoble, France
Bernard.Courtois@imag.fr

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