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Kees Goossens's BibTex Page

The BibTex entries of my papers and public technical notes are listed in reverse chronological order.

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@Article{Hansson09IETCDT,
  author = {Andreas Hansson, Maarten Wiggers, Arno Moonen, Kees Goossens, Marco Bekooij},
  title = {Enabling Application-Level Performance Guarantees in Network-Based Systems on Chip by Applying Dataflow Analysis},
  journal = {IET Computers \& Digital Techniques},
  year = {2009}
}

@Article{Hansson09TODAES,
  author = {Andreas Hansson and Kees Goossens and Marco Bekooij and Jos Huiskens},
  title = {{CoMPSoC}: A Template for Composable and Predictable Multi-Processor System on Chips},
  journal = {ACM Transactions on Design Automation of Electronic Systems},
  year = {2009}
}

@InProceedings{Abramovici08CODESIS,
  author = {Miron Abramovici and Neal Stollon and Kees Goossens and Bart Vermeulen and Adam Donlin and Jack Greenbaum and Adam Donlin},
  title = {You Can Catch More Bugs With Transaction Level Honey },
  booktitle = {CODESIS},
  pages = {121--124},
  year = 2008,
  month = oct
}

@InProceedings{Milutinovic08SOC,
  author = {Aleksandar Milutinovi{'c} and Kees Goossens and Gerard Smit},
  title = {Impact of Power-Management Granularity on The Energy-Quality Trade-off for Soft and Hard Real-Time Applications},
  booktitle = {SOC},
  OPTpages = {},
  year = 2008,
  month = nov
}

@InProceedings{Hur08VLSISOC,
  author = {Jae Young Hur and Kees Goossens and Lotfi Mhamdi},
  title = {Performance Analysis of Soft and Hard Single-Hop and Multi-Hop Circuit-Switched Interconnects for {FPGA}s},
  booktitle = {VLSISOC},
  OPTpages = {},
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@InProceedings{Akesson08RTCSA,
  author = {Benny Akesson and Liesbeth Steffens and Eelke Strooisma and Kees Goossens},
  title = {Real-Time Scheduling Using Credit-Controlled Static-Priority Arbitration},
  booktitle = {Proc. Int'l Conference on Embedded and Real-Time Computing Systems and Applications (RTCSA)},
  OPTpages = {},
  year = 2008,
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@InProceedings{vandenBerg08ETS,
  author = {van den Berg, Ardy and Ren, Pengwei and Marinissen, Erik Jan and Gaydadjiev, Georgi and Goossens, Kees},
  title = {Bandwidth Analysis for Reusing Functional Interconnect as Test Access Mechanism},
  booktitle = {Proc. European Test Symposium (ETS)},
  OPTpages = {},
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  month = may
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@InProceedings{Hansson08NOCs,
  author = {Andreas Hansson and Maarten Wiggers and Arno Moonen and Kees Goossens and Marco Bekooij},
  title = {Applying Dataflow Analysis to Dimension Buffers for Guaranteed Performance in Networks on Chip},
  booktitle = {NOCs},
  OPTpages = {},
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  month = apr
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@InProceedings{Goossens08NOCs,
  author = {Kees Goossens and Martijn Bennebroek and Hur, Jae Young and Wahlah, Muhammad Aqeel},
  title = {Hardwired Networks on Chip in {FPGA}s to unify Data and Configuration Interconnects},
  booktitle = {Proc. Int'l Symposium on Networks on Chip (NOCs)},
  year = 2008,
  month = apr
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@InProceedings{Vermeulen08NOCs,
  author = {Bart Vermeulen and Kees Goossens and Siddharth Umrani},
  title = {Debugging Distributed-Shared-Memory Communication at Multiple Granularities in Networks on Chip},
  booktitle = {Proc. Int'l Symposium on Networks on Chip (NOCs)},
  year = 2008,
  month = apr
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@InCollection{Rijpkema08SPRINGER,
  author = {E. Rijpkema and K. G. W. Goossens and A. R\{u\}dulescu and J. Dielissen and van Meerbergen, J. and P. Wielage and E. Waterlander},
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title = {Trade Offs in the Design of a Router with Both Guaranteed and Best-Effort Services for Networks on Chip},
booktitle = {Design Automation, and Test in Europe. The Most Influential Papers of 10 Years {DATE}},
publisher = {Springer},
year = 2008,
editor = {Rudy Lauwereins and Jan Madsen},
series = {Circuits \& Systems},
chapter = {2 (Networks on Chip)},
month = jan,
isbn = {978-1-4020-6487-6}
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@Article{Ciordas07JSA,
author = {C\u{a}lin Ciorda\c{s} and Andreas Hansson and Kees Goossens and Twan Basten},
title = {A Monitoring-aware Network-On-Chip Design Flow},
journal = {Journal of Systems Architecture},
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month = oct,
DOI = {http://dx.doi.org/10.1016/j.sysarc.2007.10.003}
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@InProceedings{Milutinovic07PRORISC,
author = {Aleksandar Milutinovi'\c{} and Kees Goossens and Gerard J.M. Smit},
title = {Slack Exploitation for Aggressive Dynamic Power Reduction in {SoC}},
booktitle = PRORISC,
year = 2007,
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month = nov
}

@InCollection{Moonen07SPRINGER,
author = {Arno Moonen and Chris Bartels and Marco Bekooij and van den Berg, Ren'\e{} and Harpreet Bhullar and Kees Goossens and Patrick Groeneveld},
title = {Comparison of an {Aethereal} Network on Chip and traditional interconnects - Two case studies},
booktitle = {{VLSI-SoC: Research Trends in VLSI and Systems on Chip}},
publisher = {Springer},
year = {2007},
editor = {De Micheli, Giovanni and Mir, Salvador and Reis, Ricardo},
number = {249},
series = {IFIP International Federation for Information Processing},
isbn = {978-0-387-74908-2}
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@InProceedings{Akeson07ISSS,
author = {Benny Akeson and Kees Goossens and Markus Ringhofer},
title = {Predator: A Predictable {SDRAM} Memory Controller},
booktitle = {Int'l Conf. on Hardware/Software Codesign and System Synthesis (CODES+ISSS)},
pages = {251--256},
year = 2007,
isbn = {978-1-59593-824-4},
month = oct,
publisher = {ACM},
doi = {http://doi.acm.org/10.1145/1289816.1289877},
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@InProceedings{Hansson07ISSS,
author = {Andreas Hansson and Martijn Coenen and Kees Goossens},
title = {Channel Trees: Reducing Latency by Sharing Time Slots in Time-Multiplexed Networks on Chip},
booktitle = {Int'l Conf. on Hardware/Software Codesign and System Synthesis (CODES+ISSS)},
pages = {149--154},
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month = oct,
publisher = {ACM},
doi = {http://doi.acm.org/10.1145/1289816.1289855},
annotate = {NXP-R-MS 27.342}
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@Book{Bertels07BOOK,
editor = {Korn Bertels and Sorin Cotofana and Georgi N. Gaydadjiev and Kees Goossens and Said Hamdioui and van Genderen, Arjan and Stephan Wong},
title = {The Future of Computing --- Essays in Memory of {Stamatis Vassiliadis}},
year = 2007,
month = sep,
isbn = {978-90-807959-3-0}
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@Article{Amory07IETCDT,
author = {Amory, Alexandre M. and Goossens, Kees and Marinissen, Erik Jan and Marcelo Lubaszewski and Fernando Moraes},
title = {Wrapper Design for the Reuse of a Bus, Network-on-Chip, or ther Functional Interconnect as Test Access Mechanism},
journal = {{IET} Computers \& Digital Techniques},
year = {2007},
volume = 1,
number = 3,
pages = {197--206},
month = may
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@Article{Hansson07VLSIDESIGNa,
author = {Andreas Hansson and Kees Goossens and Andrei R\u{a}dulescu},
title = {A Unified Approach to Mapping and Routing on a Network on Chip for both Best-Effort and Guaranteed Service Traffic},
journal = {{VLSI} Design},
year = {2007},
volume = {2007},
pages = {Article ID 68432, 16 pages},
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  author = {Andreas Hansson and Kees Goossens and Andrei R\{a\}dulescu},
  title = {Avoiding Message-Dependent Deadlock in Network-Based Systems on Chip},
  journal = {{VLSI} Design},
  year = {2007},
  volume = {2007},
  pages = {Article ID 95859, 10 pages},
  month = may,
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  doi = {http://dx.doi.org/10.1155/2007/95859}
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@InProceedings{Hansson07NOCS,
  author = {Andreas Hansson and Kees Goossens},
  title = {Trade-offs in the Configuration of a Network on Chip for Multiple Use-Cases},
  booktitle = {Proc. Int'l Symposium on Networks on Chip (NOCS)},
  pages = {233--242},
  year = 2007,
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  doi = {http://doi.ieeecomputersociety.org/10.1109/NOCS.2007.45}
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  author = {Kees Goossens and Bart Vermeulen and van Steeden, Remco and Martijn Bennebroek},
  title = {Transaction-Based Communication-Centric Debug},
  booktitle = {Proc. Int'l Symposium on Networks on Chip (NOCS)},
  pages = {195--206},
  year = {2007},
  month = may,
  doi = {http://doi.ieeecomputersociety.org/10.1109/NOCS.2007.46}
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@InProceedings{Vermeulen07ETS,
  author = {Bart Vermeulen and Kees Goossens and van Steeden, Remco and Martijn Bennebroek},
  title = {Communication-centric {SOC} debug using transactions},
  booktitle = {Proc. European Test Symposium (ETS)},
  pages = {69--76},
  year = 2007,
  location = {Freiburg, Germany},
  month = may,
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  isbn = {0-7695-2827-9}
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@Misc{Ren07DATEposter,
  author = {Ren, Pengwei and Amory, Alexandre M. and Marinissen, Erik Jan and Kees Goossens and Goel, Sandeep K. and Gaydadjiev, Georgi N. and Mar},
  title = {Test Wrapper Design that Allows a Core to be Tested Via a Network-on-Chip or Other Functional Interconnect},
  year = 2007,
  month = apr,
  howpublished = {Diagnostic Services in Networks-on-Chips workshop at Design, Automation and Test in Europe Conference and Exhibition (DATE)}
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  author = {Hansson, Andreas and Coenen, Martijn and Goossens, Kees},
  title = {Undisrupted Quality-Of-Service during Reconfiguration of Multiple Applications in Networks on Chip},
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  year = 2007,
  month = apr,
  pages = {954--959},
  doi = {http://dx.doi.org/10.1109/DATE.2007.364416}
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@InProceedings{vandenBrand07DATE,
  author = {van den Brand, Jan Willem and Ciorda\{c\}, C\{u\}lin and Basten, Twan and Goossens, Kees},
  title = {Congestion-Controlled Best-Effort Communication for Networks-on-Chip},
  booktitle = "Proc. Design, Automation and Test in Europe Conference and Exhibition (DATE)",
  year = 2007,
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  pages = {948-953},
  doi = {http://doi.ieeecomputersociety.org/10.1109/DATE.2007.364415}
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@InProceedings{Coenen06ISSS,
  author = {Martijn Coenen and Srinivasan Murali and Andrei R\{a\}dulescu and Kees Goossens and De Micheli, Giovanni},
  title = {A buffer-sizing Algorithm for Networks on Chip using {TDMA} and credit-based end-to-end Flow Control},
  booktitle = "Int'l Conf. on Hardware/Software Codesign and System Synthesis (CODES+ISSS)",
  year = 2006,
  isbn = {1-59593-370-0},
  pages = {130--135},
  location = {Seoul, Korea},
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  address = {New York, NY, USA}
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  author = {C\{u\}lin Ciorda\{c\} and Kees Goossens and Twan Basten and Andrei R\{a\}dulescu and Andre Boon},
  title = {Transaction Monitoring in Networks on Chip: The On-Chip Run-Time Perspective},
  booktitle = "Proc. Symposium on Industrial Embedded Systems (IES)",
  pages = {1--10},
  year = 2006,
  address = {Antibes, France},
  month = oct,
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publisher = {IEEE},
doi = {http://dx.doi.org/10.1109/IES.2006.357464}
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@InProceedings{Bartels06VLSISOC,
author = {Chris Bartels and Jos Huisken and Kees Goossens and Patrick Groeneveld and van Meerbergen, Jef},
title = {Comparison of An {\AE}thereal Network on Chip and A Traditional Interconnect for A Multi-Processor {DVB-T} System on Chip},
booktitle = "Proc. IFIP Int'l Conference on Very Large Scale Integration (VLSI-Soc)",
pages = {80--85},
year = 2006,
month = oct,
doi = {http://dx.doi.org/10.1109/VLSISOC.2006.313208}
}

@InProceedings{Ciordas06DSD,
author = {C\u{a}lin Ciorda\c{s} and Andreas Hansson and Kees Goossens and Twan Basten},
title = {A Monitoring-aware {NoC} Design Flow},
booktitle = "Proc. Euromicro Symposium on Digital System Design",
pages = {97--104},
year = 2006,
month = aug,
publisher = {IEEE},
doi = {http://dx.doi.org/10.1109/DSD.2006.11}
}

@InCollection{Cidon06MORGANKAUFMANN,
title = {Network and Transport Layers in Networks on Chip},
booktitle = {Networks on Chips: Technology and Tools},
author = {Israel Cidon and Kees Goossens},
editor = {De Micheli, Giovanni and Benini, Luca},
publisher = {Morgan Kaufmann},
year = 2006,
pages = {147--202},
series = {The Morgan Kaufmann Series in Systems on Silicon},
chapter = {5},
month = jul
}

@inproceedings{Pastrnak06ISCE,
author = {Milan Pastrnak and de With, Peter H.N. and C\u{a}lin Ciorda\c{s} and van Meerbergen, Jef and Kees Goossens},
title = {Mixed adaptation and fixed-reservation {QoS} for improving Picture Quality and Resource Usage of Multimedia {NoC} Chips},
booktitle = "Proc. Int'l Symposium on Consumer Electronics (ISCE)",
year = 2006,
month = jun,
pages = {1--6},
doi = {http://dx.doi.org/10.1109/ISCE.2006.1689507}
}

@Proceedings{Goossens06ACSD,
title = {Sixth International Conference on Applications of Concurrency to System Design (ACSD 2006)},
year = 2006,
editor = {Kees Goossens and Laure Petrucci},
address = {Turku, Finland},
month = jun,
publisher = {IEEE}
}

@InProceedings{Amory06ETS,
author = {Amory, Alexandre M. and Goossens, Kees and Marinissen, Erik Jan and Marcelo Lubaszewski and Fernando Moraes},
title = {Wrapper Design for the Reuse of Networks-on-Chip as Test Access Mechanism},
booktitle = "Proc. European Test Symposium (ETS)",
pages = {213--218},
year = 2006,
month = may,
isbn = {0-7695-2566-0},
doi = {http://dx.doi.org/10.1109/ETS.2006.48},
publisher = {IEEE Computer Society},
address = {Washington, DC, USA}
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@InProceedings{Ciordas06ISCAS,
author = {C\u{a}lin Ciorda\c{s} and Kees Goossens and Andrei R\u{a}dulescu and Twan Basten},
title = {{NoC} Monitoring: Impact on the Design Flow},
booktitle = "Proc. Int'l Symposium on Circuits and Systems (ISCAS)",
pages = {1981--1984},
month = may,
year = 2006,
doi = {http://dx.doi.org/10.1109/ISCAS.2006.1693001}
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@Misc{Goossens06DATEposter,
author = {Goossens, Kees},
title = {Multiple Use-Cases for A Network-on-Chip Design Flow},
year = 2006,
month = mar,
howpublished = {Future Interconnects and Network on Chip workshop at Design, Automation and Test in Europe Conference and Exhibition (DATE)}
}

@Misc{Ru06DATEposter,
author = {Ru, Xiao and Dielissen, John and Svensson, Christer and Goossens, Kees},
title = {Synchronous Latency-Insensitive Design in {\AE}thereal NoC},
year = 2006,
month = mar,
howpublished = {Future Interconnects and Network on Chip workshop at Design, Automation and Test in Europe Conference and Exhibition (DATE)}
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@InProceedings{Steenhof06DATE,
author = {Frits Steenhof and Harry Duque and Bj\u{orn} Nilsson and Kees Goossens and Peset Llopis, Rafael},

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title = {Networks on Chips for High-End Consumer-Electronics {TV} System Architectures},
booktitle = "Proc. Design, Automation and Test in Europe Conference and Exhibition (DATE)",
pages = {148--153},
year = 2006,
month = mar,
volume = {2},
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location = {Munich, Germany},
publisher = {European Design and Automation Association},
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@InProceedings{Murali06DATE,
author = {Murali, Srinivasan and Coenen, Martijn and R\{u\{a\}dulescu, Andrei and Goossens, Kees and De Micheli, Giovanni},
title = {A Methodology for Mapping Multiple Use-Cases on to Networks on Chip},
booktitle = "Proc. Design, Automation and Test in Europe Conference and Exhibition (DATE)",
pages = {118--123},
year = 2006,
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@InProceedings{Murali06ASPDAC,
author = {Murali, Srinivasan and Coenen, Martijn and R\{u\{a\}dulescu, Andrei and Goossens, Kees and De Micheli, Giovanni},
title = {Mapping and Configuration Methods for Multi-Use-Case Networks on Chips},
booktitle = "Proc. Design Automation Conference. Asia and South Pacific (ASPDAC)",
pages = {146--151},
year = 2006,
month = jan,
isbn = {0-7803-9451-8},
location = {Yokohama, Japan},
doi = {http://doi.acm.org/10.1145/1118299.1118344},
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}

@Article{Ciordas05TODAES,
author = {C\{u\{a\}lin Ciorda\{c\{s\} and Twan Basten and Andrei R\{u\{a\}dulescu and Kees Goossens and van Meerbergen, Jef},
title = {An Event-based Monitoring Service for Networks on Chip},
journal = {ACM Transactions on Design Automation of Electronic Systems},
year = {2005},
volume = {10},
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pages = {702--723},
month = oct,
issn = {1084-4309},
doi = {http://doi.acm.org/10.1145/1109118.1109126}
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@Article{Goossens05DT,
author = {Goossens, Kees and Dielissen, John and R\{u\{a\}dulescu, Andrei},
title = {The {\AE}thereal Network on Chip: Concepts, Architectures, and Implementations},
journal = {IEEE Design and Test of Computers},
year = {2005},
volume = {22},
number = {5},
pages = {414--421},
month = {Sept-Oct},
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doi = {http://dx.doi.org/10.1109/MDT.2005.99},
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@InProceedings{Hansson05ISSS,
author = {Hansson, Andreas and Goossens, Kees and R\{u\{a\}dulescu, Andrei},
title = {A Unified Approach to Constrained Mapping and Routing on Network-on-Chip Architectures},
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@InProceedings{Goossens05ACACES,
author = {Goossens, Kees},
title = {Networks on Chip for Consumer Electronics},
booktitle = "Proc. Int'l Summer School on Advanced Computer Architecture and Compilation for Embedded Systems (ACACES)",
pages = {2272-230},
year = {2005},
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@InProceedings{Goossens05ACSD,
author = {Goossens, Kees},
title = {Formal Methods for Networks on Chips},
booktitle = "Proc. Int'l Conference on Application of Concurrency to System Design (ACSD)",
pages = {188--189},
year = {2005},
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@InProceedings{Gebremichael05CHARME,
author = {Biniam Gebremichael and Frits Vaandrager and Miaomiao Zhang and Kees Goossens and Edwin Rijpkema and Andrei R\u{a}dulescu},
title = {Deadlock Prevention in the {\AE}thereal Protocol},
booktitle = "Proc. Working Conference on Correct Hardware Design and Verification Methods (CHARME)",
editor = "Dominique Borriane and Wolfgang Paul",
series = LNCS,
volume = 3725,
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month = oct
}

@InCollection{Gangwal05SPRINGER,
author = {Gangwal, Om Prakash and R\u{a}dulescu, Andrei and Goossens, Kees and Gonz\'alez Pestana, Santiago and Edwin Rijpkema},
title = {Building Predictable Systems on Chip: An Analysis of Guaranteed Communication in the {\AE}thereal Network on Chip},
booktitle = {Dynamic and Robust Streaming In And Between Connected Consumer-Electronics Devices},
pages = {1--36},
publisher = {Springer},
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@InCollection{Goossens05SPRINGER,
author = {Goossens, Kees and Gonz\'alez Pestana, Santiago and Dielissen, John and Gangwal, Om Prakash and van Meerbergen, Jef and R\u{a}dulescu, And},
title = {Service-Based Design of Systems on Chip and Networks on Chip},
booktitle = {Dynamic and Robust Streaming In And Between Connected Consumer-Electronics Devices},
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@InProceedings{Goossens05DATE,
author = {Goossens, Kees and Dielissen, John and Gangwal, Om Prakash and Gonz\'alez Pestana, Santiago and R\u{a}dulescu, Andrei and Rijpkema, Edwin},
title = {A Design Flow for Application-Specific Networks on Chip with Guaranteed Performance to Accelerate {SOC} Design and Verification},
booktitle = "Proc. Design, Automation and Test in Europe Conference and Exhibition (DATE)",
pages = {1182--1187},
year = 2005,
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doi = {http://dx.doi.org/10.1109/DATE.2005.11},
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@Article{Radulescu05TCAD,
author = {R\u{a}dulescu, Andrei and Dielissen, John and Gonz\'alez Pestana, Santiago and Gangwal, Om Prakash and Rijpkema, Edwin and Wielage, Paul and},
journal = {{IEEE} Transactions on {CAD} of Integrated Circuits and Systems},
year = {2005},
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doi = {http://dx.doi.org/10.1109/TCAD.2004.839493(410) 24}
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author = {C\u{a}lin Ciorda\c{s} and Twan Basten and Andrei R\u{a}dulescu and Kees Goossens and van Meerbergen, Jef},
title = {An Efficient On-Chip Network Interface Offering Guaranteed Services, Shared-Memory Abstraction, and Flexible Network Programming},
booktitle = "Proc. Workshop on High-Level Design Validation and Test (HLDVT)",
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pages = {878--883},
year = 2004,
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doi = {http://dx.doi.org/10.1109/DATE.2004.1268998}
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author = {Gonz\'alez Pestana, Santiago and Rijpkema, Edwin and R\u{a}dulescu, Andrei and Goossens, Kees and Gangwal, Om Prakash},
title = {Cost-Performance Trade-offs in Networks on Chip: A Simulation-Based Approach},
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