

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

QUALCOMM INCORPORATED
Petitioner

v.

NETWORK SYSTEM TECHNOLOGIES, LLC,
Patent Owner

Case: IPR2024-00343
U.S. Patent No. 7,594,052

**PETITIONER QUALCOMM INCORPORATED'S
UPDATED EXHIBIT LIST**

PETITIONER’S EXHIBIT LIST

Exhibit No.	Description
1001	U.S. Patent No. 7,594,052
1002	File History of U.S. Patent No. 7,594,052
1003	Declaration of Dr. Marwan Hassoun
1004	Curriculum Vitae of Dr. Marwan Hassoun
1005	A. Rădulescu, et al. " <i>An Efficient On-Chip Network Interface Offering Guaranteed Services, Shared-Memory Abstraction, and Flexible Network Configuration</i> ," Proceedings of the Design, Automation and Test in Europe Conference and Exhibition, 2004 (“Rădulescu2004”)
1006	Open Core Protocol Specification, Release 1.0, OCP-IP Association (2001) (“OCP”)
1007	U.S. Patent Publication No. 2002/0129173 A1 (“Weber”)
1008	U.S. Patent No. 5,948,089 (“Wingard”)
1009	Networks on Chip; Axel Jantsch, Hannu Tenhunen (Eds.). Kluwer Academic Publishers, Boston (2003) (“Jantsch”)
1010	A. Rădulescu, et al. " <i>Communication Services for Networks on Chip</i> ," Philips Research Laboratories, Eindhoven, The Netherlands, SAMOS, II(), 275–299 (2002) (“Rădulescu2000”)
1011	J. Dielissen, et al. " <i>Concepts and Implementation of the Philips Network-on-Chip</i> ," Philips Research Laboratories, Eindhoven, The Netherlands (2003) (“Dielissen”)
1012	Declaration of Nikil Dutt, Ph.D. Under 37 C.F.R. §1.68
1013	Affidavit of Nathaniel E. Frank-White, Records Request Processor at the Internet Archive
1014	Declaration of Sylvia Hall-Ellis, Ph.D.
1015	Plaintiff Network System Technologies, LLC’s Responses to Qualcomm’s First Set of Requests for Admission (Nos. 1-9), <i>Network System Technologies, LLC v. Qualcomm Inc. et al.</i> , 1:22-cv-01331 (W.D. Tex.)

Exhibit No.	Description
1016	Defendant's Opening Claim Construction Brief, <i>Network System Technologies, LLC v. Qualcomm Inc. et al.</i> , 1:22-cv-01331 (W.D. Tex.)
1017	Network System Technologies, LLC's Response to Defendants' Joint Opening Claim Construction Brief, <i>Network System Technologies, LLC v. Qualcomm Inc. et al.</i> , 1:22-cv-01331 (W.D. Tex.)
1018	Declaration of Gordon Macpherson, Director, Board Governance & Policy Development of The Institute of Electrical and Electronics Engineers, Incorporated ("IEEE")
1019	Declaration of Nathaniel E Frank-White, Records Request Processor at the Internet Archive
1020	U.S. Patent No. 7,366,818 ("Rădulescu")
1021	P. Guerrier et al., " <i>A generic architecture for on-chip packet-switched interconnections</i> ," Proceedings Design, Automation and Test in Europe Conference and Exhibition 2000 (2000)
1022	William J. Dally et al., " <i>Route Packets, Not Wires: On-Chip Interconnection Networks</i> ," Proceedings – Design Automation Conference (2001)
1023	Shashi Kumar et al., " <i>A Network on Chip Architecture and Design Methodology</i> ," IEEE Computer Society Annual Symposium (2002)
1024	José Duato et al., <i>Interconnection Networks, An Engineering Approach</i> . Revised Printing (2003)
1025	Luca Benini et al., " <i>Powering Networks on Chips, Energy-efficient and reliable interconnect designs for SoCs. Proceedings of the International Symposium on Systems Synthesis</i> " (2001)
1026	Andrew S. Tanenbaum, <i>Computer Networks</i> , Third Edition (1996)
1027	Network System Technologies, LLC's Sur-Reply Claim Construction Brief, <i>Network System Technologies, LLC v. Qualcomm Inc. et al.</i> , 1:22-cv-01331 (W.D. Tex.)
1028	Declaration of Peter M. Kohlhepp in Support of Petitioner's Unopposed Motion for <i>Pro Hac Vice</i> Admission
1029	[intentionally omitted]

Exhibit No.	Description
1030	Reply Declaration of Dr. Marwan Hassoun
1031	Welcome to DATE '04 message
1032	Dr. Luca Benini homepage
1033	DATE '04 Call for Papers
1034	Third Amended Initial Disclosures by Network System Technologies, LLC, <i>Network System Technologies, LLC v. Qualcomm Inc. et al.</i> , 1:22-cv-01331 (W.D. Tex.)
1035	IEEE Xplore Conference Proceedings Search
1036	DATE Conference press release
1037	[Intentionally omitted]
1038	Reply Declaration of Dr. Nikil Dutt
1039	Pasricha et al. "Fast Exploration of Bus-based On-chip Communication Architectures." CODES+ISSS '04: Proceedings of the 2nd IEEE/ACM/IFIP International Conference on Hardware/Software Codesign and System Synthesis, September 8-10, 2004
1040	DATE Press Releases webpage and DATE06 PRESS SLIDES
1041–1049	[Intentionally Omitted]
1050	Deposition Transcript of Nader Bagherzadeh, dated December 12, 2024
1051	Horak, Ray (Ed.). <i>Webster's New World Telecom Dictionary</i> (excerpts)
1052	<i>The Authoritative Dictionary of IEE Standard Terms</i> . Published by Standards Information Network IEEE Press, 7 th ed. (excerpts)
1053	Tadaaki Yamauchi, et al., "The hierarchical multi-bank DRAM: a high-performance architecture for memory integrated with processors," <i>Proceedings Seventeenth Conference on Advanced Research in VLSI</i> , Ann Arbor, MI, USA, 1997, pp. 303-319
1054	TriCore™ 1.3 32-bit Unified Processor Core, <i>Architecture Overview Handbook, V1.3.3</i> (May 2002)
1055	TMS320C6000 Technical Brief (February 1999)

Exhibit No.	Description
1056	Harry K. Charles, et al., “Very Large Scale Integrated Circuitry,” <i>Johns Hopkins APL Technical Digest</i> , Volume 7, Number 3 (1986)
1057	F. Vahid and T. Givargis, “Platform tuning for embedded systems design” <i>Computer</i> , vol. 34, no. 02, pp. 112-114, (March 2001)
1058	The Microprocessor Today, <i>IEEE Micro</i> , pp. 32-44 (December 1996)
1059	U.S. Copyright Office Public Catalog entry for 2004 Design, Automation and Test in Europe conference and exhibition: proceedings
1060	Declaration of Sage Kruse
1061	IEEE Conference Proceedings “Metrics” (compilation)
1062	Kees Goossens’s Publications Page
1063	Kees Goossens’s BibTex Page
1064	Kees Goossens’s Research Page
1065	Design, Automation and Test in Europe DATE ’04, Proceedings (DATE Conference Archives, excerpts)
1066	Design, Automation and Test in Europe DATE ’04, Proceedings Volume II (Library of Congress copy, excerpts)
1067	Design, Automation and Test in Europe DATE ’04, Proceedings (Table of Contents, IEEE Computer Society Digital Library)
1068	Declaration of Sylvia D. Hall-Ellis, Ph.D.
1069	IEEE Xplore Celebrates 15 years of Information Driving Innovation (April 24, 2005)
1070	Library of Congress Catalog Item Information, Design, Automation and Test in Europe Conference and Exhibition 2004: proceedings
1071	Claim Construction Order, <i>Network Sys. Techs., LLC v. Qualcomm Incorporated</i> , No. 1:22-cv-01331-DAE, Dkt. 296 (W.D. Tex. Mar. 31, 2025)

Respectfully submitted,

Dated: April 3, 2025

By: /Peter M. Kohlhepp/
J. Derek Vandenburg (Reg. No. 32,179)
Peter M. Kohlhepp (*pro hac vice*)
Aaron W. Pederson (Reg. No. 58,607)
Nathan D. Louwagie (Reg. No. 70,057)
Ryan D. Rypka (Reg. No. 79,451)
Carlson Caspers Vandenburg
& Lindquist, P.A.
225 South Sixth Street, Suite 4200
Minneapolis, MN 55402
Telephone: (612) 436-9600
Facsimile: (612) 436-9605
dvandenburg@carlsoncaspers.com
pkohlhepp@carlsoncaspers.com
apederson@carlsoncaspers.com
nlouwagie@carlsoncaspers.com
rrypka@carlsoncaspers.com

*Counsel for Petitioner Qualcomm
Incorporated*

CERTIFICATE OF SERVICE

Pursuant to 37 C.F.R. § 42.6(e) and the agreement of the parties, the undersigned certifies that on April 3, 2025, a true and correct copy of the foregoing Petitioner Qualcomm Incorporated's Updated Exhibit List was served via electronic mail upon the following:

Jennifer Hayes (Reg. No. 50,845)
Nixon Peabody LLP
300 South Grand Ave., Suite 4100
Los Angeles, CA 90071-3151
Phone: 213-629-6179
Facsimile: 213-629-6001
jenhayes@nixonpeabody.com

Hang Zheng (Reg. No. 73,464)
Nixon Peabody LLP
799 9th St. NW, Suite 500
Washington, DC 20001-5327
Phone: 202-585-8307
Facsimile: 833-739-7878
hzheng@nixonpeabody.com

Daniel Stringfield (Reg. No. 68,677)
Nixon Peabody LLP
70 West Madison, Suite 5200
Chicago, IL 60602-4224
Phone: 312-977-4130
Facsimile: 833-923-0056
dstringfield@nixonpeabody.com

Peter Krusiewicz (*pro hac vice*)
Nixon Peabody LLP
70 West Madison, Suite 5200
Chicago, IL 60602-4224
Phone: 312-977-4473
Facsimile: 312-977-4405
pkrusiewicz@nixonpeabody.com

Angelo J. Christopher (Reg. No. 81,594)
Nixon Peabody LLP
70 West Madison, Suite 5200
Chicago, IL 60602-4224
Phone: 312-977-4391
Facsimile: 312-977-4405
achristopher@nixonpeabody.com

/Peter M. Kohlhepp/
Peter M. Kohlhepp