IN THE UNITED STATES DISTRICT COURT
FOR THE WESTERN DISTRICT OF TEXAS
AUSTIN DIVISION

ICPILLAR LLC,	§	
	§	
	§	
Plaintiff,	§	**Civil Action No. 1:23-CV-00282-ADA**
	§	
v.	§	
	§	**JURY TRIAL DEMANDED**
ARM LTD and ARM, INC.,	§	
	§	
	§	
Defendants.	§	

CLAIM CONSTRUCTION ORDER

Before the Court are the parties' claim construction briefs: Defendants' Opening Claim Construction Brief (ECF No. 32), Plaintiff's Responsive Claim Construction Brief (ECF No. 33), Defendants' Reply Claim Construction Brief (ECF No. 36), Plaintiff's Sur-Reply Claim Construction Brief (ECF No. 38), and the Joint Claim Construction Statement (ECF No. 39). On February 26, 2024, the Court provided the parties with its Preliminary Claim Constructions, and on February 27, 2024, the Court held a *Markman* hearing. The Court issues this Order to memorialize the Court's final claim construction rulings for the parties, and to inform the parties that the Court plans to issue a more-detailed Order explaining its analysis in due course. The deadline to file any objections to the undersigned's claim construction rulings (pursuant to Federal Rules of Civil Procedure 59 and 72) do not need to be filed until 14 days after that more fulsome Order is entered upon the docket.

SIGNED this 26th day of March, 2024.

ALAN D ALBRIGHT
UNITED STATES DISTRICT JUDGE

I. DISPUTED CONSTRUCTIONS:

Claim Term	Plaintiff's Proposal	Defendants' Proposal	Court's Preliminary Construction
"categorized input and output characteristics of [a/an] [electronic] device" '899 Patent Claims 1, 8 (Jointly identified)	"categorized characteristics or qualities of inputs and outputs of an electronic device" '899 Patent Claim 1 "categorized characteristics or qualities of inputs and outputs of a device" '899 Patent Claim 8	"input and output characteristics of [an/the] electronic device selected from a hierarchal classification database"	Plain and ordinary meaning
"a memory of a processor programmed by software commands to design a hardware integrated circuit and associated operating software" '899 Patent, Claim 1 '657 Patent, Claim 1 (Plaintiff identified)	"one or more instances of memory associated with one or more processors that are programmed by software commands to design a hardware integrated circuit and associated operating software"	Plain and ordinary meaning	Plain and ordinary meaning
"control type parameter associated with an electronic device" / "control type parameter" '657 Patent, Claims 1,9 (Jointly identified)	"one or more parameters associated with an electronic device relating to the control of the electronic device"	Plain and ordinary meaning	Plain and ordinary meaning

"display type parameter associated with the electronic device" / "display parameter" '657 Patent, Claims 1,9 (Jointly identified)	"one or more parameters associated with an electronic device relating to a display of the electronic device"	Plain and ordinary meaning	Plain and ordinary meaning
"input type parameter for the electronic device" / "input type parameter" '657 Patent, Claims 1, 9 (Jointly identified)	"one or more parameters for the electronic device relating to an input of the electronic device"	Plain and ordinary meaning	Plain and ordinary meaning
"output type parameter for the electronic device" / "output type parameter" '657 Patent, Claims 1, 9 (Jointly identified)	"one or more parameters for the electronic device relating to an output of the electronic device"	Plain and ordinary meaning	Plain and ordinary meaning
"fabless development kit" '657 Patent, Claim 8 (Plaintiff identified)	"a collection of software applications or programs for creating an integrated circuit design and its software separate from the manufacture of the integrated circuit"	Plain and ordinary meaning	Plain and ordinary meaning