

From: Benzler Ulrich (FV/SLM) * <Ulrich.Benzler@de.bosch.com>
Sent: Monday, September 30, 2002 2:32 PM
To: 'Kazushi SATO'; Shun-ichi Sekiguchi
Cc: Luthra Ajay-MGI1164; 'Jani.Lainema@nokia.com'; jvt-experts@mail.imtc.org
Subject: [jvt-experts] RE: Special position + MC memory analysis adho c group

Dear Ajay, experts,

I also agree with Sekiguchi-san (ABT OFF), and also with the proposal to keep full 30 Hz for the B-frame cases.

Regards,

Ulrich

P.S.: The search range of 32 refers, to my understanding, to the config file parameter SearchRange (so this should mean +-32, I think).

> -----Original Message-----

> From: Kazushi SATO [mailto:kazushi@av.crl.sony.co.jp]
> Sent: Monday, September 30, 2002 4:09 AM
> To: Shun-ichi Sekiguchi
> Cc: Luthra Ajay-MGI1164; 'Jani.Lainema@nokia.com';
> Ulrich.Benzler@de.bosch.com; jvt-experts@mail.imtc.org
> Subject: Re: [jvt-experts] RE: Special position + MC memory analysis
> adho c group

>

>

> I agree with Sekiguchi-san: I prefer simulation with ABT off.

> Kazushi

>

> > Dear Ajay, experts,

> >

> > Luthra Ajay-MGI1164 <aluthra@motorola.com> wrote:

> >

> > > Yes, let us keep the original common conditions and for

> B-frames run at the full (30/25 Hz) rate. With regard to ABT, as ABT

> is in a way tied to MC block sizes, it may be good to know if

> different MC approaches will have any impact on ABT's performance and

> keep ABT ON.

> >

> > Do you mean we shall turn on ABT for all cases?

> > I don't see any reason why we need the evaluation of ABT for

> > QCIF/CIF cases. If we need such evaluation for HHR, I would like to

> > propose only one "mandatory" ABT simulation.(for example, only for

> > "Bus using B-frames")

> >

> > Best regards,

> > Shun-ichi Sekiguchi

>>
>>>
>>> Ajay
>>>
>>>> -----Original Message-----
>>>> From: Jani.Lainema@nokia.com [mailto:Jani.Lainema@nokia.com]
>>>> Sent: Friday, September 27, 2002 11:39 AM
>>>> To: Ulrich.Benzler@de.bosch.com; Ajay Luthra;
>>>> jvt-experts@mail.imtc.org
>>>> Subject: RE: [jvt-experts] RE: Special position + MC
> memory analysis
>>>> adho c group
>>>>
>>>>
>>>> Hi Ulrich, Ajay, All,
>>>>
>>>> I too like the idea of switching off the ABT for this
>>>> experiment. For the frame rate issue I would propose to keep the
>>>> original common condition frame rates for the IPPP cases.
>>>> For IBBP we could just run everything at 30 Hz. I believe this
>>>> is more realistic B-picture usage scenario.
>>>>
>>>> And one question for clarification: Does the 32x32 search range
>>>> mean +-16 search range, or +-32 as in common conditions?
>>>>
>>>> BR,
>>>>
>>>> Jani
>>>>
>>>>> -----Original Message-----
>>>>> From: ext Benzler Ulrich (FV/SLM) *
>>>>> [mailto:Ulrich.Benzler@de.bosch.com]
>>>>> Sent: Monday, September 23, 2002 11:20 AM
>>>>> To: 'Luthra Ajay-MGI1164'; 'jvt-experts@mail.imtc.org'
>>>>> Subject: [jvt-experts] RE: Special position + MC memory
>>>>> analysis adho c group
>>>>>
>>>>>
>>>>> ...and I would propose to switch off ABT for the sake of
>>>>> simulation time (it shouldn't make a difference for the things
>>>>> we want to show).
>>>>>
>>>>> Best regards,
>>>>>
>>>>> Ulrich
>>>>>
>>>>>> -----Original Message-----
>>>>>> From: Benzler Ulrich (FV/SLM) *
>>>>>> [mailto:Ulrich.Benzler@de.bosch.com]
>>>>>> Sent: Monday, September 23, 2002 5:51 PM
>>>>>> To: 'Luthra Ajay-MGI1164'; 'jvt-experts@mail.imtc.org'
>>>>>> Subject: [jvt-experts] RE: Special position + MC memory

>>>>> analysis adho c group
>>>>>
>>>>>
>>>>> Dear Ajay, colleagues,
>>>>>
>>>>> comparing the test conditions with VCEG-N81, I found that
>>>>> there was a
>>>>> frame_skip=2 for the QCIF's and frame_skip=1 for
> silent_qcif
>>>>> and paris_cif.
>>>>> Sorry I brought this up at such a late stage...how does the
>>>>> group feel about this?
>>>>>
>>>>> Best regards,
>>>>>
>>>>> Ulrich
>>>>>
>>>>> P.S.: Any information about the display equipment we will
>>>>> have in Geneva?
>>>>>
>>>>>> -----Original Message-----
>>>>>> From: Luthra Ajay-MGI1164 [mailto:aluthra@motorola.com]
>>>>>> Sent: Saturday, September 21, 2002 12:00 AM
>>>>>> To: 'jvt-experts@mail.imtc.org'
>>>>>> Subject: [jvt-experts] RE: Special position + MC memory
>>>>>> analysis adho c group
>>>>>>
>>>>>>
>>>>>> Dear Experts,
>>>>>> Attached are the updated test condition files. Also,
>>>>>> experimenters are welcome to do more tests than
> specified here.
>>>>>>
>>>>>> Experiments related to D-134 are done by the
> Level Constraint
>>>>>> ad-hoc group and not this group.
>>>>>>
>>>>>> Ajay
>>>>>>
>>>>>>
>>>>>>> -----Original Message-----
>>>>>>> From: Luthra Ajay-MGI1164
>>>>>>> Sent: Thursday, September 19, 2002 1:45 PM
>>>>>>> To: jvt-experts@mail.imtc.org
>>>>>>> Subject: RE: [jvt-experts] RE: Special position + MC
>>>>>>> memory analysis
>>>>>>> adhoc group
>>>>>>>
>>>>>>>
>>>>>>> OK. I have not heard any objections. So, let us
> go with the
>>>>>>> HHR testing method below. I will send the updated Test

>>>>>>> Condition files tomorrow. Thanks.

>>>>>>>

>>>>>>> Regards,

>>>>>>>

>>>>>>> Ajay

>>>>>>>

>>>>>>>> -----Original Message-----

>>>>>>>> From: Jani.Lainema@nokia.com

> [mailto:Jani.Lainema@nokia.com]

>>>>>>>> Sent: Wednesday, September 18, 2002 7:41 AM

>>>>>>>> To: Ajay Luthra; jvt-experts@mail.imtc.org

>>>>>>>> Subject: RE: [jvt-experts] RE: Special position + MC

>>>>>> memory analysis

>>>>>>> adhoc group

>>>>>>>>

>>>>>>>>

>>>>>>>> Hi Ajay, All,

>>>>>>>>

>>>>>>>> Proposal (i) sounds good. What sounds even

> better is that

>>>>>>>> someone volunteers to create and distribute the

>>>> sequences so

>>>>>>>> that we all end up with the same material... ;)

>>>>>>>>

>>>>>>>> Best Regards,

>>>>>>>>

>>>>>>>> Jani

>>>>>>>>

>>>>>>>>> -----Original Message-----

>>>>>>>>> From: ext Luthra Ajay-MGI1164

>>>>> [mailto:aluthra@motorola.com]

>>>>>>>>> Sent: Tuesday, September 17, 2002 8:44 PM

>>>>>>>>> To: 'Jvt-Experts \ (E-mail\)'

>>>>>>>>> Subject: [jvt-experts] RE: Special position

> + MC memory

>>>>>>>>> analysis adhoc

>>>>>>>>> group

>>>>>>>>>

>>>>>>>>>

>>>>>>>>> As I was afraid of, issues related to HHR testing

>>>>> conditions

>>>>>>>>> are becoming unnecessarily more and more complex

>>>>> ... sigh :-(.

>>>>>>>>>

>>>>>>>>> Let me reset the HHR testing condition. There are

>>>>> more than

>>>>>>>>> one ways to do it. Let me suggest that we go back

>>>>> to our good

>>>>>>>>> old side by side comparison method for the work of

>>>>> this adhoc

>>>>>>>>> group. All the experimenters understand and know

>>>>> how to do

>>>>>>>> that. Plus, for CIF and QCIF we are going to do
>>>>> side by side
>>>>>>>> testing only. So, that should be ok for HHR also.
>>>>>>>>
>>>>>>>> With regard to which 352 pixels to pick
>>>> horizontally, there
>>>>>>>> were three possibilities proposed:
>>>>>>>>
>>>>>>>> (i) Pick the right most 352 pixels or (ii) take the
>>>>> left most
>>>>>>>> 704 pixels (1,2,3 ... 704) and throw away
> even number of
>>>>>>>> pixels or (iii) use MPEG-2 test model method.
>>>>>>>>
>>>>>>>> They all have their pluses and minuses that I am
>>>>> sure you all
>>>>>>>> are familiar with. Couple of experimenters
> (Ulrich and
>>>>>>>> Sekiguchi-san) for the MC memory Analysis part are
>>>>> proposing
>>>>>>>> the first one above. I would suggest that
> we accept their
>>>>>>>> proposal. Any comments?
>>>>>>>>
>>>>>>>> With regards,
>>>>>>>> Ajay
>>>>>>>>
>>>>>>>>
>>>>>>>>
>>>>>>>>
>>>>>>>> ---
>>>>>>>> The use of the jvt-experts list is intended only for the
>>>>>>>> video coding standardization work conducted by the Joint
>>>>>>>> Video Team (JVT) of ISO/IEC JTC1/SC29/WG11 (MPEG) and ITU-T
>>>>>>>> SG16 Q.6 (VCEG).
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>>>>>>>> ulrich.benzler@de.bosch.com
>>>>>>>> To unsubscribe send a blank email to %%email.unsub%%
>>>>>>>>
>>>>>>>>
>>>>>>>> ---
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>>>>>>>> To unsubscribe send a blank email to %%email.unsub%%
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>>>>>>>>

>>>
>>> ---
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> video coding standardization work conducted by the Joint Video Team
> (JVT) of ISO/IEC JTC1/SC29/WG11 (MPEG) and ITU-T
> SG16 Q.6 (VCEG).
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> SG16 Q.6 (VCEG).
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>
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> *****
>
>

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You are currently subscribed to jvt-experts as: suehring@hhi.de To unsubscribe send a blank email to leave-jvt-experts-12909W@mail.imtc.org