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Executed at Los Angeles, CA, on October 19, 2022.

*Alyssa G. Resnick*

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Alyssa G. Resnick



# Silicon Processing

for the VLSI Era  
Volume 1 - Process Technology  
Second Edition

S. Wolf and R.N. Tauber

**SILICON PROCESSING  
FOR  
THE VLSI ERA**

**VOLUME 1:  
PROCESS TECHNOLOGY  
Second Edition**

**STANLEY WOLF Ph.D.  
RICHARD N. TAUBER Ph.D.**

**LATTICE PRESS**

**Sunset Beach, California**

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**Published by:**

Lattice Press,  
Post Office Box 340  
Sunset Beach, California 90742, U.S.A.

Cover design by Roy Montibon, Visionary Art Resources, Inc., Santa Ana, CA.

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Library of Congress Cataloging in Publication Data  
Wolf, Stanley

Silicon Processing for the VLSI Era  
Volume 1 : Process Integration

Includes Index

1. Integrated circuits-Very large scale integration. 2. Silicon. I. Title

86-081923

ISBN 0-961672-4-5

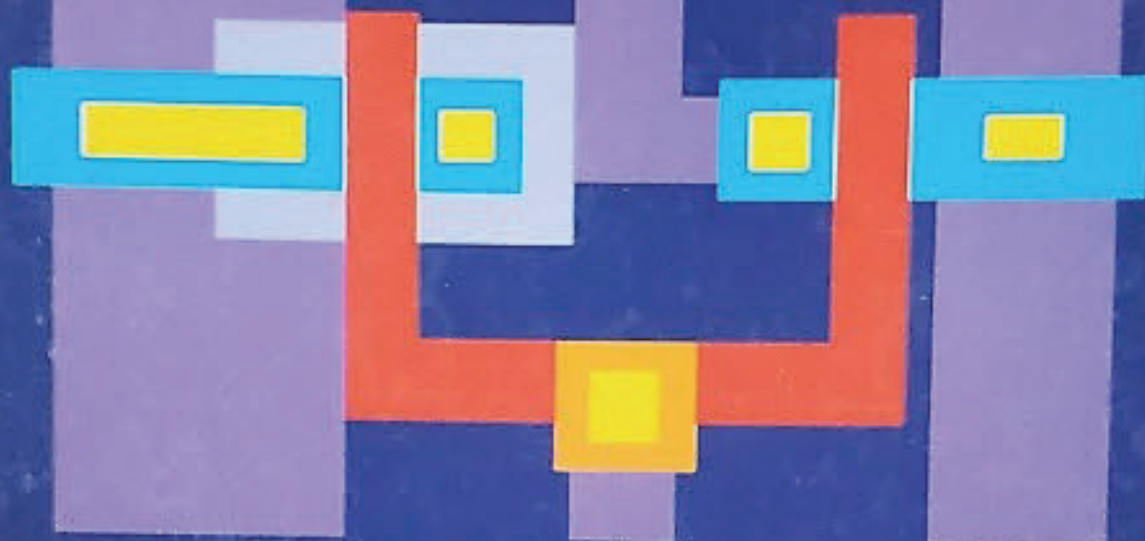
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# Silicon Processing

for the VLSI Era

Volume 2 - Process Integration



S. Wolf

# **SILICON PROCESSING FOR THE VLSI ERA**

## **VOLUME 2: PROCESS INTEGRATION**

**STANLEY WOLF Ph.D.**  
Professor, Department of Electrical Engineering  
California State University, Long Beach  
Long Beach, California

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Published by:

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Post Office Box 340  
Sunset Beach, California 90742, U.S.A.

Cover design by Roy Montibon, Visionary Art Resources, Inc., Santa Ana, CA.

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**Analysis and design of digital integrated circuits: In deep submicron technology**

McGraw-Hill 2004 3rd edition

Hodges, David A. et al.

**University of Southern California Libraries Call #:** TK7874.65 H64 2004

Date of public availability: on or around 9/10/2003 as indicated by library's received date in the ILS

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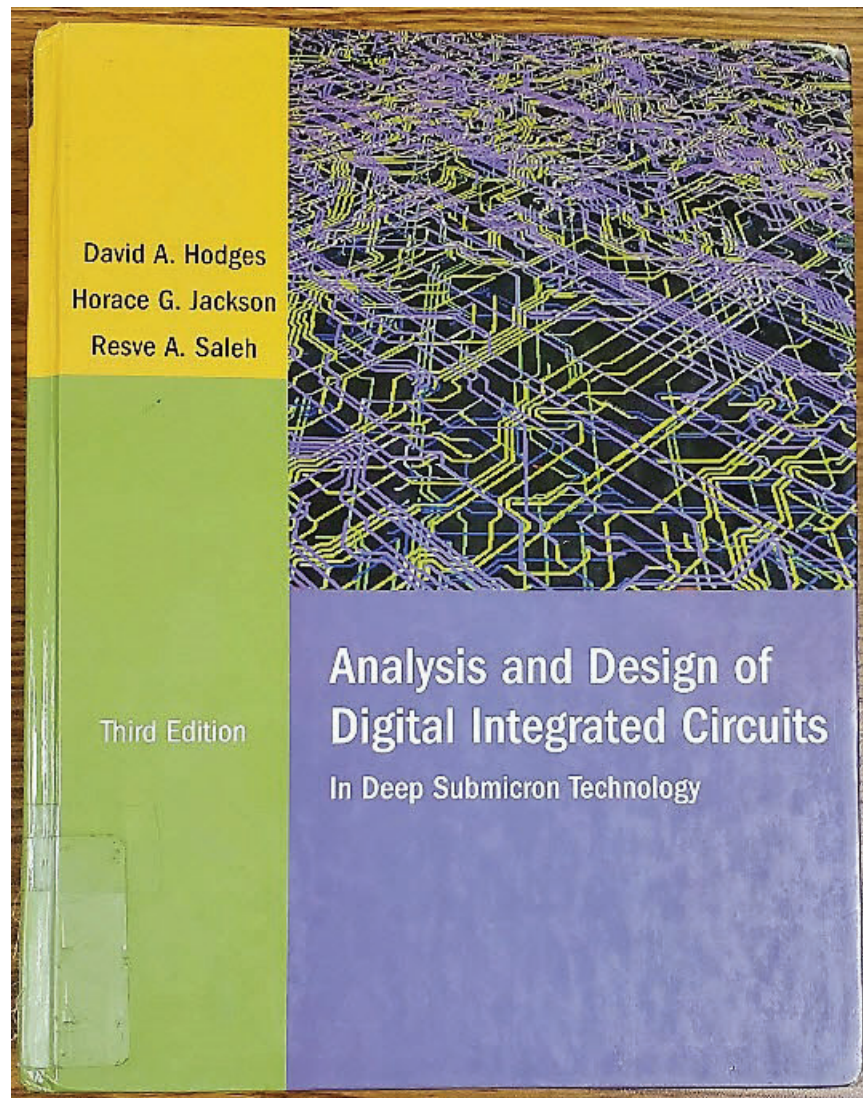
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Alyssa G. Resnick







# Analysis and Design of Digital Integrated Circuits

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## *In Deep Submicron Technology*

THIRD EDITION

**David A. Hodges**

*University of California, Berkeley*

**Horace G. Jackson**

*University of California, Berkeley*

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1 2 3 4 5 6 7 8 9 0 DOC/DOC 0 9 8 7 6 5 4 3

ISBN 0-07-228365-3

Publisher: Elizabeth A. James

Senior sponsoring editor: Carlise Paulson

Senior developmental editor: Emily J. Lupash

Marketing manager: Dawn R. Bercier

Project manager: Joyce Watters

Senior production supervisor: Laura Fuller

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Senior media technology producer: Phillip Meek

Senior coordinator of freelance design: Michelle D. Whitaker

Cover/interior designer: Argasy Publishing

Cover image: © X-Architecture Routing Illustration Courtesy of Cadence Design Systems, Inc.

Lead photo research coordinator: Carrie K. Burger

Compositor: Lachina Publishing Services

Typeface: 10.5/12 Times Roman

Printer: R. R. Donnelley Crawfordsville, IN

Library of Congress Cataloging-in-Publication Data

Hodges, David A., 1937-

Analysis and design of digital integrated circuits : in deep submicron technology. David A. Hodges, Horace G. Jackson, Resve Saleh. — 3rd ed

p. cm.—(McGraw-Hill series in electrical engineering)

Includes index.

ISBN 0-07-228365-3

1. Digital integrated circuits—Design and construction. 2. Integrated circuits—Ultra large scale integration. 3. Electric circuit analysis. I. Jackson, Horace G. II. Saleh, Resve. III. Title. IV. Series.

TK7874.65.H64 2004  
621.3815—dc21

2003051006  
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