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Factorized Power



Factorized Power Architecture



Factorized Power Architecture maximizes a power system by providing a high degree of system flexibility, superior power density, efficiency, transient response, and reliability. This new architecture is enabled by V•I Chip™ components: the BCM™ intermediate bus converter, the PRM™ non-isolated regulator, and the VTM™ current multiplier. These families of integrated power components provide new ways to create small, cost-effective, high-performance power system solutions.

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V•I Chip Customer Evaluation Boards



The V•I Chip Customer Evaluation Boards have been designed to facilitate testing of the superior performance of V•I Chips in density, efficiency, fast response and quiet, low-noise operation. These boards are designated by adding the suffix —EB or —CB to the V•I Chip Model number and can be used to test and understand V•I Chips in a variety of applications configurations.



VICBrick Intermediate Bus Converter



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The V•I Chip™ intermediate bus converter modules use Vicor's V•I Chip BCM™ intermediate bus converter to achieve the highest power density for Intermediate Bus Architecture applications. Operating from a 38-55 Vdc input, nine different fixed ratio outputs are available from 3 to 48 Vdc. You can choose the intermediate bus voltage that is optimal for your system and load requirements.



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FPA Evaluation Board