DECLARATION OF JUNE ANN MUNFORD

1. My name is June Ann Munford. I am over the age of 18, have personal knowledge of the facts set forth herein, and am competent to testify to the same.

2. I earned a Master of Library and Information Science (MLIS) from the University of Wisconsin-Milwaukee in 2009. I have over ten years of experience in the library/information science field. Beginning in 2004, I have served in various positions in the public library sector including Assistant Librarian, Youth Services Librarian and Library Director. I have attached my Curriculum Vitae as Appendix CV.

3. During my career in the library profession, I have been responsible for materials acquisition for multiple libraries. In that position, I have cataloged, purchased and processed incoming library works. That includes purchasing materials directly from vendors, recording publishing data from the material in question, creating detailed material records for library catalogs and physically preparing that material for circulation. In addition to my experience in acquisitions, I was also responsible for analyzing large collections of library materials, tailoring library records for optimal catalog

search performance and creating lending agreements between libraries during my time as a Library Director.

4. I am fully familiar with the catalog record creation process in the library sector. In preparing a material for public availability, a library catalog record describing that material would be created. These records are typically written in Machine Readable Catalog (herein referred to as "MARC") code and contain information such as a physical description of the material, metadata from the material's publisher, and date of library acquisition. In particular, the 008 field of the MARC record is reserved for denoting the date of creation of the library record itself. As this typically occurs during the process of preparing materials for public access, it is my experience that an item's MARC record indicates the date of an item's public availability.

5. Typically, in creating a MARC record, a librarian would gather various bits of metadata such as book title, publisher and subject headings among others and assign each value to a relevant numerical field. For example, a book's physical description is tracked in field 300 while title/attribution is tracked in field 245. The 008 field of the MARC record is reserved for denoting the creation of the library record itself. As this is the only date reflecting the inclusion of said materials within the library's collection, it is my experience

that an item's 008 field accurately indicates the date of an item's public availability.

6. I have reviewed Exhibit MTK-1004, "A 32-bit PowerPC System-on-a-Chip with Support for Dynamic Voltage Scaling and Dynamic Frequency Scaling" by Kevin J. Nowka, et. al as published in *IEEE Journal of Solid-State Circuits*, November 2002.

7. Attached hereto as Appendix NOWKA01 is a true and correct copy of the MARC record for *IEEE Journal of Solid-State Circuits* as held by the Carnegie Library of Pittsburgh. I secured this record myself from the library's public catalog. The MARC record contained within Appendix NOWKA01 accurately describes the title, author, publisher, and ISSN number of *IEEE Journal of Solid-State Circuits.* The 'Lib Has' field of this record visible on page 2 indicates this collection includes the November 2002 edition of *IEEE Journal of Solid-State Circuits* containing "A 32-bit PowerPC System-on-a-Chip with Support for Dynamic Voltage Scaling and Dynamic Frequency Scaling." In comparing Exhibit MTK-1004 to Appendix NOWKA01, it is my determination that Exhibit MTK-1004 is a true and correct copy of "A 32-bit PowerPC System-on-a-Chip with Support for Dynamic Voltage Scaling and Dynamic Frequency Scaling" by Kevin J.

Nowka et. al as published in *IEEE Journal of Solid-State Circuits* November 2002.

8. The 008 field of the MARC record in Appendix NOWKA01 indicates the date of record creation. The 008 field of Appendix NOWKA01 indicates the Carnegie Library of Pittsburgh first acquired this journal as of September 1, 1975 and held the journal in perpetuity until 2008. Considering this information, it is my determination that *IEEE Journal of Solid-State Circuits* November 2002 and therefore "A 32-bit PowerPC System-on-a-Chip with Support for Dynamic Voltage Scaling and Dynamic Frequency Scaling" was made available to the public shortly after its initial release in November 2002.

9. I have reviewed Exhibit MTK-1015, *Semiconductor Device Fundamentals* by Robert F. Pierret.

10. Attached hereto as Appendix PIERRET01 is a true and correct copy of the MARC record for *Semiconductor Device Fundamentals* as held by the Penn State University library. I secured this record myself from the library's public catalog. The MARC record contained within Appendix PIERRET01 accurately describes the title, author, publisher, and ISBN number of

Semiconductor Device Fundamentals. In comparing Exhibit MTK-1015 to Appendix PIERRET01, it is my determination that Exhibit MTK-1015 is a true and correct copy of *Semiconductor Device Fundamentals* by Robert F. Pierret.

11. The 008 field of the MARC record in Appendix PIERRET01 indicates the date of record creation. The 008 field of Appendix PIERRET01 indicates Penn State University library first acquired this book as of December 4, 1995. Considering this information, it is my determination that *Semiconductor Device Fundamentals* was made available to the public shortly after its initial acquisition as of December 4, 1995.

12. I have reviewed Exhibit MTK-1016, "Variable Supply-Voltage Scheme for Low-Power High-Speed CMOS Digital Design" by Tadahiro Kuroda, et. al as published in *IEEE Journal of Solid-State Circuits*, March 1998.

13. Attached hereto as Appendix KURODA01 is a true and correct copy of the MARC record for *IEEE Journal of Solid-State Circuits* as held by the Carnegie Library of Pittsburgh. I secured this record myself from the library's public catalog. The MARC record contained within Appendix KURODA01 accurately describes the title, author, publisher, and ISSN

number of *IEEE Journal of Solid-State Circuits.* The 'Lib Has' field of this record visible on page 2 indicates this collection includes the March 1998 edition of *IEEE Journal of Solid-State Circuits* containing "Variable Supply-Voltage Scheme for Low-Power High-Speed CMOS Digital Design." In comparing Exhibit MTK-1016 to Appendix KURODA01, it is my determination that Exhibit MTK-1016 is a true and correct copy of "Variable Supply-Voltage Scheme for Low-Power High-Speed CMOS Digital Design" by Tadahiro Kuroda, et. al as published in *IEEE Journal of Solid-State Circuits* March 1998.

14. The 008 field of the MARC record in Appendix KURODA01 indicates the date of record creation. The 008 field of Appendix KURODA01 indicates the Carnegie Library of Pittsburgh first acquired this journal as of September 1, 1975 and held the journal in perpetuity until 2008. Considering this information, it is my determination that *IEEE Journal of Solid-State Circuits* March 1998 and therefore "Variable Supply-Voltage Scheme for Low-Power High-Speed CMOS Digital Design" was made available to the public shortly after its initial release in March 1998.

15. I have reviewed Exhibit MTK-1017, *Dynamic Power Management: Design Techniques and CAD Tools* by Luca Benini and Giovanni de Micheli.

16. Attached hereto as Appendix BENINI01 is a true and correct copy of the MARC record for *Dynamic Power Management: Design Techniques and CAD Tools* as held by the Penn State University library. I secured this record myself from the library's public catalog. The MARC record contained within Appendix BENINI01 accurately describes the title, author, publisher, and ISBN number of *Dynamic Power Management: Design Techniques and CAD Tools.* In comparing Exhibit MTK-1017 to Appendix BENINI01, it is my determination that Exhibit MTK-1017 is a true and correct copy of *Dynamic Power Management: Design Techniques and CAD Tools* by Luca Benini and Giovanni de Micheli.

17. The 008 field of the MARC record in Appendix BENINI01 indicates the date of record creation. The 008 field of Appendix BENINI01 indicates Penn State University library first acquired this book as of February 18, 1998. Considering this information, it is my determination that *Dynamic Power Management: Design Techniques and CAD Tools* was made available to the public shortly after its initial acquisition as of February 18, 1998.

18. I have reviewed Exhibit MTK-1018, "A Survey of Design Techniques for System-Level Dynamic Power Management" by Luca Benini, et. al as

published in *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, June 2000.

19. Attached hereto as Appendix BENINI02 is a true and correct copy of the MARC record for *IEEE Transactions on Very Large Scale Integration (VLSI) Systems* as held by the Carnegie Library of Pittsburgh. I secured this record myself from the library's public catalog. The MARC record contained within Appendix BENINI02 accurately describes the title, author, publisher, and ISSN number of *IEEE Transactions on Very Large Scale Integration (VLSI) Systems.* The 'Lib Has' field of this record visible on page 2 indicates this collection includes the June 2000 edition of *IEEE Transactions on Very Large Scale Integration (VLSI) Systems* containing "A Survey of Design Techniques for System-Level Dynamic Power Management." In comparing Exhibit MTK-1018 to Appendix BENINI02, it is my determination that Exhibit MTK-1018 is a true and correct copy of "A Survey of Design Techniques for System-Level Dynamic Power Management" by Luca Benini, et. al as published in *IEEE Transactions on Very Large Scale Integration (VLSI) Systems* June 2000.

20. The 008 field of the MARC record in Appendix BENINI02 indicates the date of record creation. The 008 field of Appendix BENINI02 indicates the

Carnegie Library of Pittsburgh first acquired this journal as of July 7, 1992 and held the journal in perpetuity until 2009. Considering this information, it is my determination that *IEEE Transactions on Very Large Scale Integration Systems* June 2000 and therefore "A Survey of Design Techniques for System-Level Dynamic Power Management" was made available to the public shortly after its initial release in June 2000.

21. I have reviewed Exhibit MTK-1019, "Managing Power and Performance for System-on-Chip Designs using Voltage Islands" by David Lackey, et. al as published in *IEEE/ACM International Conference on Computer-Aided Design*, December 2002.

22. Attached hereto as IEEE01 is a screen capture of a webpage entitled "Managing power and performance for system-on-chip designs using Voltage Islands". I secure these screen captures myself from https://ieeexplore.ieee.org/document/1167534. This page is found on the Institute of Electrical and Electronics Engineers' website and describes "Managing Power and Performance for System-on-Chip Designs using Voltage Islands" by David Lackey, et. al as found in Exhibit MTK-1019. Appendix IEEE01's description of the Lackey paper indicates this paper was published in the proceedings for IEEE/ACM International Conference on

Computer Aided Design 2002. According to Appendix IEEE01, this conference took place from November 10 – 14, 2002 and was added to the IEEE Xplore database as of January 29, 2003.

23. Attached hereto as Appendix LACKEY01 is a true and correct copy of the MARC record for *IEEE/ACM International Conference on Computer-Aided Design* as held by the Penn State University library. I secured this record myself from the library's public catalog. The MARC record contained within Appendix LACKEY01 accurately describes the title, author, publisher, and ISSN number of *IEEE/ACM International Conference on Computer-Aided Design.* The 'Availability' field of this record visible on page 3 indicates this collection includes the December 2002 edition of *IEEE/ACM International Conference on Computer-Aided Design* containing "Managing Power and Performance for System-on-Chip Designs using Voltage Islands." In comparing Exhibit MTK-1019 to Appendix LACKEY01, it is my determination that Exhibit MTK-1019 is a true and correct copy of "Managing Power and Performance for System-on-Chip Designs using Voltage Islands" by David Lackey, et. al as published in *IEEE/ACM International Conference on Computer-Aided Design* December 2002.

24. The 008 field of the MARC record in Appendix LACKEY01 indicates the date of record creation. The 008 field of Appendix LACKEY01 indicates the Penn State University library first acquired this journal as of March 2, 2001 and held the journal in perpetuity until 2004. Considering this information, it is my determination that *IEEE/ACM International Conference on Computer-Aided Design* December 2002 and therefore "Managing Power and Performance for System-on-Chip Designs using Voltage Islands" was made available to the public shortly after its initial release in December 2002.

25. I have reviewed Exhibit MTK-1020, "Policy Optimization for Dynamic Power Management" by Luca Benini, et. al as published in *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*, June 1999.

26. Attached hereto as Appendix BENINI03 is a true and correct copy of the MARC record for *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems* as held by the Carnegie Library of Pittsburgh. I secured this record myself from the library's public catalog. The MARC record contained within Appendix BENINI03 accurately describes the title, author, publisher, and ISSN number of *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems.*

The ‘Lib Has’ field of this record visible on page 2 indicates this collection includes the June 1999 edition of *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems* containing “Policy Optimization for Dynamic Power Management.” In comparing Exhibit MTK-1020 to Appendix BENINI03, it is my determination that Exhibit MTK-1020 is a true and correct copy of “Policy Optimization for Dynamic Power Management” by Luca Benini, et. al as published in *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems* June 1999.

27. The 008 field of the MARC record in Appendix BENINI03 indicates the date of record creation. The 008 field of Appendix BENINI03 indicates the Carnegie Library of Pittsburgh first acquired this journal as of August 18, 1981 and held the journal in perpetuity until 2009. Considering this information, it is my determination that *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems* June 1999 and therefore “Policy Optimization for Dynamic Power Management” was made available to the public shortly after its initial release in June 1999.

28. I have reviewed Exhibit MTK-1021, “Energy-Efficient Processor Design Using Multiple Clock Domains with Dynamic Voltage and Frequency Scaling” by Greg Semeraro, et. al as published in *Proceedings of the Eight*

International Symposium on High-Performance Computer Architecture (HPCA 02).

29. Attached hereto as Appendix SEMERARO01 is a true and correct copy of the MARC record for *Proceedings of the Eight International Symposium on High-Performance Computer Architecture (HPCA 02)* as held by the Penn State University library. I secured this record myself from the library's public catalog. The MARC record contained within Appendix SEMERARO01 accurately describes the title, author, publisher, and ISBN number of *Proceedings of the Eight International Symposium on High-Performance Computer Architecture (HPCA 02)*. In comparing Exhibit MTK-1021 to Appendix SEMERARO01, it is my determination that Exhibit MTK-1021 is a true and correct copy of "Energy-Efficient Processor Design Using Multiple Clock Domains with Dynamic Voltage and Frequency Scaling" by Greg Semeraro, et. al as published in *Proceedings of the Eight International Symposium on High-Performance Computer Architecture (HPCA 02)*.

30. The 008 field of the MARC record in Appendix SEMERARO01 indicates the date of record creation. The 008 field of Appendix SEMERARO01 indicates the Penn State University library first acquired these proceedings

as of June 12, 2002. Considering this information, it is my determination that *Proceedings of the Eight International Symposium on High-Performance Computer Architecture (HPCA 02)* and therefore "Energy-Efficient Processor Design Using Multiple Clock Domains with Dynamic Voltage and Frequency Scaling" was made available to the public shortly after its initial acquisition as of June 12, 2002.

31. I have been retained on behalf of the Petitioner to provide assistance in the above-illustrated matter in establishing the authenticity and public availability of the documents discussed in this declaration. I am being compensated for my services in this matter at the rate of $200.00 per hour plus reasonable expenses. My statements are objective, and my compensation does not depend on the outcome of this matter.

32. I declare under penalty of perjury that the foregoing is true and correct. I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code.

Dated: 3/25/2024



June Ann Munford

APPENDIX CV

June A. Munford
Curriculum Vitae

Education

University of Wisconsin-Milwaukee - MS, Library & Information Science, 2009
Milwaukee, WI

- Coursework included cataloging, metadata, data analysis, library systems, management strategies and collection development.
- Specialized in library advocacy, cataloging and public administration.

Grand Valley State University - BA, English Language & Literature, 2008
Allendale, MI

- Coursework included linguistics, documentation and literary analysis.
- Minor in political science with a focus in local-level economics and government.

Professional Experience

Researcher / Expert Witness, October 2017 – present
Freelance ● Pittsburgh, Pennsylvania & Grand Rapids, Michigan

- Material authentication and public accessibility determination. Declarations of authenticity and/or public accessibility provided upon research completion. Experienced with appeals and deposition process.

- Research provided on topics of public library operations, material publication history, digital database services and legacy web resources.

- Past clients include Alston & Bird, Arnold & Porter, Baker Botts, Fish & Richardson, Erise IP, Irell & Manella, O'Melveny & Myers, Perkins-Coie, Pillsbury Winthrop Shaw Pittman and Slayden Grubert Beard.

Library Director, February 2013 - March 2015
Dowagiac District Library ● Dowagiac, Michigan

- Executive administrator of the Dowagiac District Library. Located in

Southwest Michigan, this library has a service area of 13,000, an annual operating budget of over $400,000 and total assets of approximately $1,300,000.

● Developed careful budgeting guidelines to produce a 15% surplus during the 2013-2014 & 2014-2015 fiscal years while being audited.

● Using this budget surplus, oversaw significant library investments including the purchase of property for a future building site, demolition of existing buildings and building renovation projects on the current facility.

● Led the organization and digitization of the library's archival records.

● Served as the public representative for the library, developing business relationships with local school, museum and tribal government entities.

● Developed an objective-based analysis system for measuring library services - including a full collection analysis of the library's 50,000+ circulating items and their records.

November 2010 - January 2013
Librarian & Branch Manager, Anchorage Public Library ● Anchorage, Alaska

● Headed the 2013 Anchorage Reads community reading campaign including event planning, staging public performances and creating marketing materials for mass distribution.

● Co-led the social media department of the library's marketing team, drafting social media guidelines, creating original content and instituting long-term planning via content calendars.

● Developed business relationships with The Boys & Girls Club, Anchorage School District and the US Army to establish summer reading programs for children.

June 2004 - September 2005, September 2006 - October 2013
Library Assistant, Hart Area Public Library
Hart, MI

● Responsible for verifying imported MARC records and original MARC

cataloging for the local-level collection as well as the Michigan Electronic Library.

● Handled OCLC Worldcat interlibrary loan requests & fulfillment via ongoing communication with lending libraries.

Professional Involvement

Alaska Library Association - Anchorage Chapter
- Treasurer, 2012

Library Of Michigan
- Level VII Certification, 2008
- Level II Certification, 2013

Michigan Library Association Annual Conference 2014
- New Directors Conference Panel Member

Southwest Michigan Library Cooperative
- Represented the Dowagiac District Library, 2013-2015

Professional Development

Library Of Michigan Beginning Workshop, May 2008
Petoskey, MI
- Received training in cataloging, local history, collection management, children's literacy and reference service.

Public Library Association Intensive Library Management Training, October 2011
Nashville, TN
- Attended a five-day workshop focused on strategic planning, staff management, statistical analysis, collections and cataloging theory.

Alaska Library Association Annual Conference 2012 - Fairbanks, February 2012
Fairbanks, AK
- Attended seminars on EBSCO advanced search methods, budgeting, cataloging, database usage and marketing.

Depositions

2019 ● Fish & Richardson

Apple v. Qualcomm (IPR2018-001281, 39521-00421IP, IPR2018-01282 and 39521-00421IP2)

2019 ● Erise IP

Implicit, LLC v. Netscout Systems, Inc (Civil Action No. 2:18-cv-53-JRG)

2019 ● Perkins-Coie

Adobe Inc. v. RAH Color Technologies LLC (Cases IPR2019-00627, IPR2019-00628, IPR2019-00629 and IPR2019-00646)

2020 ● O'Melveny & Myers

Maxell, Ltd. v. Apple Inc. (Case No. 5:19-cv-00036-RWS)

2021 ● Pillsbury Winthrop Shaw Pittman LLP

Intel v. SRC (IPR2020-1449)

2022 ● Perkins-Coie

Realtek v. Future Link (IPR2021-01182)

2023 ● Fish & Richardson

Neuroderm Ltd. v. Abbvie, Inc (Case No. PGR2022-00040)

2023 ● Fish & Richardson

Nearmap US Inc. v. Pictometry International Corp. (IPR2022-00735)

2023 ● Fish & Richardson

Samsung Electronics v. MemoryWeb LLC (Case No. 39843-0136PS1)

2023 ● Pillsbury Winthrop Shaw Whitman LLP

Gravel Rating Systems v. Costco Wholesale Corp. (Civil Action No. 4:21-cv-149-ALM)

Limited Case and Clientele History

Alston & Bird

- Ericsson

  v. Collision Communications (IPR2022-01233)

- Nokia

  v. Neptune Subsea, Xtera (Case No. 1:17-cv-01876)

- Universal Electronics Inc

  v. Roku Inc (IPR2022-00818)

Arnold & Porter

- Ivantis

  v. Glaukos (Case No. 8:18-cv-00620)

- Samsung

  v. Jawbone (Case No. 2:21-cv-00186)

Benesch Friedlander Coplan & Aronoff

- Voyis

  v. Cathx (Case No. 5:21-cv-00077-RWS)

Erise I.P.

- Apple
  - v. Ericsson Inc. (IPR2022-00715)
  - v. Future Link Systems (IPRs 6317804, 6622108, 6807505, and 7917680)
  - v. INVT (Case No. 20-1881)
  - v. Navblazer LLC (IPR2020-01253)
  - v. Qualcomm (IPR2018-001281, 39521-00421IP, IPR2018-01282, 39521-00421IP2)
  - v. Quest Nettech Corp (Case No. 2:19-cv-00118-JRG)
  - v. Telefonaktiebolaget LM Ericsson (IPR2022-00275)
- Fanduel
  - v. CGT (Case No. 19-1393)
- Garmin
  - v. Phillips North America LLC (Case No. 2:19-cv-6301-AB-KS)
- Netscout
  - v. Longhorn HD LLC (Case No. 2:20-cv-00349)
  - v. Implicit, LLC (Civil Action No. 2:18-cv-53-JRG)
- Sony Interactive Entertainment LLC
  - v. Bot M8 LLC (IPR2020-01288)
  - v. Infernal Technology LLC (Case No. 2:19-CV-00248-JRG)
- Unified Patents

v. GE Video Compression (Civil Action No. 2:19-cv-248)

Fish & Richardson

- Apple

  v. AliveCor (3:21-cv-03958)

  v. LBS Innovations (Case No. 2:19-cv-00119-JRG-RSP)

  v. Koss Corporation (IPR2021-00305)

  v. Masimo (IPR 50095-0012IP1, 50095-0012IP2, 50095-0013IP1, 50095-0013IP2, 50095-0006IP1, 50095-0135IP1)

  v. Neonode (Case No. 21-cv-08872-EMC)

  v. Qualcomm (IPR2018-001281, 39521-00421IP, IPR2018-01282, 39521-00421IP2)

- Dell

  v. Neo Wireless (IPR2022-00616)

- Dish Network

  v. Entropic Communications, LLC (Case No. 2:2023-CV-01043)

  v. Realtime Adaptive Streaming (Case No. 1:17-CV-02097-RBJ)

  v. TQ Delta LLC (Case No. 18-1798)

- Evapco Dry Cooling

  v. SPG Dry Cooling (IPR2021-00688)

- Genetec

  v. Sensormatic Electronics (Case No. 1:20-CV-00760)

- Huawei

v. Bell Northern Research LLC (IPR2019-01174)

- Kianxis

  v. Blue Yonder (Case No. 3:20-cv-03636)

- LG Electronics

  v. Bell Northern Research LLC (Case No. 3:18-cv-2864-CAB-BLM)

- Metaswitch

  v. Sonus Networks (IPR2018-01719)

- Microsoft

  v. Throughputer Inc (IPR2022-00757)

- Mom Enterprises

  v. Ddrops Company (Case No. 1:22-cv-00332)

- MLC Intellectual Property

  v. MicronTech (Case No. 3:14-cv-03657-SI)

- Nearmap Inc

  v. EagleView Technologies (IPR2022-01009)

- Neuroderm Ltd.

  v. Abbvie, Inc (Case No. PGR2022-00040)

- Realtek Semiconductor

v. Future Link (IPR2021-01182)

- Quectel
  - v. Koninklijke Philips (Case No. 1:20-cv-01710)

- Samsung
  - v. Aire Technology (IPR2022-00877)
  - v. Bell Northern Research (Case No. 2:19-cv-00286-JRG)
  - v. Communication Technologies Inc (IPR2022-01221)
  - v. Jawbone Innovations (IPR2022-00865)
  - v. MemoryWeb LLC (IPR2022-00885)
  - v. Dodots Licensing Solutions (IPR2023-00701)
  - v. Telefonaktiebolaget LM Ericsson (IPR2021-00615)

- Texas Instruments
  - v. Vantage Micro LLC (IPR2020-01261)

- Xilinx
  - v. Sentient Sensors LCC (Case No. 1:22-cv-00173)

Irell & Manella

- Curium

O'Melveny & Myers

- Apple
  - v. Maxell (Case No. 5:19-cv-00036-RWS)

- Micron Technology Inc.

  v. Besang Inc (IPR2023-00900)

- Samsung

  v. Daedalus Prime LLC (1335 Investigation)

Perkins-Coie

- Heru Industries

  v. The UAB Research Foundation (IPR2022-01148)

- Intel Corporation

  v. BitMICRO (Case No. 5:23-cv-00625)

- Realtek Semiconductor

  v. Future Link (IPR2021-01182)

- Twitter Inc

  v. VOIP-Pal.com (Case No. 3:20-cv-02397-JD)

- TCL Industries

  v. Koninklijke Philips NV (IPR2021-00495, IPR2021-00496 and IPR2021-00497)

Pillsbury Winthrop Shaw Pittman

- Intel

  v. FG SRC LLC (Case No. 6:20-cv-00315)

- Gravel Rating Systems

v. Costco (Case No. 4:21-cv-149)
v. Lowe's Home Centers (Case No. 4:21-cv-150)
v. T-Mobile USA (Case No. 4:21-cv-152)
v. Kohl's Inc. (Case No. 4:21-cv-258)
v. Under Armor (Case No. 4:21-cv-356)

Quinn Emanuel

- Wolfspeed

v. The Trustees of Purdue University (Case No. 1:2021-cv-00840)

Stern, Kessler, Goldstein & Fox

- Preliminary Research

Sheppard, Mullin, Richter & Hampton LLP

- Cadence Design Systems Inc.

v. Semiconductor Design Technologies (Case No. 3:23-cv-01001)

APPENDIX NOWKA01

screenshot-iiisy1.einetwork.net-2024.01.21-12_12_29
https://iiisy1.einetwork.net/search~S1?/o1589745/o1589745/1%2C1%2C1%2CE/marc&FF=o1589745&1%2C1%2C
21.01.2024


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LEADER 00000cas  2200000 a 4500
001    1589745
003    OCoLC
005    20150414133918.0
008    750901c19669999nyumr p       0   a0eng d
010    89657065 |zsn 78000342
022    0018-9200
030    IJSCBC
032    925080|bUSPS
035    325360|bMULS|a200004|a300007
040    MUL|cMUL|dNSD|dYUS|dHUL|dSER|dNSD|dOCL|dAIP|dNST|dNSD|dDLC
       |dNST|dDLC|dNST|dIUL|dWaOLN|dUtOrBLW
042    nsdp|alc
050 00 TK7871.85|b.I23
210 0  IEEE j. solid-state circuits
222  0 IEEE journal of solid-state circuits
245 00 IEEE journal of solid-state circuits.
246 10 Journal of solid-state circuits
246 23 IEEE journal of solid state circuits
246 23 Journal of solid state circuits
260    New York, N.Y. :|bInstitute of Electrical and Electronics
       Engineers,|c©1966-
300    volumes :|billustrations ;|c28 cm
310    Monthly,|b1991-
321    Quarterly,|b1966-
321    Bimonthly,|b<1986- >
336    text|btxt|2rdacontent
337    unmediated|bn|2rdamedia
338    volume|bnc|2rdacarrier
362 0  Vol. SC-1, no. 1 (Sept. 1966)-
510 1  Computer & control abstracts|x0036-8113|b1968-
510 1  Electrical & electronics abstracts|x0036-8105|b1968-
510 1  Index to IEEE publications|x0099-1368
510 1  Physics abstracts. Science abstracts. Series A|x0036-8091
       |b1968-
510 2  Chemical abstracts|x0009-2258|b1966-1982
510 2  Energy research abstracts|x0160-3604
510 2  Engineering index annual (1968)|x0360-8557
510 2  Engineering index bioengineering abstracts|x0736-6213
510 2  Engineering index energy abstracts|x0093-8408
510 2  International aerospace abstracts|x0020-5842
510 2  ISMEC bulletin|x0306-0039
510 2  Pollution abstracts with indexes|x0032-3624
510 2  Safety science abstracts journal|x0160-1342
510 0  Engineering abstracts monthly|x0013-7960
510 0  Nuclear science abstracts|x0029-5612
510 1  Applied science & technology index|x0003-6986
550    Issued by the IEEE Solid-State Circuits Council.
650  0 Semiconductors|vPeriodicals.
650  0 Integrated circuits|vPeriodicals.
650  0 Solid state electronics|vPeriodicals.
710 2  IEEE Solid-State Circuits Council.
850    CaSRU|aDLC|aIC|aInU|aMChB|aNN|aNcU|aOkU|aPPT|aViBlbV
890    IEEE journal of solid-state circuits.
901    |cSer
935    ADM-6317
936    Vol. 26, no. 1 (Jan. 1991) (surrogate) LIC
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Location	Carnegie Library of Pittsburgh - Off Site Storage (Please ask for assistance)
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https://iiisy1.einetwork.net/search/o1589745
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Title	**IEEE journal of solid-state circuits.**
Publisher	New York, N.Y. : Institute of Electrical and Electronics Engineers, ©1966-
Description	volumes : illustrations ; 28 cm
Frequency	Monthly, 1991- Quarterly, 1966- Bimonthly, <1986- >
Pub Date	Vol. SC-1, no. 1 (Sept. 1966)-

Location	Carnegie Library of Pittsburgh - Off Site Storage (Please ask for assistance)
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Addt'l Author	IEEE Solid-State Circuits Council.
Key Title	IEEE j. solid-state circuits IEEE journal of solid-state circuits
Addt'l Title	IEEE journal of solid state circuits Journal of solid state circuits
ISSN	0018-9200

APPENDIX PIERRET01

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LEADER 00961nam a22003018a 4500				
001 1424766				
003 SIRSI				
005 20151215020947.0				
008 951204t19961996xxu 000 0 eng d				
010			a| 95017387	
019			a| MARS	
020			a| 0201543931	
035			a| LIAS1885570	
035			a| (OCoLC)32429067	
040			c| PSt d| WaOLN d| UtOrBLW	
050	0	0	a| TK7871.85 b| .P484 1996	
100	1		a| Pierret, Robert F.	
245	1	0	a| Semiconductor device fundamentals / c| Robert F. Pierret.	
264		1	a| Reading, Mass. : b| Addison-Wesley, c| [1996]	
264		4	c| ©1996	
300			a| xxiii, 792 pages : b| illustrations ; c| 25 cm	
336			a| text b| txt 2| rdacontent	
337			a| unmediated b| n 2| rdamedia	
338			a| volume b| nc 2| rdacarrier	
500			a| Includes index.	
504			a| Includes bibliographical references.	
650		0	a| Semiconductors.	
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Semiconductor device fundamentals / Robert F. Pierret

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Copyright Date: ©1996
Physical Description: xxiii, 792 pages : illustrations ; 25 cm

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LEADER 00000cas  2200000 a 4500
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010    89657065 |zsn 78000342
022    0018-9200
030    IJSCBC
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035    325360|bMULS|a200004|a300007
040    MUL|cMUL|dNSD|dYUS|dHUL|dSER|dNSD|dOCL|dAIP|dNST|dNSD|dDLC
       |dNST|dDLC|dNST|dIUL|dWaOLN|dUtOrBLW
042    nsdp|alc
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210 0  IEEE j. solid-state circuits
222  0 IEEE journal of solid-state circuits
245 00 IEEE journal of solid-state circuits.
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260    New York, N.Y. :|bInstitute of Electrical and Electronics
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300    volumes :|billustrations ;|c28 cm
310    Monthly,|b1991-
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362 0  Vol. SC-1, no. 1 (Sept. 1966)-
510 1  Computer & control abstracts|x0036-8113|b1968-
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       |b1968-
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890    IEEE journal of solid-state circuits.
901    |cSer
935    ADM-6317
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Title	**IEEE journal of solid-state circuits.**
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Subject	Semiconductors -- Periodicals. Integrated circuits -- Periodicals. Solid state electronics -- Periodicals.
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Key Title	IEEE j. solid-state circuits IEEE journal of solid-state circuits
Addt'l Title	IEEE journal of solid state circuits Journal of solid state circuits
ISSN	0018-9200

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001 1520235				
003 SIRSI				
005 20151215043630.0				
008 980218t19981998xxua b 000 0 eng d				
010			a| 97032140	
019			a| MARS	
020			a| 079238086X q| (alk. paper)	
035			a| LIAS2070955	
035			a| (OCoLC)37748409	
040			c| PSt d| WaOLN d| UtOrBLW	
050	0	0	a| TK7895.P68 b| B46 1998	
100	1		a| Benini, Luca, d| 1967-	
245	1	0	a| Dynamic power management : b| design techniques and CAD tools / c| by Luca Benini and Giovanni De Micheli.	
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650		0	a| Electronic digital computers x| Power supply.	
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700	1		a| De Micheli, Giovanni.	
949			a| TK7895.P68B46 1998 w| LC c| 1 i| 23430404009 d| 12/19/2002 e| 9/28/2002 l| CATO-PARK m| UP-ANNEX n| 4 r| Y s| Y t| BOOK u| 4/13/2001 x| B2 z| 2343 0| 0404 1| 009	

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Author:	Benini, Luca, 1967-
Published:	Boston : Kluwer, [1998]
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010    93641510 |zsn 92000187
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       |dUtOrBLW
042    nsdp|alc
050 00 TK7874|b.I3273
082 00 621.39/5|220
210 0  IEEE trans. very large scale integr. (VLSI) syst.
222  0 IEEE transactions on very large scale integration (VLSI)
       systems
245 00 IEEE transactions on very large scale integration (VLSI)
       systems.
246 10 Very large scale integration (VLSI) systems
246 23 Institute of Electrical and Electronics Engineers
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       systems
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       Engineers,|c©1993-
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362 0  Vol. 1, no. 1 (Mar. 1993)-
500    Title from cover.
550    A joint publication of: IEEE Circuits and Systems Society,
       IEEE Computer Society, and IEEE Solid-State Circuits
       Council.
650  0 Integrated circuits|xVery large scale integration|xDesign
       and construction|vPeriodicals.
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890    IEEE transactions on very large scale integration (VLSI)
       systems.
901    |cSer
935    ADO-0864
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Title	**IEEE transactions on very large scale integration (VLSI) systems.**
Publisher	New York, NY : Institute of Electrical and Electronics Engineers, ©1993-
Description	volumes : illustrations ; 28 cm
Frequency	Quarterly
Pub Date	Vol. 1, no. 1 (Mar. 1993)-
Location	CLP Main (Oakland) Second Floor - Magazines
Lib Has	Volume 1 (1993) to Volume 17, no. 3 (2009).

Details

Note	Title from cover.
Issued By	A joint publication of: IEEE Circuits and Systems Society, IEEE Computer Society, and IEEE Solid-State Circuits Council.
Subject	Integrated circuits -- Very large scale integration -- Design and construction -- Periodicals. *Show similar items*
Addt'l Author	Institute of Electrical and Electronics Engineers. IEEE Circuits and Systems Society. IEEE Computer Society. IEEE Solid-State Circuits Council.
Key Title	IEEE trans. very large scale integr. (VLSI) syst. IEEE transactions on very large scale integration (VLSI) systems
Addt'l Title	Institute of Electrical and Electronics Engineers transactions on very large scale integration (VLSI) systems
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Managing power and performance for system-on-chip designs using Voltage Islands

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Abstract

Document Sections

» Background

» Voltage Islands Overview

» Voltage Island Scenarios and Examples

» System-level Power Management Approaches

» Design Implications of Voltage Islands

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Abstract:
This paper discusses Voltage Islands, a system architecture and chip implementation methodology, that can be used to dramatically reduce active and static power consumption for System-on-Chip (SoC) designs. As technology scales for increased circuit density and performance, the need to reduce power consumption increases in significance as designers strive to utilize the advancing silicon capabilities. The consumer product market further drives the need to minimize chip power consumption. Effective use of Voltage Islands for meeting SoC power and performance requirements, while meeting Time to Market (TAT) demands, requires novel approaches throughout the design flow as well as special circuit components and chip powering structures. This paper outlines methods being used today to design Voltage Islands in a rapid-TAT product development environment, and discusses the need for industry EDA advances to create an industry-wide Voltage Island design capability.

Published in: IEEE/ACM International Conference on Computer Aided Design, 2002. ICCAD 2002.

Date of Conference: 10-14 November 2002

Date Added to IEEE *Xplore*: 29 January 2003

Print ISBN:0-7803-7607-2

Print ISSN: 1092-3152

DOI: 10.1109/ICCAD.2002.1167534

Publisher: IEEE

Conference Location: San Jose, CA, USA

Background
The total power consumed by conventional CMOS circuitry is composed of two sources. The first is active power, this power represents the power consumed by the intended work of the circuit to switch states and thus execute logic functions. Active power is primarily composed of the power associated with the charging or discharging of the capacitance of the switching nodes [1]. The magnitude of this power is given by equation 1.

$$P_{active} = C * Vdd^2 * F \quad (1)$$

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Published: 1999

A 32-bit PowerPC system-on-a-chip with support for dynamic voltage scaling and dynamic frequency scaling
IEEE Journal of Solid-State Circuits
Published: 2002

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operation increase by 30% each. Assuming perfect scaling, these increases are exactly offset by a corresponding 30% decrease in Vdd, and the power per unit area remains constant. However, frequency of operation has increased 30 x over this time

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003 SIRSI			
005 20151215033250.0			
008 010302c20009999nyuar 1 a0eng d			
010			a| 2002238312 z| 2004295043
019			a| MARS
022			a| 1933-7760 y| 1092-3152 2| 1
035			a| (OCoLC)46362883
040			a| LDL c| LDL d| IUL d| HNK d| OCLCQ d| TJC d| DLC d| NSD d| OCLCQ d| MYG d| UtOrBLW
042			a| lcd a| nsdp
050	0	0	a| TK7874 b| .I3235a
082	1	0	a| 621 2| 14
111	2		a| IEEE/ACM International Conference on Computer-Aided Design.
210	0		a| ICCAD -- IEEE/ACM Int. Conf. Comput. Aided Des.
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300			a| volumes : b| illustrations ; c| 28 cm
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500			a| Published: Boulder, CO : MP Associates, Inc., 2001-
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949			a| TK7874.I3235 2003 w| LCPER c| 1 i| 000051117887 l| CATO-2 m| UP-ANNEX r| Y s| Y t| SERIAL u| 11/20/2003 z| 6915
949			a| TK7874.I3235 2002 w| LCPER c| 1 i| 000049780505 d| 2/18/2003 e| 2/4/2003 l| CATO-2 m| UP-ANNEX n| 1 r| Y s| Y t| SERIAL u| 11/25/2002 z| 6915
949			a| TK7874.I3235 2001 w| LCPER c| 1 i| 000046631206 l| CATO-2 m| UP-ANNEX r| Y s| Y t| SERIAL u| 1/25/2002 z| 6915
852	0	1	a| PSt b| UP-ANNEX c| ANNEX h| TK7874 i| .I3235
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       |aICarbS|aIEN|aIU|aIaAS|aIaU|aInLP|aKMK|aKyU|aLNT|aLU|aMBU
       |aMCM|aMMeT|aMdU|aMiD|aMiDW|aMiEM|aMiU|aMnU|aMoKL|aMoSW
       |aMoU|aMsSM|aMsU|aMtBC|aN|aNN|aNNC|aNNE|aNRU|aNSyU|aNcRS
       |aNcD|aNcU|aNdU|aNhU|aNjP|aNjR|aNmU|aOCU|aOU|aPPD|aPPi
       |aPPiC|aPPiU|aPSt|aPU|aRPB-S|aScCleU|aScU|aTU|aTxArU|aTxCM
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245	1	0	a| 8th International Symposium on High Performance Computer Architecture h| [electronic resource]: b| Proceedings 2002: Cambridge, MA	
260			a| Los Alamitos : b| IEEE Computer Society Press c| Jan. 2002	
300			a| 323 p. b| ill c| 28.000 x 020.000 cm.	
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520	8		a| Annotation b| The keynote speaker at the February 2002 conference discussed "the software industry: ten lessons for long life." Subsequent papers (27 in all) are arranged in sections on energy and thermal management, speculative multithreading, potpourri, memory-aware scheduling, latency tolerance and caches, speculation and prediction, multiprocessor systems, and pipelining and microarchitecture. A panel discussion addresses the question: "What will have the greatest impact in 2010: the processor, the memory, or the interconnect?" Indexing is by author only. Annotation copyrighted by Book News Inc., Portland, OR.	
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