DECLARATION OF JUNE ANN MUNFORD

1. My name is June Ann Munford. I am over the age of 18, have personal knowledge of the facts set forth herein, and am competent to testify to the same.

2. I earned a Master of Library and Information Science (MLIS) from the University of Wisconsin-Milwaukee in 2009. I have over ten years of experience in the library/information science field. Beginning in 2004, I have served in various positions in the public library sector including Assistant Librarian, Youth Services Librarian and Library Director. I have attached my Curriculum Vitae as Appendix CV.

3. During my career in the library profession, I have been responsible for materials acquisition for multiple libraries. In that position, I have cataloged, purchased and processed incoming library works. That includes purchasing materials directly from vendors, recording publishing data from the material in question, creating detailed material records for library catalogs and physically preparing that material for circulation. In addition to my experience in acquisitions, I was also responsible for analyzing large collections of library materials, tailoring library records for optimal catalog

search performance and creating lending agreements between libraries during my time as a Library Director.

4. I am fully familiar with the catalog record creation process in the library sector. In preparing a material for public availability, a library catalog record describing that material would be created. These records are typically written in Machine Readable Catalog (herein referred to as "MARC") code and contain information such as a physical description of the material, metadata from the material's publisher, and date of library acquisition. In particular, the 008 field of the MARC record is reserved for denoting the date of creation of the library record itself. As this typically occurs during the process of preparing materials for public access, it is my experience that an item's MARC record indicates the date of an item's public availability.

5. Typically, in creating a MARC record, a librarian would gather various bits of metadata such as book title, publisher and subject headings among others and assign each value to a relevant numerical field. For example, a book's physical description is tracked in field 300 while title/attribution is tracked in field 245. The 008 field of the MARC record is reserved for denoting the creation of the library record itself. As this is the only date reflecting the inclusion of said materials within the library's collection, it is my experience

that an item's 008 field accurately indicates the date of an item's public availability.

6. I have reviewed Exhibit MTK-1008, *A Practical Approach to Digital Electronics* by Alan C. Dixon and James L. Antonakos.

7. Attached hereto as Appendix DIXON01 is a true and correct copy of the MARC record for *A Practical Approach to Digital Electronics* as held by the Ferris State University library. I secured this record myself from the library's public catalog. The MARC record contained within Appendix DIXON01 accurately describes the title, author, publisher, and ISBN number of *A Practical Approach to Digital Electronics.* In comparing Exhibit MTK-1008 to Appendix DIXON01, it is my determination that Exhibit MTK-1008 is a true and correct copy of *A Practical Approach to Digital Electronics* by Alan C. Dixon and James L. Antonakos.

8. The 008 field of the MARC record in Appendix DIXON01 indicates the date of record creation. The 008 field of Appendix DIXON01 indicates Ferris State University library first acquired this book as of March 19, 1999. Considering this information, it is my determination that *A Practical*

Approach to Digital Electronics was made available to the public shortly after its initial acquisition as of March 19, 1999.

9. I have reviewed Exhibit MTK-1009, *Computer Systems Organization & Architecture* by John D. Carpinelli.

10. Attached hereto as Appendix CARPINELLI01 is a true and correct copy of the MARC record for *Computer Systems Organization & Architecture* as held by the Wayne State University library. I secured this record myself from the library's public catalog. The MARC record contained within Appendix CARPINELLI01 accurately describes the title, author, publisher, and ISBN number of *Computer Systems Organization & Architecture.* In comparing Exhibit MTK-1009 to Appendix CARPINELLI01, it is my determination that Exhibit MTK-1009 is a true and correct copy of *Computer Systems Organization & Architecture* by John D. Carpinelli.

11. The 008 field of the MARC record in Appendix CARPINELLI01 indicates the date of record creation. The 008 field of Appendix CARPINELLI01 indicates Wayne State University library first acquired this book as of June 15, 2000. Considering this information, it is my determination that

Computer Systems Organization & Architecture was made available to the public shortly after its initial acquisition as of June 15, 2000.

12. I have reviewed Exhibit MTK-1015, *Modern Dictionary of Electronics* by Rudolf F. Graf, 7th Edition.

13. Attached hereto as Appendix GRAF01 is a true and correct copy of the MARC record for *Modern Dictionary of Electronics* as held by the Ohio University library. I secured this record myself from the library's public catalog. The MARC record contained within Appendix GRAF01 accurately describes the title, author, publisher, and ISBN number of *Modern Dictionary of Electronics.* In comparing Exhibit MTK-1015 to Appendix GRAF01, it is my determination that Exhibit MTK-1015 is a true and correct copy of *Modern Dictionary of Electronics* by Rudolf F. Graf.

14. The 008 field of the MARC record in Appendix GRAF01 indicates the date of record creation. The 008 field of Appendix GRAF01 indicates Ohio University library first acquired this book as of January 20, 1999. Considering this information, it is my determination that *Modern Dictionary of Electronics* was made available to the public shortly after its initial acquisition as of January 20, 1999.

15. I have reviewed Exhibit MTK-1016, *Dictionary of Science and Technology* by Simon Collin.

16. Attached hereto as Appendix COLLIN01 is a true and correct copy of the MARC record for *Dictionary of Science and Technology* as held by the University of Pittsburgh library. I secured this record myself from the library's public catalog. The MARC record contained within Appendix COLLIN01 accurately describes the title, author, publisher, and ISBN number of *Dictionary of Science and Technology.* In comparing Exhibit MTK-1016 to Appendix COLLIN01, it is my determination that Exhibit MTK-1016 is a true and correct copy of *Dictionary of Science and Technology* by Simon Collin.

17. The 008 field of the MARC record in Appendix COLLIN01 indicates the date of record creation. The 008 field of Appendix COLLIN01 indicates University of Pittsburgh library first acquired this book as of December 1, 2003. Considering this information, it is my determination that *Dictionary of Science and Technology* was made available to the public shortly after its initial acquisition in December 1, 2003.

18. I have reviewed Exhibit MTK-1017, *Microelectronic Circuits* by Adel S. Sedra and Kennith C. Smith, 4th Edition.

19. Attached hereto as Appendix SEDRA01 is a true and correct copy of the MARC record for *Microelectronic Circuits* as held by the Penn State University library. I secured this record myself from the library's public catalog. The MARC record contained within Appendix SEDRA01 accurately describes the title, author, publisher, and ISBN number of *Microelectronic Circuits.* In comparing Exhibit MTK-1017 to Appendix SEDRA01, it is my determination that Exhibit MTK-1017 is a true and correct copy of *Microelectronic Circuits* by Adel S. Sedra and Kennith C. Smith.

20. The 008 field of the MARC record in Appendix SEDRA01 indicates the date of record creation. The 008 field of Appendix SEDRA01 indicates Penn State University library first acquired this book as of August 19, 1997. Considering this information, it is my determination that *Microelectronic Circuits* was made available to the public shortly after its initial acquisition as of August 19, 1997.

21. I have reviewed Exhibit MTK-1025, *The Ultimate Competitive Advantage: Secrets of Continually Developing a More Profitable Business Model* by Donald Mitchell, 3rd Edition.

22. Attached hereto as Appendix MITCHELL01 is a true and correct copy of the MARC record for *The Ultimate Competitive Advantage: Secrets of Continually Developing a More Profitable Business Mode* as held by the University of Pittsburgh library. I secured this record myself from the library's public catalog. The MARC record contained within Appendix MITCHELL01 accurately describes the title, author, publisher, and ISBN number of *The Ultimate Competitive Advantage: Secrets of Continually Developing a More Profitable Business Mode.* In comparing Exhibit MTK-1025 to Appendix MITCHELL01, it is my determination that Exhibit MTK-1025 is a true and correct copy of *The Ultimate Competitive Advantage: Secrets of Continually Developing a More Profitable Business Mode* by Donald Mitchell.

23. The 008 field of the MARC record in Appendix MITCHELL01 indicates the date of record creation. The 008 field of Appendix MITCHELL01 indicates University of Pittsburgh library first acquired this book as of September 19, 2002. Considering this information, it is my determination that *The Ultimate*

Competitive Advantage: Secrets of Continually Developing a More Profitable Business Mode was made available to the public shortly after its initial acquisition as of September 19, 2002.

24. I have reviewed Exhibit MTK-1026, *Modern VLSI Design, System-on-Chip Design* by Wayne Wolf, 3rd ed.

25. Attached hereto as Appendix WOLF01 is a true and correct copy of the MARC record for *Modern VLSI Design, System-on-Chip Design* as held by the Carnegie Mellon University library. I secured this record myself from the library's public catalog. The MARC record contained within Appendix WOLF01 accurately describes the title, author, publisher, and ISBN number of *Modern VLSI Design, System-on-Chip Design.* In comparing Exhibit MTK-1026 to Appendix WOLF01, it is my determination that Exhibit MTK-1026 is a true and correct copy of *Modern VLSI Design, System-on-Chip Design* by Wayne Wolf.

26. The 008 field of the MARC record in Appendix WOLF01 indicates the date of record creation. The 008 field of Appendix WOLF01 indicates Carnegie Mellon University library first acquired this book as of April 2, 2002. Considering this information, it is my determination that *Modern VLSI*

Design, System-on-Chip Design was made available to the public shortly after its initial acquisition in April 2, 2002.

27. I have reviewed Exhibit MTK-1028, "On-Chip High-Voltage Generation in MNOS Integrated Circuits Using and Improved Voltage Multiplier Technique" by John F. Dickson as published in *IEEE Journal of Solid-State Circuits*, June 1976.

28. Attached hereto as Appendix DICKSON01 is a true and correct copy of the MARC record for *IEEE Journal of Solid-State Circuits* as held by the Carnegie Library of Pittsburgh. I secured this record myself from the library's public catalog. The MARC record contained within Appendix DICKSON01 accurately describes the title, author, publisher, and ISSN number of *IEEE Journal of Solid-State Circuits.* The 'Lib Has' field of this record visible on page 2 indicates this collection includes the June 1976 edition of *IEEE Journal of Solid-State Circuits* containing "On-Chip High-Voltage Generation in MNOS Integrated Circuits Using and Improved Voltage Multiplier Technique". In comparing Exhibit MTK-1028 to Appendix DICKSON01, it is my determination that Exhibit MTK-1028 is a true and correct copy of "On-Chip High-Voltage Generation in MNOS Integrated Circuits Using and Improved Voltage Multiplier Technique" by

John F. Dickson as published in *IEEE Journal of Solid-State Circuits* June 1976.

29. The 008 field of the MARC record in Appendix DICKSON01 indicates the date of record creation. The 008 field of Appendix DICKSON01 indicates the Carnegie Library of Pittsburgh library first acquired this journal as of September 1, 1975 and held the journal in perpetuity until 2008. Considering this information, it is my determination that *IEEE Journal of Solid-State Circuits* June 1976 and therefore "On-Chip High-Voltage Generation in MNOS Integrated Circuits Using and Improved Voltage Multiplier Technique" was made available to the public shortly after its initial release in June 1976.

30. I have reviewed Exhibit MTK-1029, *CMOS Digital Circuit Technology* by Masakazu Shoji.

31. Attached hereto as Appendix SHOJI01 is a true and correct copy of the MARC record for *CMOS Digital Circuit Technology* as held by the Penn State University library. I secured this record myself from the library's public catalog. The MARC record contained within Appendix SHOJI01 accurately describes the title, author, publisher, and ISBN number of *CMOS*

Digital Circuit Technology. In comparing Exhibit MTK-1029 to Appendix SHOJI01, it is my determination that Exhibit MTK-1029 is a true and correct copy of *CMOS Digital Circuit Technology* by Masakazu Shoji.

32. The 008 field of the MARC record in Appendix SHOJI01 indicates the date of record creation. The 008 field of Appendix SHOJI01 indicates Penn State University library first acquired this book as of March 18, 1988. Considering this information, it is my determination that *CMOS Digital Circuit Technology* was made available to the public shortly after its initial acquisition as of March 18, 1988.

33. I have reviewed Exhibit MTK-1030, *Nonvolatile Semiconductor Memory Technology: A Comprehensive Guide to Understanding and Using NVSM Devices* by William D. Brown.

34. Attached hereto as Appendix BROWN01 is a true and correct copy of the MARC record for *Nonvolatile Semiconductor Memory Technology: A Comprehensive Guide to Understanding and Using NVSM Devices* as held by the Carnegie Library of Pittsburgh library. I secured this record myself from the library's public catalog. The MARC record contained within Appendix BROWN01 accurately describes the title, author, publisher, and

ISBN number of *Nonvolatile Semiconductor Memory Technology: A Comprehensive Guide to Understanding and Using NVSM Devices.* In comparing Exhibit MTK-1030 to Appendix BROWN01, it is my determination that Exhibit MTK-1030 is a true and correct copy of *Nonvolatile Semiconductor Memory Technology: A Comprehensive Guide to Understanding and Using NVSM Devices* by William D. Brown.

35. The 008 field of the MARC record in Appendix BROWN01 indicates the date of record creation. The 008 field of Appendix BROWN01 indicates Carnegie Library of Pittsburgh library first acquired this book as of May 14, 1997. Considering this information, it is my determination that *Nonvolatile Semiconductor Memory Technology: A Comprehensive Guide to Understanding and Using NVSM Devices* was made available to the public shortly after its initial acquisition in May 14, 1997.

36. I have reviewed Exhibit MTK-1031, "Charge-Pump Circuits: Power-Consumption Optimization" by Gaetano Palumbo, Domenico Pappalardo and Maurizio Gaibotti as published in *IEEE Transactions on Circuits on Systems - 1: Fundamental Theory and Applications*, November 2002.

37. Attached hereto as Appendix PALUMBO01 is a true and correct copy of the MARC record for *IEEE Transactions on Circuits on Systems - 1: Fundamental Theory and Applications* as held by the Carnegie Library of Pittsburgh. I secured this record myself from the library's public catalog. The MARC record contained within Appendix PALUMBO01 accurately describes the title, author, publisher, and ISSN number of *IEEE Transactions on Circuits on Systems - 1: Fundamental Theory and Applications.* The 'Lib Has' field of this record visible on page 2 indicates this collection includes the November 2002 edition of *IEEE Transactions on Circuits on Systems - 1: Fundamental Theory and Applications* containing "Charge-Pump Circuits: Power-Consumption Optimization". In comparing Exhibit MTK-1031 to Appendix PALUMBO01, it is my determination that Exhibit MTK-1031 is a true and correct copy of "Charge-Pump Circuits: Power-Consumption Optimization" by Gaetano Palumbo, Domenico Pappalardo and Maurizio Gaibotti as published in *IEEE Transactions on Circuits on Systems - 1: Fundamental Theory and Applications* November 2002.

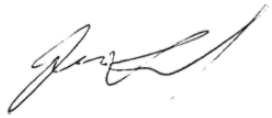
38. The 008 field of the MARC record in Appendix PALUMBO01 indicates the date of record creation. The 008 field of Appendix PALUMBO01 indicates the Carnegie Library of Pittsburgh first acquired this journal as of July 19,

1991 and held the journal in perpetuity until 2003. Considering this information, it is my determination that *IEEE Transactions on Circuits on Systems - 1: Fundamental Theory and Applications* November 2002 and therefore "Charge-Pump Circuits: Power-Consumption Optimization" was made available to the public shortly after its initial release in November 2002.

39. I have been retained on behalf of the Petitioner to provide assistance in the above-illustrated matter in establishing the authenticity and public availability of the documents discussed in this declaration. I am being compensated for my services in this matter at the rate of $200.00 per hour plus reasonable expenses. My statements are objective, and my compensation does not depend on the outcome of this matter.

40. I declare under penalty of perjury that the foregoing is true and correct. I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code.

Dated: 3/22/2024



June Ann Munford

APPENDIX CV

June A. Munford
Curriculum Vitae

Education

University of Wisconsin-Milwaukee - MS, Library & Information Science, 2009
Milwaukee, WI

- Coursework included cataloging, metadata, data analysis, library systems, management strategies and collection development.
- Specialized in library advocacy, cataloging and public administration.

Grand Valley State University - BA, English Language & Literature, 2008
Allendale, MI

- Coursework included linguistics, documentation and literary analysis.
- Minor in political science with a focus in local-level economics and government.

Professional Experience

Researcher / Expert Witness, October 2017 – present
Freelance ● Pittsburgh, Pennsylvania & Grand Rapids, Michigan

- Material authentication and public accessibility determination. Declarations of authenticity and/or public accessibility provided upon research completion. Experienced with appeals and deposition process.

- Research provided on topics of public library operations, material publication history, digital database services and legacy web resources.

- Past clients include Alston & Bird, Arnold & Porter, Baker Botts, Fish & Richardson, Erise IP, Irell & Manella, O'Melveny & Myers, Perkins-Coie, Pillsbury Winthrop Shaw Pittman and Slayden Grubert Beard.

Library Director, February 2013 - March 2015
Dowagiac District Library ● Dowagiac, Michigan

- Executive administrator of the Dowagiac District Library. Located in

Southwest Michigan, this library has a service area of 13,000, an annual operating budget of over $400,000 and total assets of approximately $1,300,000.

● Developed careful budgeting guidelines to produce a 15% surplus during the 2013-2014 & 2014-2015 fiscal years while being audited.

● Using this budget surplus, oversaw significant library investments including the purchase of property for a future building site, demolition of existing buildings and building renovation projects on the current facility.

● Led the organization and digitization of the library's archival records.

● Served as the public representative for the library, developing business relationships with local school, museum and tribal government entities.

● Developed an objective-based analysis system for measuring library services - including a full collection analysis of the library's 50,000+ circulating items and their records.

November 2010 - January 2013
Librarian & Branch Manager, Anchorage Public Library ● Anchorage, Alaska

● Headed the 2013 Anchorage Reads community reading campaign including event planning, staging public performances and creating marketing materials for mass distribution.

● Co-led the social media department of the library's marketing team, drafting social media guidelines, creating original content and instituting long-term planning via content calendars.

● Developed business relationships with The Boys & Girls Club, Anchorage School District and the US Army to establish summer reading programs for children.

June 2004 - September 2005, September 2006 - October 2013
Library Assistant, Hart Area Public Library
Hart, MI

● Responsible for verifying imported MARC records and original MARC

cataloging for the local-level collection as well as the Michigan Electronic Library.

● Handled OCLC Worldcat interlibrary loan requests & fulfillment via ongoing communication with lending libraries.

Professional Involvement

Alaska Library Association - Anchorage Chapter
● Treasurer, 2012

Library Of Michigan
● Level VII Certification, 2008
● Level II Certification, 2013

Michigan Library Association Annual Conference 2014
● New Directors Conference Panel Member

Southwest Michigan Library Cooperative
● Represented the Dowagiac District Library, 2013-2015

Professional Development

Library Of Michigan Beginning Workshop, May 2008
Petoskey, MI
● Received training in cataloging, local history, collection management, children's literacy and reference service.

Public Library Association Intensive Library Management Training, October 2011
Nashville, TN
● Attended a five-day workshop focused on strategic planning, staff management, statistical analysis, collections and cataloging theory.

Alaska Library Association Annual Conference 2012 - Fairbanks, February 2012
Fairbanks, AK
● Attended seminars on EBSCO advanced search methods, budgeting, cataloging, database usage and marketing.

Depositions

2019 ● Fish & Richardson

Apple v. Qualcomm (IPR2018-001281, 39521-00421IP, IPR2018-01282 and 39521-00421IP2)

2019 ● Erise IP

Implicit, LLC v. Netscout Systems, Inc (Civil Action No. 2:18-cv-53-JRG)

2019 ● Perkins-Coie

Adobe Inc. v. RAH Color Technologies LLC (Cases IPR2019-00627, IPR2019-00628, IPR2019-00629 and IPR2019-00646)

2020 ● O'Melveny & Myers

Maxell, Ltd. v. Apple Inc. (Case No. 5:19-cv-00036-RWS)

2021 ● Pillsbury Winthrop Shaw Pittman LLP

Intel v. SRC (IPR2020-1449)

2022 ● Perkins-Coie

Realtek v. Future Link (IPR2021-01182)

2023 ● Fish & Richardson

Neuroderm Ltd. v. Abbvie, Inc (Case No. PGR2022-00040)

2023 ● Fish & Richardson

Nearmap US Inc. v. Pictometry International Corp. (IPR2022-00735)

2023 ● Fish & Richardson

Samsung Electronics v. MemoryWeb LLC (Case No. 39843-0136PS1)

2023 ● Pillsbury Winthrop Shaw Whitman LLP

Gravel Rating Systems v. Costco Wholesale Corp. (Civil Action No. 4:21-cv-149-ALM)

Limited Case and Clientele History

Alston & Bird

- Ericsson

  v. Collision Communications (IPR2022-01233)

- Nokia

  v. Neptune Subsea, Xtera (Case No. 1:17-cv-01876)

- Universal Electronics Inc

  v. Roku Inc (IPR2022-00818)

Arnold & Porter

- Ivantis

  v. Glaukos (Case No. 8:18-cv-00620)

- Samsung

  v. Jawbone (Case No. 2:21-cv-00186)

Benesch Friedlander Coplan & Aronoff

- Voyis

  v. Cathx (Case No. 5:21-cv-00077-RWS)

Erise I.P.

- Apple
  - v. Ericsson Inc. (IPR2022-00715)
  - v. Future Link Systems (IPRs 6317804, 6622108, 6807505, and 7917680)
  - v. INVT (Case No. 20-1881)
  - v. Navblazer LLC (IPR2020-01253)
  - v. Qualcomm (IPR2018-001281, 39521-00421IP, IPR2018-01282, 39521-00421IP2)
  - v. Quest Nettech Corp (Case No. 2:19-cv-00118-JRG)
  - v. Telefonaktiebolaget LM Ericsson (IPR2022-00275)

- Fanduel
  - v. CGT (Case No. 19-1393)

- Garmin
  - v. Phillips North America LLC (Case No. 2:19-cv-6301-AB-KS)

- Netscout
  - v. Longhorn HD LLC (Case No. 2:20-cv-00349)
  - v. Implicit, LLC (Civil Action No. 2:18-cv-53-JRG)

- Sony Interactive Entertainment LLC
  - v. Bot M8 LLC (IPR2020-01288)
  - v. Infernal Technology LLC (Case No. 2:19-CV-00248-JRG)

- Unified Patents

v. GE Video Compression (Civil Action No. 2:19-cv-248)

Fish & Richardson

- Apple

  v. AliveCor (3:21-cv-03958)

  v. LBS Innovations (Case No. 2:19-cv-00119-JRG-RSP)

  v. Koss Corporation (IPR2021-00305)

  v. Masimo (IPR 50095-0012IP1, 50095-0012IP2, 50095-0013IP1, 50095-0013IP2, 50095-0006IP1, 50095-0135IP1)

  v. Neonode (Case No. 21-cv-08872-EMC)

  v. Qualcomm (IPR2018-001281, 39521-00421IP, IPR2018-01282, 39521-00421IP2)

- Dell

  v. Neo Wireless (IPR2022-00616)

- Dish Network

  v. Entropic Communications, LLC (Case No. 2:2023-CV-01043)

  v. Realtime Adaptive Streaming (Case No. 1:17-CV-02097-RBJ)

  v. TQ Delta LLC (Case No. 18-1798)

- Evapco Dry Cooling

  v. SPG Dry Cooling (IPR2021-00688)

- Genetec

  v. Sensormatic Electronics (Case No. 1:20-CV-00760)

- Huawei

v. Bell Northern Research LLC (IPR2019-01174)

- Kianxis

  v. Blue Yonder (Case No. 3:20-cv-03636)

- LG Electronics

  v. Bell Northern Research LLC (Case No. 3:18-cv-2864-CAB-BLM)

- Metaswitch

  v. Sonus Networks (IPR2018-01719)

- Microsoft

  v. Throughputer Inc (IPR2022-00757)

- Mom Enterprises

  v. Ddrops Company (Case No. 1:22-cv-00332)

- MLC Intellectual Property

  v. MicronTech (Case No. 3:14-cv-03657-SI)

- Nearmap Inc

  v. EagleView Technologies (IPR2022-01009)

- Neuroderm Ltd.

  v. Abbvie, Inc (Case No. PGR2022-00040)

- Realtek Semiconductor

v. Future Link (IPR2021-01182)

- Quectel

  v. Koninklijke Philips (Case No. 1:20-cv-01710)

- Samsung

  v. Aire Technology (IPR2022-00877)

  v. Bell Northern Research (Case No. 2:19-cv-00286-JRG)

  v. Communication Technologies Inc (IPR2022-01221)

  v. Jawbone Innovations (IPR2022-00865)

  v. MemoryWeb LLC (IPR2022-00885)

  v. Dodots Licensing Solutions (IPR2023-00701)

  v. Telefonaktiebolaget LM Ericsson (IPR2021-00615)

- Texas Instruments

  v. Vantage Micro LLC (IPR2020-01261)

- Xilinx

  v. Sentient Sensors LCC (Case No. 1:22-cv-00173)

Irell & Manella

- Curium

O'Melveny & Myers

- Apple

  v. Maxell (Case No. 5:19-cv-00036-RWS)

- Micron Technology Inc.

  v. Besang Inc (IPR2023-00900)

- Samsung

  v. Daedalus Prime LLC (1335 Investigation)

Perkins-Coie

- Heru Industries

  v. The UAB Research Foundation (IPR2022-01148)

- Intel Corporation

  v. BitMICRO (Case No. 5:23-cv-00625)

- Realtek Semiconductor

  v. Future Link (IPR2021-01182)

- Twitter Inc

  v. VOIP-Pal.com (Case No. 3:20-cv-02397-JD)

- TCL Industries

  v. Koninklijke Philips NV (IPR2021-00495, IPR2021-00496 and IPR2021-00497)

Pillsbury Winthrop Shaw Pittman

- Intel

  v. FG SRC LLC (Case No. 6:20-cv-00315)

- Gravel Rating Systems

v. Costco (Case No. 4:21-cv-149)
v. Lowe's Home Centers (Case No. 4:21-cv-150)
v. T-Mobile USA (Case No. 4:21-cv-152)
v. Kohl's Inc. (Case No. 4:21-cv-258)
v. Under Armor (Case No. 4:21-cv-356)

Quinn Emanuel

- Wolfspeed

    v. The Trustees of Purdue University (Case No. 1:2021-cv-00840)

Stern, Kessler, Goldstein & Fox

- Preliminary Research

Sheppard, Mullin, Richter & Hampton LLP

- Cadence Design Systems Inc.

    v. Semiconductor Design Technologies (Case No. 3:23-cv-01001)

APPENDIX DIXON1

screenshot-catalog.libraries.psu.edu-2024.01.11-13_34_46
https://catalog.libraries.psu.edu/catalog/1847493
11.01.2024

MARC View

LEADER 01305nam a22003618a 4500				
001 1847493				
003 SIRSI				
005 20151215034147.0				
008 970819t19981998xxu b 000 0 eng d				
010			a|	97011254
019			a|	MARS
020			a|	0195116631
035			a|	LIAS1996241
035			a|	(OCoLC)36510043
040			c|	PSt d| WaOLN d| UtOrBLW
050	0	0	a|	TK7867 b| .S39 1997
100	1		a|	Sedra, Adel S.
245	1	0	a|	Microelectronic circuits / c| Adel S. Sedra, Kenneth C. Smith.
250			a|	4th ed.
264		1	a|	New York : b| Oxford University Press, c| [1998]
264		4	c|	©1998
300			a|	1 volume (various pagings) : b| illustrations ; c| 25 cm + e| 1 computer laser optical disc (4 3/4 in.).
336			a|	text b| txt 2| rdacontent
337			a|	unmediated b| n 2| rdamedia
338			a|	volume b| nc 2| rdacarrier
490	1		a|	Oxford series in electrical engineering
504			a|	Includes bibliographical references and index.
538			a|	System requirements for system disc: Windows, CD-ROM drive.
650		0	a|	Electronic circuits.
650		0	a|	Integrated circuits.
700	1		a|	Smith, Kenneth C. q| (Kenneth Carless)
830		0	a|	Oxford series in electrical engineering.
949			a|	TK7867.S39 1998 w| LC c| 1 i| 000032545241 d| 5/9/2022 e| 1/14/2022 l| STACKS-EG m| UP-ENGIN n| 562 q| 1 r| Y s| Y t| BOOK u| 4/13/2001

PennState

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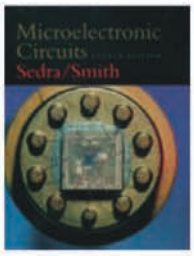

Microelectronic circuits / Adel S. Sedra, Kenneth C. Smith

Author:	Sedra, Adel S.
Published:	New York : Oxford University Press, [1998]
Copyright Date:	©1998
Edition:	4th ed.
Physical Description:	1 volume (various pagings) : illustrations ; 25 cm + 1 computer laser optical disc (4 3/4 in.).
Additional Creators:	Smith, Kenneth C. (Kenneth Carless)

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245 10 Computer systems organization & architecture /|cJohn D.
       Carpinelli.
246 3  Computer systems organization and architecture
260    Boston, MA :|bAddison-Wesley,|cc2001.
300    xxiii, 584 p. :|bill. ;|c24 cm.
500    Includes index.
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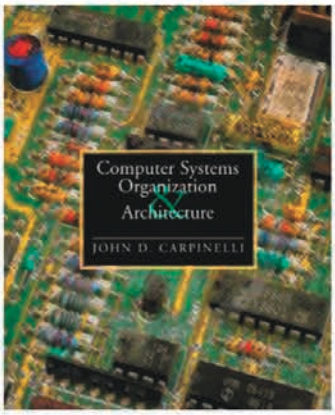

Computer systems organization & architecture / John D. Carpinelli.

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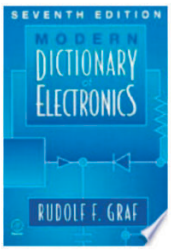

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Author Graf, Rudolf F

Title **Modern dictionary of electronics / Rudolf F. Graf**

Imprint Boston, Mass. : Newnes, 1999

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Edition 7th ed., rev. and updated

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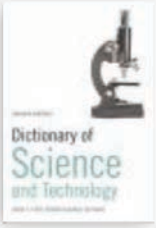

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Language	English
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Date	2003
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Notes	"Bloomsbury reference book"
Summary	"This book contains over 17,000 terms covering all aspects of science and technology, including chemistry, physics, biological and environmental sciences, earth sciences, engineering, astronomy, electronics, telecommunications and computing." "It includes: complex terms explained in clear English; examples showing words used in context; and is ideal for anyone dealing with scientific or technological language."--BOOK JACKET.
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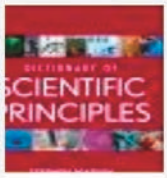

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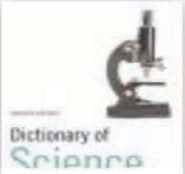

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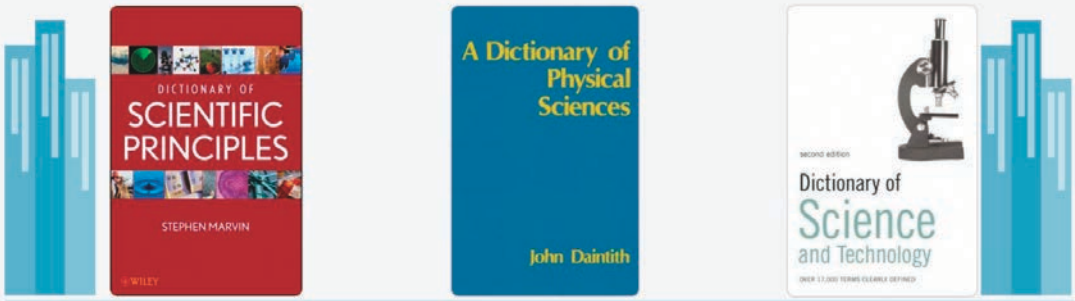

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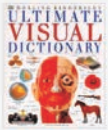

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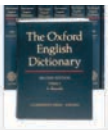

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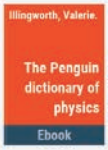

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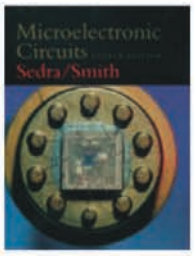

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Contributor	Coles, Carol. >
Language	English
Edition	1st ed.
Publisher	San Francisco : Berrett-Koehler
Date	c2003
Type	Book
Description	xvii, 310 p. ; 25 cm.
Notes	Includes bibliographical references (p. [293]-294) and index.
Subject	Competition > Strategic planning > Industrial management >
Identifier	LC : 2002190856 OCLC : (OCoLC)ocm50670612 ISBN : 1576751678 (alk. paper)

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SUMMARY

Authors Donald Mitchell and Carol Coles conducted a ten-year study of companies that had grown the fastest over a three-year period. Their research reveals that while unsuccessful companies doggedly apply outdated business models, the successful ones improve their models every two to four years.
The Ultimate Competitive Advantage provides a straightforward, systematic method any company (read more)

ABOUT THE AUTHOR

Donald Mitchell

Donald Mitchell is CEO and chairman of Mitchell and Company, a financial and strategy consulting firm working with the most senior officers at hundreds of major companies including Black & Decker, Procter & Gamble, Charles Schwab, Southwest Airlines, Time Warner and Xerox. Carol Coles is COO and president of Mitchell and Company, and co-founded the firm. She has an extensive practice (read more)

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Preface p. xi

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March 4, 2003

Publishers Weekly

It's a challenge to discuss business models compellingly, but management consultants Mitchell and Coles do an impressive job of it here. Their thesis: the best-performing corporations are those that constantly review and update their business models to adapt to changing conditions. Their supporting stories in particular are well-chosen, fleshing out entrepreneurial (read more)

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246     3#$aModern very large scale integration design
246     30$aSystem-on-chip design
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260     ##$aUpper Saddle River, N.J. : $bPrentice Hall PTR, $cc2002.
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	Integrated circuits -- Very large scale integration -- Design and construction >
	System design >
Other title	Modern very large scale integration design
	System-on-chip design
Publisher	Upper Saddle River, N.J. : Prentice Hall PTR
Creation Date	c2002
Edition	3rd ed.
Format	xx, 618 p. ; 24 cm.
Bibliography Note	Includes bibliographical references (p. 599-606) and index.
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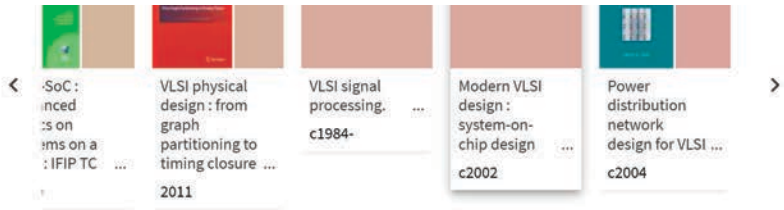
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245 00 IEEE journal of solid-state circuits.
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246 23 IEEE journal of solid state circuits
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300    volumes :|billustrations ;|c28 cm
310    Monthly,|b1991-
321    Quarterly,|b1966-
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Publisher	New York, N.Y. : Institute of Electrical and Electronics Engineers, ©1966-
Description	volumes : illustrations ; 28 cm
Frequency	Monthly, 1991- Quarterly, 1966- Bimonthly, <1986- >
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Issued By	Issued by the IEEE Solid-State Circuits Council.
Subject	Semiconductors -- Periodicals. Integrated circuits -- Periodicals. Solid state electronics -- Periodicals.
Addt'l Author	IEEE Solid-State Circuits Council.
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245	1	0	a| CMOS digital circuit technology / c| Masakazu Shoji.
264		1	a| Englewood Cliffs, N.J. : b| Prentice Hall, c| 1988.
300			a| xiv, 434 pages : b| illustrations ; c| 25 cm
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338			a| volume b| nc 2| rdacarrier
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650		0	a| Metal oxide semiconductors, Complementary.
949			a| TK7871.99.M44S52 1988 w| LC c| 1 i| 000013718459 d| 5/21/2009 e| 5/18/2009 l| CATO-2 m| UP-ANNEX n| 52 q| 1 r| Y s| Y t| BOOK u| 4/13/2001 z| 8160

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Author: Shōji, Masakazu, 1915-

Published: Englewood Cliffs, N.J. : Prentice Hall, 1988.

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Series	IEEE Press series on microelectronic systems >
Publisher	New York : IEEE Press
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Format	xxiii, 589 p. : ill. ; 26 cm.
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Publisher	New York, NY : IEEE, ©1992-
Description	volumes : illustrations ; 28 cm
Frequency	Monthly
Pub Date	Vol. 39, no. 1 (Jan. 1992)-

Location	CLP Main (Oakland) Second Floor - Magazines
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Note	Available also on microfiche and microfilm.
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