DECLARATION OF MARTIN L. KNOTT

I, Martin L. Knott, declare as follows:

1. I am a librarian and Metadata and Linked Data Strategist within the University of Michigan Library's Technical Services Department, located at 913 S. University, Ann Arbor, MI 48109-1190. I hold a Master of Science in Information from the University of Michigan, where I studied library and information science. I joined the University Library in August of 1992. Through the course of my employment and based on the University Library's business records, I have personal knowledge of the University Library's normal business practices, including during the time period starting in the early 2000s. The University Library's normal business practices, which follows standard library practices, are described in further detail below.

2. This Declaration relates to the dates of receipt and public availability of the following:

> **Hodges, D. A. (2003).** ***Analysis and design of digital integrated circuits : in deep submicron technology*** **/ (3rd ed.). Dubuque, Iowa : McGraw-Hill.**

3. <u>*Standard operating procedures for materials at the University of Michigan Libraries*</u>. During the early 2000s and through the present, when a journal volume or other publication was or is received by the University Library, it would be checked in, inscribed with the date of receipt, added to library holdings records, and made available to library patrons or visitors (*i.e.*, the general public) for review, photocopying, and/or

borrowing as soon after its arrival as possible. This procedure normally took a few days or as long as 4 months when there were high numbers of new materials in the workflow.

4. The image attached to this Declaration is a true and accurate copy of ***Analysis and design of digital integrated circuits*** with a University Library date inscription of October 1, 2003 from the University of Michigan Library collection. The date inscription on the page following the title page indicates that ***Analysis and design of digital integrated circuits*** was a replacement copy which was received by the University of Michigan Library Technical Services located in the Harlan Hatcher Graduate Library on October 1, 2003. ***Analysis and design of digital integrated circuits*** was purchased and transported to the Media Union Art, Architecture & Engineering Library, one of several University of Michigan campus libraries, for user access.

5. Based on the information in the attached image, it is clear that ***Analysis and design of digital integrated circuits*** was received by the Art, Architecture & Engineering Library on October 1, 2003, and was catalogued and available to the public within a few days or at most 4 months later, after which the public could search for it by at least subject matter, title, author, year, and/or topic in the University Library's online catalog. The attached image also indicates that ***Analysis and design of digital integrated circuits*** was published in 2003 but has a copyright date of 2004. In my experience as a cataloger, it is not uncommon for a book to be published with a copyright year of the following year. Some of this may be due to the copyright workflow volume in the

Library of Congress Copyright Office but this is primarily due to a choice by the publishers when a book is published in the latter half of the year. By indicating that the copyright is in the following year, the books appear newer in inventory lists. Books with the following year as the copyright date begin to appear in library technical services' workflows during the summer that precedes the year indicated as the copyright date.

I declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code.

Date: April 10, 2019

Martin L. Knott
Metadata and Linked Data Strategist

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Analysis and Design of Digital Integrated Circuits

In Deep Submicron Technology

THIRD EDITION

David A. Hodges
University of California, Berkeley

Horace G. Jackson
University of California, Berkeley

Resve A. Saleh
University of British Columbia

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David A. Hodges is the Daniel M. Tellep Distinguished Professor of Engineering Emeritus at the University of California, Berkeley. He earned the B.E.E. degree at Cornell University and the M.S. and Ph.D. degrees at the University of California, Berkeley. In 1970 he joined the faculty in Electrical Engineering and Computer Sciences at UC Berkeley. Following a year as Chair of the EECS Department, he served as Dean of the College of Engineering from July 1990 through June 1996. Professor Hodges was the winner of the 1997 IEEE Education Medal and the 1999 ASEE Benjamin Garver Lamme Award. He was the founding editor of the *IEEE Transactions on Semiconductor Manufacturing,* and a past editor of the *IEEE Journal of Solid-State Circuits.* Professor Hodges is a Fellow of the IEEE and a Member of the National Academy of Engineering. He is a Director of Silicon Image, Inc. and a former Director of Mentor Graphics.

Horace G. Jackson was born and educated in England. In 1947 he went to Canada, where until 1956 he was engaged in nuclear physics research at the Chalk River Laboratories of Atomic Energy of Canada. In 1956 he joined the Lawrence Berkeley Laboratory of the University of California, where he worked as a Senior Staff Scientist, as well as serving as a Resident Lecturer in the Department of Electrical Engineering and Computer Sciences until his retirement. He has published widely in nuclear science and electronic engineering journals and is coauthor on two books. His research interests are in high frequency analog and high-speed digital integrated circuits.

Resve A. Saleh currently holds the NSERC/PMC-Sierra Chair in the Department of Electrical and Computer Engineering at the University of British Columbia. Dr. Saleh obtained his M.S. and Ph.D. degrees in electrical engineering from the University of California, Berkeley. He received the prestigious Presidential Young Investigator Award in 1990 from the National Science Foundation in the United States. Dr. Saleh has published two books and over 60 journal articles and conference papers. He was an Associate Editor of IEEE Transactions on Computer-Aided Design and technical program chair of the Custom Integrated Circuits Conference. He has also served on numerous program committees. He is a founder and former