UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

MICRON TECHNOLOGY, INC.,
Petitioner

v.

YANGTZE MEMORY TECHNOLOGIES COMPANY, LTD.,
Patent Owner

Case No.: IPR2024-00790
U.S. Patent No. 10,868,031
Issue Date: December 15, 2020

Title: MULTIPLE-STACK THREE-DIMENSIONAL MEMORY DEVICE AND FABRICATION METHOD THEREOF

PETITIONER'S UPDATED EXHIBIT LIST

LISTING OF EXHIBITS

Exhibit	Description
1001	U.S. Patent No. 10,868,031 to Liu et al.
1002	File History of U.S. Patent No. 10,868,031 to Liu et al.
1003	Declaration of Dr. Sanjay Banerjee ("Banerjee")
1004	*Curriculum Vitae* of Dr. Sanjay Banerjee
1005	U.S. Patent No. 10,381,369 to Kim et al. ("Kim")
1006	U.S. Patent Publ. No. 2019/0081061 A1 to Tessariol et al. ("Tessariol")
1007	Aritome, "NAND Flash Memory Technologies" (2016)
1008	Silvagni, "3D NAND Flash Based on Planar Cells," Computers 2017
1009	Micheloni et al., "Inside Solid State Drives (SSDs)" (2nd Ed. 2018)
1010	U.S. Patent No. 10,475,737 to Tang et al.
1011	U.S. Patent No. 10,559,583 to Park et al.
1012	U.S. Patent No. 9,768,186 to Shimabukuro et al.
1013	U.S. Patent No. 9,728,499 to Shimabukuro et al.
1014	U.S. Patent No. 9,299,641 to Sekar et al.
1015	U.S. Patent No. 11,244,725 to Goda et al.
1016	U.S. Patent No. 9,142,302 to Dong et al.
1017	Minute Order, Dkt. No. 42, dated February 22, 2024 in *Yangtze Memory Technologies Company, Ltd. v. Micron Technology, Inc. and Micron Consumer Products Group, LLC*, Case No. 3:23-cv-05792-RFL

Exhibit	Description
1018	Combined Civil and Criminal Federal Court Management Statistics (December 31, 2023), available at https://www.uscourts.gov/sites/default/files/data_tables/fcms_na_distcomparison1231.2023.pdf
1019	Docket from *Yangtze Memory Technologies Company, Ltd. v. Micron Technology, Inc. and Micron Consumer Products Group, LLC*, Case No. 3:23-cv-05792-RFL
1020	Micheloni, "3D Flash Memories" (2016)
1021	Top Seiko Co., Ltd., Guide to Materials
1022	Sharpe, W.N., et al., "Mechanical Properties of MEMS Materials," DARPA, AFRL-IF-RS-TR-2004-76 (March 2004)
1023	Wolf. S., et al., "Silicon Processing for the VLSI Era, Volume 1 – Process Technology, Chapter 15, Multilevel Interconnects for ULSI," (2nd Ed. 2000)
1024	Declaration of Jared Bobrow in Support of Petitioner's Motion for Admission *Pro Hac Vice*
1025	Reply Declaration of Dr. Sanjay Banerjee ("Banerjee-Reply")
1026	Transcript of the deposition of Dr. Kelin Kuhn, dated March 31, 2025, in IPR2024-00790, concerning U.S. Patent No. 10,868,031
1027	Wolf. S., et al., "Silicon Processing for the VLSI Era, Volume 1 – Process Technology," (1st Ed. 1986)
1028	Transcript of the deposition of Dr. Kelin Kuhn, dated March 28, 2025, in IPR2024-00789, concerning U.S. Patent No. 10,861,872
1029	U.S. Patent Publ. No. 2017/0148805 to Nishikawa et al. ("Nishikawa")
1030	U.S. Patent Publ. No. 2017/0236746 to Yu et al. ("Yu")
1031	Petitioner's Hearing Demonstratives

Respectfully submitted,

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Dated: July 28, 2025

/s/ Jeremy Jason Lang

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CERTIFICATE OF SERVICE

The undersigned hereby confirms that the foregoing Petitioner's Reply and associated exhibits were caused to be served on July 28, 2025 via electronic mail, upon the following attorneys of record for the Patent Owner:

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