UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

APPLE INC.,
Petitioner

v.

THETA IP, LLC.,
Patent Owner

Case No. IPR2024-00818
Patent No. 10,524,202

PETITIONER'S UPDATED EXHIBIT LIST

APPENDIX OF EXHIBITS

Exhibit 1001	U.S. Patent No. 10,524,202 (the "'202 Patent")
Exhibit 1002	File History for U.S. Patent 10,524,202 (the "*'202 File History*")
Exhibit 1003	Asad Abidi Declaration ("*Dec.*")
Exhibit 1004	Farbod Behbahani et al., *Adaptive Analog IF Signal Processor for a Wide-Band CMOS Wireless Receiver*, 36 IEEE Journal of Solid-State Circuits 1205, (Aug. 2001) ("*Behbahani*")
Exhibit 1005	Farbod Behbahani et al., *A 2.4-GHz Low-IF Receiver for Wideband WLAN in 0.6μm CMOS – Architecture and Front-End*, 35 IEEE Journal of Solid-State Circuits 1908, (Dec. 2000) ("*Leete*")
Exhibit 1006	Farbod Behbahani et al., *A Broad-Band Tunable CMOS Channel-Select Filter for a Low-IF Wireless Receiver*, 35 IEEE Journal of Solid-State Circuits 476, (April 2000) ("*Tan*")
Exhibit 1007	Theta IP, LLC's Response Claim Construction Br., Dec. 08, 2023 *(Theta Markman Br.)*
Exhibit 1008	Joint Claim Construction Statement, Jan. 18, 2024 *(Joint Markman Statement)*
Exhibit 1009	Theta IP, LLC's Sur-Reply Claim Construction Br., Jan. 16, 2024 *(Theta Markman Br. Sur-Reply)*
Exhibit 1010	*June A. Munford Declaration ("Munford Dec.")*
Exhibit 1011	Y. Tsividis et al*., Internally Varying Analog Circuit Minimize Power Dissipation,* IEEE Circuits & Devices Magazine, Jan. 2003**.**
Exhibit 1012	Mihai Banu & Yannis Tsividis, *Fully Integrated Active RC Filters in Technology*, 18 IEEE Journal of Solid-State Circuits 644, (Dec. 1983) ("Fully Integrated")
Exhibit 1013	Farbod Behbahani et. al, *An Adaptive 2.4GHz Low-IF Receiver in 0.6μm CMOS for Wideband Wireless LAN*, IEEE International Solid-State Circuits Conference, 2000 ("*Kishigami*").
Exhibit 1014	U.S. Patent No. 6,335,952 to Lee et al. ("*Lee*")
Exhibit 1015	Reinhold Ludwig, et. al, *RF Circuit Design,* 2000, *("Ludwig")*
Exhibit 1016	"AN-844 Integrated LNA and Mixer Basics" by Texas Instruments (April 1993) ("*Texas*")
Exhibit 1017	U.S. Patent No. 6,498,927 to Kang et al. ("*Kang*")
Exhibit 1018	*Intentionally left blank*
Exhibit 1019	Rex Miller & Mark R. Miller, *Barron's Electronics The Easy Way* (4th ed.) (2002) ("*Barron")*
Exhibit 1020	*Intentionally left blank*

Exhibit 1021	Thomas Byunghak Cho & Paul R. Gray, *A 10 b, 20 Msample/s, 35 mW Pipeline A/D Converter,* IEEE Journal Of Solid-State Circuits Conference 166 (Mar. 1995*) ("Cho")*
Exhibit 1022	*Intentionally left blank*
Exhibit 1023	Roubik Gregorian & Gabor C. Temes, *Analog MOS Integrated Circuits for Signal Processing* (Robert A. Johnson & George Szentirmai eds., 1986) ("*Gregorian*")
Exhibit 1024	John Pritiskutch & Brett Hanson, *UNDERSTANDING LDMOS DEVICE FUNDAMENTALS,* AN1226 App. Note (July 2000) *("STMicroelectronics")*
Exhibit 1025	*Intentionally left blank*
Exhibit 1026	John G. Proakis & Masoud Salehi, *Communications Systems Engineering,* (1994) ("*Proakis*")
Exhibit 1027	*Intentionally left blank*
Exhibit 1028	The Evolution of Wi-Fi Technology, IEEE Standards Association, (May 16, 2023) ("IEEE")
Exhibit 1029	Kevin Werbach, *The New Wireless Paradigm*, Open Spectrum (October 2002) ("*Werbach*")
Exhibit 1030	Robert G. Fichman et. al, *Information-Rich Commerce At A Crossroads: Business and Technology Adoption Requirements*, (July 2002) ("*Fichman*")
Exhibit 1031	U.S. Patent No. 6,480,700 to Groe et al. ("*Groe*")
Exhibit 1032	*Intentionally left blank*
Exhibit 1033	*The American Heritage Dictionary* (4th ed., 2001) ("*Heritage*")
Exhibit 1034	Rudolf F. Graf, *Modern Dictionary of Electronics* (7th ed., 1999) ("*Graf*")
Exhibit 1035	Arthur D. Spaulding et al, *Optimum Reception in an Impulsive Interference Environment-Part I: Coherent Detection,* COM-25 IEEE Transactions on Communications 910 (Sept. 1997) *(Spaulding")*
Exhibit 1036	*Intentionally left blank*
Exhibit 1037	*Intentionally left blank*
Exhibit 1038	*Intentionally left blank*
Exhibit 1039	*Intentionally left blank*
Exhibit 1040	Paul R. Gray & Robert G. Meyer, *Analysis and Design of Analog Integrated Circuits,* pp. 147-148, John Wiley & Sons, (1997) ("*Gray*")
Exhibit 1041	*Left Intentionally Blank*
Exhibit 1042	Supplemental Declaration of Dr. Asad Abidi
Exhibit 1043	Transcript of Deposition of Dr. Lawrence E. Larson
Exhibit 1044	U.S. Patent No. 7,436,251 (the "'251 Patent")

Exhibit 1045	Petitioner's Demonstratives

Respectfully submitted,

BY: */s/ Paul R. Hart*
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COUNSEL FOR PETITIONER

CERTIFICATE OF SERVICE ON PATENT OWNER UNDER 37 C.F.R. § 42.6

Pursuant to 37 C.F.R. §§ 42.6(e) and 42.105(b), the undersigned certifies that on August 20, 2025, a complete and entire copy of this *Petitioner's Updated Exhibit List and Exhibit* was served on Patent Owner via email.

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