

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

TEXAS INSTRUMENTS INC.,

Petitioner,

v.

PARKERVISION, INC.

Patent Owner

U.S. Patent No. 9,118,528

Issue Date: August 25, 2015

Title: METHOD AND SYSTEM FOR DOWN-CONVERTING AN
ELECTROMAGNETIC SIGNAL, AND TRANSFORMS FOR SAME, AND
APERTURE RELATIONSHIPS

Inter Partes Review No. IPR2024-00936

PATENT OWNER'S UPDATED EXHIBIT LIST

Exhibit No.	Description
2006	U.S. Patent No. 6,061,551
2007	Joint Claim Construction Statement, ParkerVision, Inc. v. Texas Instruments Inc., 6:23-cv-00384, Dkt. 50 (April 29, 2024)
2008	RESERVED
2009	UA741 datasheet
2010	Boeing Report dated April 29, 1998
2011	ParkerVision schematic dated January 20, 1998
2012	Eddie chip layout dated March 6, 1997
2013	ParkerVision circuit board layout
2014	ParkerVision schematic dated August 21, 1997
2015	Conception/Reduction to practice chart
2016	Declaration of David Sorrells
2017	RESERVED
2018	RESERVED
2019	ParkerVision Announces Breakthrough in Wireless Radio Frequency Technology
2020	Laboratory Tests Verify ParkerVision Wireless Technology
2021	Universal Direct Conversion Receiver Evaluation ASIC Contract between ParkerVision and The Boeing Company
2022	Notes from Boeing Tests
2023	RESERVED
2024	Declaration of Dr. David Ricketts
2025	Excerpts from Howe, R. et al., Microelectronics: An Integrated Approach
2026	RESERVED
2027	Development Agreement between ParkerVision and TriQuint Semiconductor dated March 4, 1997
2028	Default Protective Order
2029	Spreadsheet showing ParkerVision files
2030	Claim Construction Order dated 2/20/13 – No. 3:11-cv-00719 (MDFL)
2031	Excerpt of Graf, R., Modern Dictionary of Electronics, Seventh Edition
2032	Rofougaran, A., et al., A 1 GHz CMOS RF Front-End IC for a Direct Conversion Wireless Receiver

2033	RESERVED
2034	Deposition Transcript of Ian Galton, dated March 12, 2025
2035	Sedra, Microelectronic Circuits (4 th ed. 1998)
2036	ParkerVision's Disclosure of Additional Final Infringement Contentions, dated March 31, 2025.
2037	CMOS Analog Circuit Design
2038	Full table of contents for Howe, R. et al., Microelectronics: An Integrated Approach
2039	Full table of contents for Sedra, Microelectronic Circuits (4th ed. 1998)
2040	Full table of contents for CMOS Analog Circuit Design
2041	Declaration of Taylor Lepore
2042	Deposition Transcript of Ian Galton, dated July 16, 2025
2043	Deposition Transcript of James Dickens, dated July 18, 2025
2044	Exhibit 3 of Ex.-2043, Figure 7-3 from Ex.-2010
2045	Patent Owner's Demonstratives

Dated: September 3, 2025

Respectfully submitted,

By: /Jason S. Charkow/

Jason S. Charkow (USPTO Reg. No. 46,418)*

Richard Juang (USPTO Reg. No. 71,478)*

Chandran B. Iyer (USPTO Reg. No. 48,434)

Stephanie Mandir (USPTO Reg. No. 72,930)

Ronald M Daignault*

jason.s.charkow@gmail.com

richard.juang@gmail.com

cbiyer@daignaultiyer.com

smandir@daignaultiyer.com

rdaignault@daignaultiyer.com

DAIGNAULT IYER LLP

8229 Boone Boulevard

Suite 450

Vienna, VA 22182

**Not admitted in Virginia*

Attorneys for ParkerVision, Inc.

CERTIFICATE OF SERVICE

The undersigned hereby certifies that the foregoing PATENT OWNER'S
UPDATED EXHIBIT LIST was served electronically via email on September 3,
2025, on the following counsel of record for Petitioners:

Joshua A. Griswold
Rishi Gupta
Yao Wang
Patrick J. Bisenius
W. Karl Renner
Fish & Richardson P.C.
IPR12189-0092IP1@fr.com
PTABInbound@fr.com
griswold@fr.com
rgupta@fr.com
ywang@fr.com
bisenius@fr.com
renner@fr.com

Robert S. Magee
Blue Peak Law Group LLP
robert@bluepeak.law

John J. Patti
Patti Pershern, PLLC
john@mppfirm.com

Christopher Crumbley
Michael Chibib
Christopher Mierzejewski
Bracewell LLP
kit.crumbley@bracewell.com
michael.chibib@bracewell.com
christopher.mierzejewski@bracewell.com
anna.hearn-darst@bracewell.com

Dated: September 3, 2025

Respectfully Submitted,

By: / Jason S. Charkow /
Jason S. Charkow
USPTO Reg. No. 46,418