

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

TEXAS INSTRUMENTS INC.,
and NXP USA, INC.,
Petitioners

v.

PARKERVISION, INC.,
Patent Owner

Case IPR2024-00936¹
Patent 9,118,528

PETITIONER’S UPDATED EXHIBIT LIST

¹ NXP USA, Inc., who filed a petition in IPR2024-01068, is joined as petitioner in this proceeding.

UPDATED EXHIBIT LIST

Exhibit No.	Description
TI-1001	U.S. Patent No. 9,118,528 (“the ’528 patent”)
TI-1002	Excerpts from the Prosecution History of the ’528 Patent (“the Prosecution History”)
TI-1003	Declaration and Curriculum Vitae of Dr. Ian Galton
TI-1004	U.S. Patent No. 6,230,000 (“Tayloe”)
TI-1005	SN74CBT3253 Dual 1-of-4 FET Multiplexer/Demultiplexer Datasheet
TI-1006	Reserved
TI-1007	Reserved
TI-1008	Complaint, <i>ParkerVision, Inc. v. Texas Instruments Incorporated</i> , 6:23-cv-00384, W.D. Tex., filed May 18, 2023
TI-1009	ParkerVision, Inc.’s Proposed Claim Constructions, <i>ParkerVision, Inc. v. Texas Instruments Incorporated</i> , 6:23-cv-00384, W.D. Tex., filed February 14, 2024
TI-1010	ParkerVision Inc.’s Responsive Claim Construction Brief, <i>ParkerVision, Inc. v. Texas Instruments Incorporated</i> , 6:23-cv-00384, W.D. Tex., filed March 27, 2024
TI-1011	Claim Chart For Infringement of U.S. Patent No. 7,865,177 by TI Chips, <i>ParkerVision, Inc. v. Texas Instruments Incorporated</i> , 6:23-cv-00384, W.D. Tex., submitted December 6, 2023
TI-1012	Reserved
TI-1013	Reserved
TI-1014	Declaration of June Ann Munford
TI-1015	Declaration of Rickea McLeod

TI-1016	Reserved
TI-1017	Razavi, <i>A 900-MHz CMOS Direct Conversion Receiver</i> , IEEE Symposium on VLSI Circuits, Kyoto, Japan, 1997, pp. 113-114, doi: 10.1109/VLSIC.1997.623833.
TI-1018	Declaration of Maureen Honeycutt
TI-1019	Ziemer Tranter, <i>Principles of Communications, Systems, Modulation, and Noise</i> , Fourth Edition, Published 1995
TI-1020	Thomas H. Lee, <i>The Design of CMOS Radio-Frequency Integrated Circuits</i> , Cambridge University Press, 1998 (Excerpts)
TI-1021	U.S. Patent No. US 7,065,327 (“MacNally”)
TI-1022	U.S. Provisional Patent Application 60/099,723 (“MacNally-Provisional”), filed September 10, 1998
TI-1023	International Publication No. WO 00/16492 (“Macnally”)
TI-1024	Reserved
TI-1025	Reserved
TI-1026	Reserved
TI-1027	D. Scott Langford et al, “A BiCMOS Analog Front-end Circuit for an FDM-Based ADSL System,” IEEE Journal of Solid-State Circuits, Vol. 33, no. 9, September 1998, 1383-1393
TI-1028-1040	Reserved
TI-1041	Stipulation
TI-1042	Special Master’s Report and Recommendation Regarding Claim Construction, <i>ParkerVision, Inc. v. Texas Instruments Incorporated</i> , 6:23-cv-00384, W.D. Tex., October 21, 2024
TI-1043	U.S. Patent Application Serial Number 09/521,879, filed March 9, 2000 (“the ’879 application”)
TI-1044	Reserved

TI-1045	D. E. Johnson, J. L. Hilburn, and J. R. Johnson, Basic Electric Circuit Analysis, 7th Edition, Prentice-Hall, 1978.
TI-1046	Adel S. Sedra, Kenneth C. Smith, Microelectronics Circuits, 4th Edition, Oxford University Press, 1997.
TI-1047	D. Tayloe, <i>Norcal NC2030 Low power, High Performance 20m or 30m DC Transceiver</i> , User Manual, December 18, 2005. (https://www.norcalqrp.org/files/NC2030/NC2030_v5.pdf).
TI-1048	CBT (5-V) and CBTLV (3.3-V) Bus Switches, Texas Instruments, August, 1998 (https://www.ti.com/lit/ug/scdd001b/scdd001b.pdf)
TI-1049	Logic Selection Guide, Texas Instruments, August, 1998 (https://web.archive.org/web/20160118225253/https://my.ece.msstate.edu/faculty/reese/EE4743/data_sheets/sdyu001k.pdf)
TI-1050	Thomas H. Lee, <i>The Design of CMOS Radio-Frequency Integrated Circuits</i> , Cambridge University Press, 1998.
TI-1051	Reserved
TI-1052	B. Gilbert, <i>Advances in BJT techniques for high-performance transceivers</i> , Proceedings of the 23rd European Solid-State Circuits Conference, Southampton, UK, 31-38, 1997.
TI-1053	B. Gilbert, "The MICROMIXER: a highly linear variant of the Gilbert mixer using a bisymmetric Class-AB input stage," in <i>IEEE Journal of Solid-State Circuits</i> , vol. 32, no. 9, pp. 1412-1423, Sept. 1997.
TI-1054	I. Galton and C. Weltin-Wu, "Understanding Phase Error and Jitter: Definitions, Implications, Simulations, and Measurement,"

	<i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , vol. 66, no. 1, pp. 1–19, Jan. 2019.
TI-1055	P. R. Gray and R. G. Meyer, <i>Analysis and Design of Analog Integrated Circuits</i> , Third Edition, John Wiley and Sons, 1993.
TI-1056	D. Knollman, “Designing with op-amps: Single formula technique keeps it simple,” EDN Design Feature Article, 1998
TI-1057	1988 Linear Products Databook, Analog Devices, Inc.
TI-1058	Reimund Wittmann, Werner Schardein, Bedrich J. Hosticka, Gert Burbach, and Juergen Arndt, <i>Trimless High Precision Ratioed Resistors in D/A and A/D Converters</i> , IEEE Journal of Solid-State Circuits, VOL. 30, NO. 8, pp. 935-938, AUGUST 1995
TI-1059	A. Abidi, “Low-Power RF-ICs in Wireless Transceivers,” 1994 IEEE Symposium on Low Power Electronics
TI-1060	Design of Analog-Digital VLSI Circuits for Telecommunications and Signal Processing, J. Franca, Y. Tsividis, ed., 2nd Edition, Prentice-Hall, 1994.
TI-1061	J. C. Rudell, et. al., “A 1.9-GHz Wide-Band IF Double Conversion CMOS Receiver for Cordless Telephone Applications,” IEEE Journal of Solid-State Circuits, vol. 32, no. 12, December, 1997
TI-1062	J.R. Cogdell, Foundations of Electrical Engineering, 1996 (Prentice Hall 2ed)
TI-1063	K.R. Laker, W. M. C. Sansen, “Design of Analog Integrated Circuits and Systems,” First. Ed., McGraw-Hill, 1994 (“Laker”)
TI-1064	P. Chang et al, “A CMOS Channel-Select Filter for a Direct-Conversion Wireless Receiver,” IEEE Journal of Solid-State Circuits, VOL. 32, NO. 5, pp. 722-729, MAY 1997

TI-1065	Deposition Transcript of David Ricketts, dated June 13, 2025
TI-1066	Second Declaration of Dr. Ian Galton
TI-1067	Declaration of Dr. James Dickens
TI-1068	Deposition Transcript of David Sorrells, dated May 23, 2025
TI-1069	Second Declaration of June Munford
TI-1070	Petitioner's Demonstratives

Respectfully submitted,

Date: September 3, 2025

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CERTIFICATE OF SERVICE

Pursuant to 37 CFR § 42.6(e), the undersigned certifies that on September 3, 2025, a complete and entire copy of the Petitioner's Updated Exhibit List and Exhibit 1070 were provided by email to the Patent Owner by serving the correspondence addresses of record as follows:

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