

**IN THE UNITED STATES DISTRICT COURT
FOR THE EASTERN DISTRICT OF TEXAS
MARSHALL DIVISION**

OPTRONIC SCIENCES LLC,

Plaintiff,

v.

BOE TECHNOLOGY GROUP CO.,
LTD.,

Defendant.

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No. 2:23-CV-00549-JRG

PRELIMINARY CONSTRUCTIONS

ID	Disputed Term	Preliminary Construction
A	“substantially L-shaped structure” (’842 Patent, Claims 3, 7)	Plain and ordinary meaning
B	“the second portion extending above the light guiding plate” (’842 Patent, Claim 7)	
C	“disposed above” (’509 Patent, Claim 1); “disposed at a side of” (’509 Patent, Claim 10)	
D	“fastener” (’842 Patent, Claims 3, 7)	“A feature that attaches or holds something in place”

ID	Disputed Term	Preliminary Construction
E	“A sealant region pattern for a liquid crystal display” (’801 Patent, Claims 9, 10) “An encapsulating structure for a liquid crystal display device” (’914 Patent, Claims 1, 5)	The preambles are limiting
F	“comprising / saw tooth opening” (’801 Patent, Claim 9) “comprising / saw tooth pattern” (’914 Patent, Claim 1)	Plain and ordinary meaning
G	“formed on” (’801 Patent, Claim 9); “directly formed on” (’121 Patent, Claim 1) “the gate oxide layer in the first area is formed directly on the substrate” (’117 Patent, Claim 1)	“formed in a manner directly touching the layer below”
H	“a predetermined material layer formed on the first substrate” (’801 Patent, Claim 9)	Plain and ordinary meaning

ID	Disputed Term	Preliminary Construction
I	“wherein the sealant is adapted to contact the first material layer and the second material layer through the opening, and the sealant has substantially flat contact bottom surfaces with the first and second material layers via the opening” (’914 Patent, Claim 1)	“wherein the sealant is adapted to contact both the first and second layers through the opening and the sealant has substantially flat contact bottom surfaces with both the first and second material layers via the opening”
J	“each pixel” / “each pixel area” (’121 Patent, Claims 1–2)	Plain and ordinary meaning
K	“defining at least a first area for capacitors and a second area for a transistor on the substrate” (’117 Patent, Claim 1) “defining at least a first area for capacitors, a second area for a transistor on the substrate and a third area for an organic light-emitting diode (OLED) on the substrate (’222 Patent, Claim 1)	Plain and ordinary meaning [note: reject “non-overlapping”]
L	“is electrically connected to” (’222 Patent, Claim 1)	“is connected such that electric conduction current can flow to”
M	“auxiliary electrode” (’509 Patent, Claims 1, 5, 10)	“electrode that works in unison with another electrode”