

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

MEDIATEK INC.,
Petitioner,

v.

DAEDALUS PRIME LLC,
Patent Owner.

Case No. IPR2024-01488
Patent No. 10,740,281

PETITIONER'S UPDATED EXHIBIT LIST

Pursuant to 37 C.F.R. § 42.63(e), Petitioner MediaTek Inc. hereby submits a current listing of Petitioner exhibits. Confidential Exhibit 1027 is being filed today.

Exhibit	Description
1001	U.S. Patent Publication No. 2008/0288748 to Sutardja
1002	U.S. Patent Publication No. 2011/0213950 to Mathieson
1003	David A. Patterson et al., Computer Architecture: A Quantitative Approach (2nd ed. 1996) (“Patterson-1”)
1004	U.S. Patent Publication No. 2009/0106494 to Knebel
1005	U.S. Patent Publication No. 2006/0053258 to Liu
1006	U.S. Patent No. 9,442,758 to Sakarda
1007	File History of U.S. Patent No. 10,740,281 (excerpted)
1008	Lucien M. Censier et al., <i>A New Solution to Coherence Problems in Multicache Systems</i> , <i>IEEE Transactions on Computers</i> , Vol. C-27, No. 12 (Dec. 1978)
1009	Homayoon Akhiani, et al., <i>Cache Coherence Verification with TLA+</i> , <i>Compaq Computer Corporation</i> , in FM’99 – Formal Methods (J.M. Wing et al eds., 1999)
1010	David A. Patterson et al., Computer Organization and Design – The Hardware/Software Interface (2nd ed. 1998) (“Patterson-2”)
1011	Enric Herrero, <i>Distributed Cooperative Caching</i> , 2008 International Conference on Parallel Architectures and Compilation Techniques (PACT), Oct. 2008.
1012	Allen B. Tucker, Computer Science Handbook (2nd ed. 2004)
1013	Balaram Sinharoy et al., <i>IBM POWER7 Multicore Server Processor</i> , 50 IBM J. Res. & Dev. 3, Paper 1 (June 2011)

Exhibit	Description
1014	Alexandra Fedorova, et al., <i>Maximizing Power Efficiency with Asymmetric Multicore Systems</i> , 7 ACM Queue 10 (Nov. 20, 2009)
1015	Mahmoud Naghibzadeh, <i>Operating Systems Concepts and Techniques</i> (2005)
1016	Jim Handy, <i>The Cache Memory Book</i> (2nd ed. 1998)
1017	U.S. Patent No. 6,801,984 to Arimilli
1018	U.S. Patent Publication No. 2009/0089510 to Lee
1019	U.S. Patent Publication No. 2009/0309243 to Carmack
1020	Nvidia, <i>Variable SMP – A Multi-Core CPU Architecture for Low Power and High Performance</i> (2011)
1021	Declaration of Murali Annavaram Ph.D.
1022	Curriculum Vitae of Murali Annavaram Ph.D.
1023	Scheduling Order, <i>Daedalus Prime LLC v. Mediatek Inc.</i> , No. 2-24-cv-00235, Dkt. 20 (E.D. Tex. Aug. 8, 2024).
1024	Complaint, <i>Daedalus Prime LLC v. Mediatek Inc.</i> , No. 2-24-cv-00235, Dkt. 1 (E.D. Tex. Aug. 8, 2024).
1025	Plaintiff's Infringement Contentions, <i>Daedalus Prime LLC v. Mediatek Inc.</i> , No. 2-24-cv-00235.
1026	U.S. Patent Publication No. 2008/0288748 to Sutardja
1027	Confidential Settlement Agreement (FILED UNDER SEAL)

Respectfully submitted,

Dated: June 11, 2025

/Cory C. Bell/
Cory C. Bell, Reg. No. 75,096
Lead Counsel for Petitioner
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CERTIFICATE OF SERVICE

The undersigned hereby certifies that a copy of the foregoing **Petitioner's Updated Exhibit List** and **Confidential Exhibit 1027** were served on June 11, 2025, via email directed to counsel of record for Patent Owner at the following:

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