

December 04, 2009 at 09:31:09

JEDEC
Compilation of Tallies
for JC-40 CMOS Digital Logic and
point committees

The following compilation was generated on 12-04-09, one day after expiration date of the ballot(s). These tallies are not deemed final.

Please attend the meeting or read the minutes of the relevant meeting to learn the final decision on any ballot.

Tallies can change in many significant ways, including, but not restricted to, the following common situations:

- votes can be added by the sponsor upon voters' request (even after the ballot is closed);
- voters can send postcards thereby adding comments;
- voters can send postcards and change a "No" vote to a "Yes";
- sponsors may further address comments;
- etc.

The list of ballots contains not only those ballots that the committee voted but also those that were distributed on a "For information and Comments" or "Information only" basis.

Committee Letter Ballot JC-40.3-09-282 Item 104.75

Subject: DDR3 Register (SSTE32882) -- DDR3 1866 Specification

PATENTS: No known patent issues on the sponsor's part

[Click here for NB09282-.pdf](#)

Ballot distributed: 09 Nov 2009, Expired: 03 Dec 2009

Sponsor: Mr. Ramzi Ammar **Silego Technology** [e-mail \(rammar@silego.com\)](mailto:rammar@silego.com)

NON FINAL REPORT/ Attend meeting or read minutes for final results.								
Company / voter	Y*	N*	A*	P*	C*	PC*	DoV*	DoR*
20 companies voted	15	0	5	0	0			
Advanced Micro Devices Inc. Patel	Y						12/02/09	
Cray Inc. Grossmeier			A				11/16/09	
Dell Inc. Berke	Y						12/01/09	
Elpida Memory Incorporated Ono	Y						11/26/09	
Hynix Semiconductor Kim			A				12/02/09	
IBM Corporation Stout	Y						11/20/09	
Inphi Corporation Washkewicz	Y						12/02/09	
Integrated Device Technology Inc. Franklin	Y						12/03/09	
Intel Corporation Tsang	Y						12/03/09	
Kingston Technology Company Inc. Cheng			A				11/23/09	
Legacy Electronics Inc. Mecker	Y						12/01/09	
Micron Technology Inc. Graham	Y						11/30/09	
Netlist Inc. Martinez	Y						12/01/09	
Numonyx B.V. Abdulla			A				11/30/09	
Nvidia Corporation Wagner			A				12/01/09	
NXP Semiconductors Roozeboom	Y						12/01/09	
Samsung Semiconductor Park	Y						12/02/09	
Silego Technology Chung	Y						12/03/09	
Smart Modular Technologies, Inc. Marino	Y						12/02/09	
Texas Instruments Inc. Frank	Y						12/03/09	
* Y = Yes, N = No, A = Abstain, P = Patent(s), C = Comments PC: Postcard, Y = approved response, N = did not approve response, N/A: not available, sponsor and / or voter did not respond, DoV: Date of Vote								

Committee Letter Ballot JC-40.4-09-255 Item 142.12

Subject: Transparent Mode Functionality

PATENTS: No known patent issues on the sponsor's part

[Click here for NB09255-.pdf](#)

Ballot distributed: 22 Oct 2009, Expired: 03 Dec 2009

Sponsor: Mr. Christian Harrieder **Texas Instruments Inc.** [e-mail \(christian.harrieder@ti.com\)](mailto:christian.harrieder@ti.com)

NON FINAL REPORT/ Attend meeting or read minutes for final results.								
Company / voter	Y*	N*	A*	P*	C*	PC*	DoV*	DoR*
19 companies voted	11	0	8	1	1Y			
Advanced Micro Devices Inc. Patel			A				12/03/09	
Agilent Technologies Keller			A				10/27/09	
Cray Inc. Grossmeier	Y						10/27/09	
Elpida Memory Incorporated Ono	Y						11/26/09	
Hynix Semiconductor Kim	Y						12/02/09	
IBM Corporation Stout			A				11/20/09	
Inphi Corporation Washkewicz	Y						12/02/09	
Integrated Device Technology Inc. Franklin	Y						12/02/09	
Intel Corporation Tsang	Y						12/03/09	
Kingston Technology Company Inc. Cheng			A				10/27/09	
Legacy Electronics Inc. Mecker			A				12/01/09	
Micron Technology Inc. Graham	Y				X	N/A	11/30/09	
Netlist Inc. Martinez			A	P			12/01/09	
Nvidia Corporation Wagner			A				12/01/09	
NXP Semiconductors Roozeboom	Y						12/01/09	
Samsung Semiconductor Park	Y						12/02/09	
Silego Technology Chung	Y						12/03/09	
Smart Modular Technologies, Inc. Marino			A				11/30/09	
Texas Instruments Inc. Harrieder	Y						10/26/09	
* Y = Yes, N = No, A = Abstain, P = Patent(s), C = Comments PC: Postcard, Y = approved response, N = did not approve response, N/A: not available, sponsor and / or voter did not respond, DoV: Date of Vote								

Comments from **Micron Technology Inc.**

No where in the ballot is it identified that this pertains to the Memory Buffer except the keywords.

Postcard Response (for above discussion) / if any present:"

PATENT ISSUE:

Netlist may have IP that applies to this ballot.

Committee Letter Ballot JC-40.4-09-297 Item 142.51

Subject: Transparent Mode CA timing

PATENTS: No known patent issues on the sponsor's part

[Click here for NB09297-.pdf](#)

Ballot distributed: 10 Nov 2009, Expired: 03 Dec 2009

Sponsor: Mr. Christian Harrieder **Texas Instruments Inc.** [e-mail \(christian.harrieder@ti.com\)](mailto:christian.harrieder@ti.com)

NON FINAL REPORT/ Attend meeting or read minutes for final results.								
Company / voter	Y*	N*	A*	P*	C*	PC*	DoV*	DoR*
20 companies voted	14	0	6	1	0			
Advanced Micro Devices Inc. Patel			A				12/03/09	
Cray Inc. Grossmeier			A				11/16/09	
Elpida Memory Incorporated Ono	Y						11/26/09	
FormFactor Inc. Gervasi	Y						12/02/09	
Hynix Semiconductor Kim	Y						12/02/09	
IBM Corporation Stout			A				11/20/09	
Inphi Corporation Washkewicz	Y						12/02/09	
Integrated Device Technology Inc. Franklin	Y						12/02/09	
Intel Corporation Tsang	Y						12/03/09	
Kingston Technology Company Inc. Cheng	Y						11/23/09	
Legacy Electronics Inc. Mecker			A				12/01/09	
Micron Technology Inc. Graham	Y						11/30/09	
Netlist Inc. Martinez			A	P			12/01/09	
Nvidia Corporation Wagner			A				12/01/09	
NXP Semiconductors Roozeboom	Y						12/01/09	
Samsung Semiconductor Park	Y						12/02/09	
Silego Technology Chung	Y						12/03/09	
Smart Modular Technologies, Inc. Marino	Y						12/02/09	
Sun Microsystems Inc. Lee	Y						11/13/09	
Texas Instruments Inc. Harrieder	Y						11/19/09	
* Y = Yes, N = No, A = Abstain, P = Patent(s), C = Comments PC: Postcard, Y = approved response, N = did not approve response, N/A: not available, sponsor and / or voter did not respond, DoV: Date of Vote								

PATENT ISSUE:
Netlist may have IP that applies towards this ballot

Committee Letter Ballot JC-40.4-09-299 Item 142.54

Subject: LR-DIMM DDR3 Memory Buffer Pin Description Proposal

PATENTS: No known patent issues on the sponsor's part

[Click here for NB09299-.pdf](#)

Ballot distributed: 10 Nov 2009, Expired: 03 Dec 2009

Sponsor: Mr. John Smolka **Silego Technology** [e-mail \(jsmolka@silego.com\)](mailto:jsmolka@silego.com)

NON FINAL REPORT/ Attend meeting or read minutes for final results.								
Company / voter	Y*	N*	A*	P*	C*	PC*	DoV *	DoR*
21 companies voted	18	0	3	1	5_Y			
Advanced Micro Devices Inc. Patel	Y				X	N/A	12/03/09	
Cray Inc. Grossmeier	Y						11/16/09	
Dell Inc. Berke	Y						12/01/09	
Elpida Memory Incorporated Ono	Y				X	N/A	11/26/09	
FormFactor Inc. Gervasi	Y				X	N/A	12/02/09	
Hynix Semiconductor Kim	Y						12/02/09	
IBM Corporation Stout	Y						11/20/09	
Inphi Corporation Washkewicz	Y				X	N/A	12/02/09	
Integrated Device Technology Inc. Franklin	Y				X	N/A	12/02/09	
Intel Corporation Tsang	Y						12/03/09	
Kingston Technology Company Inc. Cheng	Y						11/23/09	
Legacy Electronics Inc. Mecker			A				12/01/09	
Micron Technology Inc. Graham	Y						11/30/09	
Netlist Inc. Martinez			A	P			12/01/09	
Nvidia Corporation Wagner			A				12/01/09	
NXP Semiconductors Roozeboom	Y						12/01/09	
Samsung Semiconductor Park	Y						12/02/09	
Silego Technology Chung	Y						12/03/09	
Smart Modular Technologies, Inc. Marino	Y						11/30/09	
Sun Microsystems Inc. Lee	Y						11/13/09	
Texas Instruments Inc. Harrieder	Y						11/30/09	
* Y = Yes, N = No, A = Abstain, P = Patent(s), C = Comments PC: Postcard, Y = approved response, N = did not approve response, N/A: not available, sponsor and / or voter did not respond, DoV: Date of								

Comments from **Advanced Micro Devices Inc.**

TG has unanimously agreed to remove QAECC[9,6,2] pins from the buffer and hence should be removed from this ballot. Postcard Response (for above discussion) / if any present:"

Comments from **Elpida Memory Incorporated**

How should the QAECC2,6,9 be treated ? Changing to NC or RFU ? Postcard Response (for above discussion) / if any present:"

Comments from **FormFactor Inc.**

ERROUT_n should include statement about being an open drain output and that a pull-up is required on the main board. Postcard Response (for above discussion) / if any present:"

Comments from **Inphi Corporation**

Inphi approves material in red text. Postcard Response (for above discussion) / if any present:"

Comments from **Integrated Device Technology Inc.**

QAECC 9, 6, and 2 are shown in this Ballot but in MB TG discussion they should all be renamed RFU as there was no usage model for them. Postcard Response (for above discussion) / if any present:"

PATENT ISSUE:

Netlist may have IP that may apply towards this ballot

Committee Letter Ballot JC-40.4-09-300 Item 142.53

Subject: LR-DIMM DDR3 Memory Buffer Ballout Proposal

PATENTS: No known patent issues on the sponsor's part

[Click here for NB09300-.pdf](#)

Ballot distributed: 10 Nov 2009, Expired: 03 Dec 2009

Sponsor: Mr. John Smolka **Silego Technology** [e-mail \(jsmolka@silego.com\)](mailto:jsmolka@silego.com)

NON FINAL REPORT/ Attend meeting or read minutes for final results.								
Company / voter	Y*	N*	A*	P*	C*	PC*	DoV *	DoR*
20 companies voted	14	0	6	1	2_Y			
Advanced Micro Devices Inc. Patel	Y						12/02/09	
Cray Inc. Grossmeier	Y						11/16/09	
Dell Inc. Berke			A				12/01/09	
Elpida Memory Incorporated Ono	Y				X	N/A	11/26/09	
Hynix Semiconductor Kim	Y						12/02/09	
IBM Corporation Stout	Y						11/20/09	
Inphi Corporation Washkewicz	Y						12/02/09	
Integrated Device Technology Inc. Franklin	Y				X	N/A	12/02/09	
Intel Corporation Tsang	Y						12/03/09	
Kingston Technology Company Inc. Cheng			A				11/23/09	
Legacy Electronics Inc. Mecker			A				12/01/09	
Micron Technology Inc. Graham	Y						11/30/09	
Netlist Inc. Martinez			A	P			12/01/09	
Nvidia Corporation Wagner			A				12/01/09	
NXP Semiconductors Roozeboom	Y						12/01/09	
Samsung Semiconductor Park	Y						12/02/09	
Silego Technology Chung	Y						12/03/09	
Smart Modular Technologies, Inc. Marino			A				11/30/09	
Sun Microsystems Inc. Lee	Y						11/13/09	
Texas Instruments Inc. Harrieder	Y						11/30/09	
* Y = Yes, N = No, A = Abstain, P = Patent(s), C = Comments PC: Postcard, Y = approved response, N = did not approve response, N/A: not available, sponsor and / or voter did not respond, DoV: Date of Vote								

Comments from **Elpida Memory Incorporated**

How should the QAECC2,6,9 be treated ? Changing to NC or RFU ? Postcard Response (for above discussion) / if any present:"

Comments from **Integrated Device Technology Inc.**

QAECC 9, 6, and 2 are shown in this Ballot but in MB TG discussions they should all be renamed RFU as there was no useage model for them Postcard Response (for above discussion) / if any present:"

PATENT ISSUE:

Netlist may have IP that may apply towards this ballot.

Committee Letter Ballot JC-40.4-09-316 Item 142.46

Subject: LRDIMM DDR3 Memory Buffer Host Interface Protocol and Requirements

PATENTS: No known patent issues on the sponsor's part

[Click here for NB09316-.pdf](#)

Ballot distributed: 11 Nov 2009, Expired: 03 Dec 2009

Sponsor: Mr. John Smolka **Silego Technology** [e-mail \(jsmolka@silego.com\)](mailto:jsmolka@silego.com)

NON FINAL REPORT/ Attend meeting or read minutes for final results.								
Company / voter	Y*	N*	A*	P*	C*	PC*	DoV *	DoR*
20 companies voted	15	0	5	1	3_Y			
Advanced Micro Devices Inc. Patel	Y				X	N/A	12/03/09	
Cray Inc. Grossmeier	Y						11/16/09	
Dell Inc. Berke	Y						12/01/09	
Elpida Memory Incorporated Ono	Y						11/26/09	
Hynix Semiconductor Kim	Y						12/02/09	
IBM Corporation Stout	Y						11/20/09	
Inphi Corporation Washkewicz	Y				X	N/A	12/02/09	
Integrated Device Technology Inc. Franklin	Y						12/02/09	
Intel Corporation Tsang	Y						12/03/09	
Kingston Technology Company Inc. Cheng			A				11/23/09	
Legacy Electronics Inc. Mecker			A				12/01/09	
Micron Technology Inc. Graham	Y						11/30/09	
Netlist Inc. Martinez			A	P			12/01/09	
Nvidia Corporation Wagner			A				12/01/09	
NXP Semiconductors Roozeboom	Y						12/01/09	
Samsung Semiconductor Park	Y						12/02/09	
Silego Technology Chung	Y						12/03/09	
Smart Modular Technologies, Inc. Marino			A				12/01/09	
Sun Microsystems Inc. Lee	Y						11/13/09	
Texas Instruments Inc. Harrieder	Y				X	N/A	11/30/09	
* Y = Yes, N = No, A = Abstain, P = Patent(s), C = Comments PC: Postcard, Y = approved response, N = did not approve response, N/A: not available, sponsor and / or voter did not respond, DoV: Date of Vote								

Comments from **Advanced Micro Devices Inc.**
section 4.2.2.3.6.4 should be part of the ballot.

Add a statement which indicates it is not allowed by the buffer to put half the ranks in self refresh mode while other half the ranks on a dimm in power down mode to help reduce the test/validation combinations. Postcard Response (for above discussion) / if any present:"

Comments from **Inphi Corporation**
Inphi approves material in red text Postcard Response (for above discussion) / if any present:"

PATENT ISSUE:
Netlist may have IP that may apply to this ballot

Comments from **Texas Instruments Inc.**
page 7; Table 6, Section "2 Way Rank Multiplication using A[15]", Subsection "Nr. DCS=4",
First Row, Column A16/DCS[2]_n:
is 'x' should be '1'

page 15; Table 10, row NOP
"Command is not passed to the DRAM bus." contradicts chapter 4.2.2.3.9 "The MB will pass a NOP onto the DRAM bus, broadcasting to all sub-ranks." Postcard Response (for above discussion) / if any present:"

Committee Letter Ballot JC-40.4-09-317 Item 142.47

Subject: LRDIMM DDR3 Memory Buffer DRAM Interface Protocol and Requirements

PATENTS: No known patent issues on the sponsor's part

[Click here for NB09317-.pdf](#)

Ballot distributed: 11 Nov 2009, Expired: 03 Dec 2009

Sponsor: Mr. John Smolka **Silego Technology** [e-mail \(jsmolka@silego.com\)](mailto:jsmolka@silego.com)

NON FINAL REPORT/ Attend meeting or read minutes for final results.								
Company / voter	Y*	N*	A*	P*	C*	PC*	DoV *	DoR*
21 companies voted	16	0	5	1	4_Y			
Advanced Micro Devices Inc. Patel	Y				X	N/A	12/03/09	
Cray Inc. Grossmeier	Y						11/16/09	
Dell Inc. Berke	Y						12/01/09	
Elpida Memory Incorporated Ono	Y				X	N/A	11/26/09	
Hynix Semiconductor Kim	Y						12/02/09	
IBM Corporation Stout	Y						11/20/09	
Inphi Corporation Washkewicz	Y						12/02/09	
Inphi Corporation Raghavan	Y						12/02/09	
Integrated Device Technology Inc. Franklin	Y				X	N/A	12/02/09	
Intel Corporation Tsang	Y				X	N/A	12/03/09	
Kingston Technology Company Inc. Cheng			A				11/23/09	
Legacy Electronics Inc. Mecker			A				12/01/09	
Micron Technology Inc. Graham	Y						11/30/09	
Netlist Inc. Martinez			A	P			12/01/09	
Nvidia Corporation Wagner			A				12/01/09	
NXP Semiconductors Roozeboom	Y						12/01/09	
Samsung Semiconductor Park	Y						12/02/09	
Silego Technology Chung	Y						12/03/09	
Smart Modular Technologies, Inc. Marino			A				12/02/09	
Sun Microsystems Inc. Lee	Y						11/13/09	
Texas Instruments Inc. Harrieder	Y						11/30/09	
* Y = Yes, N = No, A = Abstain, P = Patent(s), C = Comments PC: Postcard, Y = approved response, N = did not approve response, N/A: not available, sponsor and / or voter did not respond, DoV: Date of								

Comments from **Advanced Micro Devices Inc.**

TG has unanimously agreed to remove all references related to QAECC[2,6,9] from the buffer spec. This ballot should editorially update it accordingly. Postcard Response (for above discussion) / if any present:"

Comments from **Elpida Memory Incorporated**

How should the QAECC 2,6,9 part be treated ?

Removing the QAECC 2,6,9 part? Postcard Response (for above discussion) / if any present:"

Comments from **Integrated Device Technology Inc.**

QAECC 9, 6, and 2 are shown in this Ballot but in MB TG discussion they should all be renamed RFU as there was no usage model for them. Postcard Response (for above discussion) / if any present:"

Comments from **Intel Corporation**

Editorial changes: Table 2: QAECC should be deleted. The ECC DRAM columns should also be deleted. Page 12, 4.2.2.3.4.3 The Targeted Refresh mode is used the Refresh Control bit .. should be The Targeted Refresh mode is used if the Refresh Control bit .. Page 14, 4.2.2.3.8 When Broadcast to All Sub-ranks is spiced, & should probably be When Broadcast to All Sub-ranks is specified, & Postcard Response (for above discussion) / if any present:"

PATENT ISSUE:

Netlist may have IP that applies towards this ballot

Committee Letter Ballot JC-40.4-09-318 Item 142.11c

Subject: DDR3 MB Control Word Definition

PATENTS: No known patent issues on the sponsor's part

[Click here for NB09318-.pdf](#)

Ballot distributed: 11 Nov 2009, Expired: 03 Dec 2009

Sponsor: Mr. John Smolka **Silego Technology** [e-mail \(jsmolka@silego.com\)](mailto:jsmolka@silego.com)

Parent ballot: JC-40.4-09-216

NON FINAL REPORT/ Attend meeting or read minutes for final results.								
Company / voter	Y*	N*	A*	P*	C*	PC*	DoV*	DoR*
21 companies voted	14	0	7	1	5_Y 1_A			
Advanced Micro Devices Inc. Patel	Y				X	N/A	12/03/09	
Cray Inc. Grossmeier			A				11/16/09	
Dell Inc. Berke	Y						12/01/09	
Elpida Memory Incorporated Ono	Y				X	N/A	11/26/09	
FormFactor Inc. Gervasi					X	N/A	12/02/09	
Hynix Semiconductor Kim	Y						12/02/09	
IBM Corporation Stout	Y						11/20/09	
Inphi Corporation Washkewicz	Y				X	N/A	12/02/09	
Integrated Device Technology Inc. Franklin	Y						12/02/09	
Intel Corporation Tsang	Y				X	N/A	12/03/09	
Kingston Technology Company Inc. Cheng			A				11/23/09	
Legacy Electronics Inc. Mecker			A				12/01/09	
Micron Technology Inc. Graham	Y						11/30/09	
Netlist Inc. Martinez			A	P			12/01/09	
Nvidia Corporation Wagner			A				12/01/09	
NXP Semiconductors Roozeboom	Y						12/01/09	
Samsung Semiconductor Park	Y						12/02/09	
Silego Technology Chung	Y						12/03/09	
Smart Modular Technologies, Inc. Marino			A				12/01/09	
Sun Microsystems Inc. Lee	Y						11/13/09	
Texas Instruments Inc. Harrieder	Y				X	N/A	11/30/09	
* Y = Yes, N = No, A = Abstain, P = Patent(s), C = Comments								

PC: Postcard, Y = approved response, N = did not approve response, N/A: not available, sponsor and / or voter did not respond, DoV: Date of Vote

Comments from **Advanced Micro Devices Inc.**

- Table 25 should have reverse the mapping as following: 00 - default - 8 rank; 01 - 4 rank; 10 - 2 rank; 11 - 1 rank
 - Footnote 2 for table 18 should be included part of the ballot
 - There are several other editorial changes that has been suggested. Will show the updated draft.Postcard Response (for above discussion) / if any present:"
-

Comments from **Elpida Memory Incorporated**

- 1) Definition of table 40: "Total QODT delay = Y Delay (F[1]RC14)+QODDextended (" should be changed to "Total QODT delay = QODT delay (F[1]RC14) + QODT Dextended"
 - 2) Encoding of table 40: "to Delay Y " should be changed to " to QODT delay "
 - 3) Bottom two row of Table 68, Definition:"Reserved" should be changed to "Vref for MDQ to MB" , and corresponding "Encoding" should be changed from "Reserved to "TBD"Postcard Response (for above discussion) / if any present:"
-

Comments from **FormFactor Inc.**

DCS[n:0]# should be DCS[n:0]_n

Postcard Response (for above discussion) / if any present:"

Comments from **Inphi Corporation**

- Footnote 2 for Table 18 should be included as part of the ballot.

- Table 25 F0RC13 - mapping should be updated as following for bits DA4, DA3.

00b - Default - 8 Ranks; QCS[7:0]_n

01b - 4 Ranks; QCS[3:0]_n

10b - 2 Ranks; QCS[1:0]_n

11b - 1 rank; QCS[0]_n

- Table 27. Footnote for Self Refresh Entry and Self Refresh exit should be included as part of the ballot.Postcard Response (for above discussion) / if any present:"
-

Comments from **Intel Corporation**

As DDR3U is not part of JEDEC standard currently, DDR3U reference should be removed from the following tables:

Table 22 F[0]RC10: Encoding for LRDIMM Operating Speed

Table 23 F[0]RC11: Operating Voltage & Parity Calculation Control WordPostcard Response (for above discussion) / if any present:"

PATENT ISSUE:

Netlist may have IP that apply towards this ballot.

Committee Letter Ballot JC-40.4-09-319 Item 142.09

Subject: LRDIMM DDR3 Memory Power Management Chapter Proposal

PATENTS: No known patent issues on the sponsor's part

[Click here for NB09319-.pdf](#)

Ballot distributed: 11 Nov 2009, Expired: 03 Dec 2009

Sponsor: Mr. John Smolka **Silego Technology** [e-mail \(jsmolka@silego.com\)](mailto:jsmolka@silego.com)

NON FINAL REPORT/ Attend meeting or read minutes for final results.								
Company / voter	Y*	N*	A*	P*	C*	PC*	DoV *	DoR*
21 companies voted	15	0	6	1	3_Y			
Advanced Micro Devices Inc. Patel	Y				X	N/A	12/03/09	
Cray Inc. Grossmeier	Y						11/16/09	
Dell Inc. Berke	Y						12/01/09	
Elpida Memory Incorporated Ono	Y						11/26/09	
Hewlett Packard Company Gharaibeh	Y						11/17/09	
Hynix Semiconductor Kim	Y						12/02/09	
IBM Corporation Stout			A				11/20/09	
Inphi Corporation Washkewicz	Y				X	N/A	12/02/09	
Integrated Device Technology Inc. Franklin	Y						12/02/09	
Intel Corporation Tsang	Y				X	N/A	12/03/09	
Kingston Technology Company Inc. Cheng			A				11/23/09	
Legacy Electronics Inc. Mecker			A				12/01/09	
Micron Technology Inc. Graham	Y						11/30/09	
Netlist Inc. Martinez			A	P			12/01/09	
Nvidia Corporation Wagner			A				12/01/09	
NXP Semiconductors Roozeboom	Y						12/01/09	
Samsung Semiconductor Park	Y						12/02/09	
Silego Technology Chung	Y						12/03/09	
Smart Modular Technologies, Inc. Marino			A				12/01/09	
Sun Microsystems Inc. Lee	Y						11/13/09	
Texas Instruments Inc. Harrieder	Y						11/30/09	
* Y = Yes, N = No, A = Abstain, P = Patent(s), C = Comments PC: Postcard, Y = approved response, N = did not approve response, N/A: not available, sponsor and / or voter did not respond, DoV: Date of								

Comments from **Advanced Micro Devices Inc.**

-- Section 8.1.3 - A note should be added which indicates it is not allowed to put half the ranks of a dimm in self refresh while other remaining half ranks in power down mode to simplify buffer test and verification combination. Postcard Response (for above discussion) / if any present:"

Comments from **Inphi Corporation**

--Section 8.1.3 - A note should be added to indicate that it is not allowed to put half the ranks of LRDIMM in self refresh mode while remaining half ranks of LRDIMM in power down mode.

Postcard Response (for above discussion) / if any present:"

Comments from **Intel Corporation**

Attached is the editorial change (ure: sing correct convention for # signals) that TG agreed Postcard Response (for above discussion) / if any present:"

PATENT ISSUE:

Netlist may have IP that applies towards this ballot

Committee Letter Ballot JC-40.4-09-320 Item 142.35

Subject: LRDIMM DDR3 Memory Initialization Chapter Proposal

PATENTS: No known patent issues on the sponsor's part

[Click here for NB09320-.pdf](#)

Ballot distributed: 11 Nov 2009, Expired: 03 Dec 2009

Sponsor: Mr. John Smolka **Silego Technology** [e-mail \(jsmolka@silego.com\)](mailto:jsmolka@silego.com)

NON FINAL REPORT/ Attend meeting or read minutes for final results.								
Company / voter	Y*	N*	A*	P*	C*	PC*	DoV *	DoR*
20 companies voted	15	0	5	1	1Y			
Advanced Micro Devices Inc. Patel	Y						12/03/09	
Cray Inc. Grossmeier	Y						11/16/09	
Dell Inc. Berke	Y						12/01/09	
Elpida Memory Incorporated Ono	Y						11/26/09	
Hynix Semiconductor Kim	Y						12/02/09	
IBM Corporation Stout			A				11/20/09	
Inphi Corporation Washkewicz	Y						12/02/09	
Integrated Device Technology Inc. Franklin	Y						12/02/09	
Intel Corporation Tsang	Y				X	N/A	12/03/09	
Kingston Technology Company Inc. Cheng	Y						11/23/09	
Legacy Electronics Inc. Mecker			A				12/01/09	
Micron Technology Inc. Graham	Y						11/30/09	
Netlist Inc. Martinez			A	P			12/01/09	
Nvidia Corporation Wagner			A				12/01/09	
NXP Semiconductors Roozeboom	Y						12/01/09	
Samsung Semiconductor Park	Y						12/02/09	
Silego Technology Chung	Y						12/03/09	
Smart Modular Technologies, Inc. Marino			A				12/02/09	
Sun Microsystems Inc. Lee	Y						11/13/09	
Texas Instruments Inc. Harrieder	Y						11/30/09	
* Y = Yes, N = No, A = Abstain, P = Patent(s), C = Comments PC: Postcard, Y = approved response, N = did not approve response, N/A: not available, sponsor and / or voter did not respond, DoV: Date of Vote								

Comments from **Intel Corporation**

Attached is the editorial change (re: using correct convention for # signals) that TG agreed
Postcard Response (for above discussion) / if any present."

PATENT ISSUE:

Netlist may have IP which may apply to this ballot

End of Compilation of Tallies (PDF Format)



December 04, 2009 at 09:31:22