



Committee Letter Ballot
JC-40.4-09-320
Item 142.35
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SPONSOR: Super Micro Computer Inc., represented by John Smolka, johns@supermicro.com,
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DISTRIBUTION: Formal Vote: JC-404
Non voting committees: [JC-40 \(Information and Comments\)](#)

Eng. No.: AS 90577

Item No.: 142.35

SUBJECT: [LRDIMM DDR3 Memory Initialization Chapter Proposal](#)

BACKGROUND: At September 09 JEDEC meeting in San Jose, motion by Intel and seconded by Montage to authorize the TG to issue one or more ballots on the items listed on pages 3 and 4 of the presentation that have item numbers, have been shown in the committee and for which the TG has reached a consensus.

The motion passed by acclamation.

This ballot proposal is to specify LRDIMM DDR3 Memory Initialization, and has been approved by DDR3 MB TG on 11/09/2009.

PATENTS: No known patent issues on the sponsor's part

ATTACHMENT: [Download NB09320-.pdf](#)

KEYWORDS: DDR3, MB, Memory Buffer, LRDIMM, Initialization

[\[Top\]](#)

