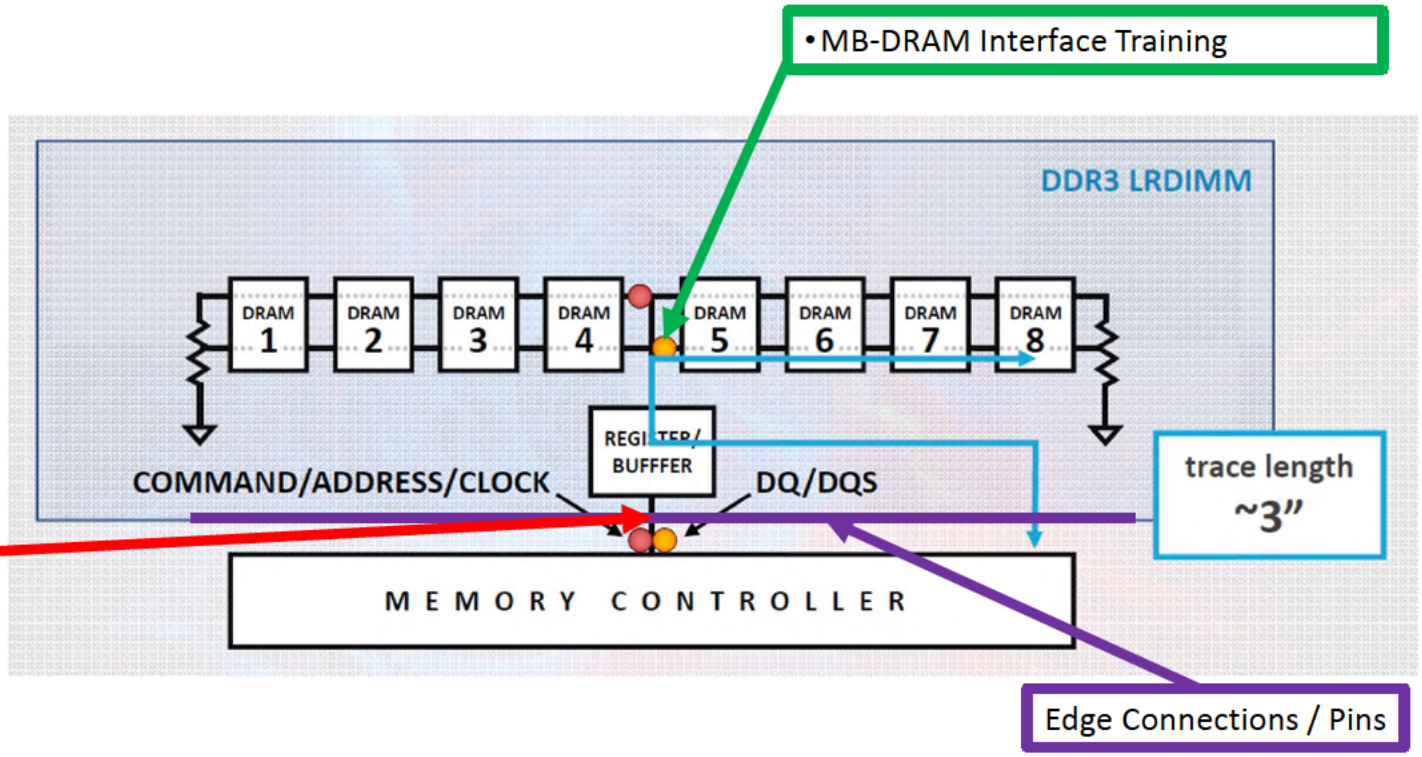

• MB-DRAM Interface Training
DDR3 LRDIMM
DRAM 1
DRAM 2
DRAM 3
DRAM 4
DRAM 5
DRAM 6
DRAM 7
DRAM 8
REGISTER/ BUFFFER
COMMAND/ADDRESS/CLOCK
DQ/DQS
trace length ~3”
MEMORY CONTROLLER
• Host-MB Training
• Host Interface CA Clk-to-CMD Training
• MB Initialization
Edge Connections / Pins