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Transcript of Thomas Herman

Date: January 17, 2025

Case: Certain Semiconductor Devices and Products Containing the Same (337-TA-1414)

Planet Depos

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Michigan #8598 | Nevada #089F | New Mexico #566

Infineon Exhibit 2003 Innoscience v. Infineon IPR2025-00094

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UNITED STATES INTERNATIONAL TRADE COMMISSION

Washington, D.C.

- - - - - x

In the Matter of :
CERTAIN SEMICONDUCTOR : Inv. No. 337-TA-1414
DEVICES AND PRODUCTS :
CONTAINING THE SAME :

- - - - - X

[REDACTED]

[REDACTED]

Videotaped Deposition of THOMAS HERMAN

Conducted Virtually

Friday, January 17, 2025

12:01 p.m. EST

Job No.: 564412

Pages 1 - 148

Reported by: Debra A. Whitehead

1 Videotaped Deposition of THOMAS HERMAN,
2 conducted virtually.

3
4
5 Pursuant to Subpoena, before Debra Ann
6 Whitehead, E-Notary Public in and for the
7 Commonwealth of Virginia.

A P P E A R A N C E S

ON BEHALF OF THE INFINEON COMPLAINANTS:

JASON W. BALICH, ESQUIRE

WOLF GREENFIELD & SACKS, PC

600 Atlantic Avenue, 23rd Floor

Boston, Massachusetts 02210

(617) 646-8000

ON BEHALF OF THE INNOSCIENCE RESPONDENTS:

LUKE H. MacDONALD, Ph.D., ESQUIRE

DEANNA C. SMILEY, ESQUIRE

LIONEL M. LAVENUE, ESQUIRE

FINNEGAN, HENDERSON, FARABOW,

GARRETT & DUNNER LLP

1875 Explorer Street, Suite 800

Reston, Virginia 20190-6023

(571) 203-2700

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A P P E A R A N C E S C O N T I N U E D

ON BEHALF OF THE WITNESS:

JOSEPH D. GRAY, ESQUIRE

SLAYDEN GRUBERT BEARD PLLC

401 Congress Avenue, Suite 1650

Austin, Texas 78701

(512) 402-3561

ALSO PRESENT:

PEYTON ABRAMS, Video Specialist

DANIEL ESPARZA, A/V Technician

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By Mr. Balich	145

E X H I B I T S

(Attached to the Transcript)

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Exhibit 2 LinkedIn Profile, Tom Herman	22
Exhibit 3 Copy of Color Photograph Bates HERMANITC 00001	37
Exhibit 4 2/20/06 Email from Mr. Herman to Mr. Weiner, et al., Bates HERMANITC 00008	48
Exhibit 5 Patent Invention Disclosure Bates HERMANITC 00002 - 00003	52

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HERMAN DEPOSITION EXHIBIT PAGE

Exhibit 6 Merged Cascode Transistor 55

(III-nitride, Compound, or
Silicon Semiconductor) for Low
Gate Charge, Fast Switching, and
High Voltage,

Bates HERMANITC 00004 - 00007

Exhibit 7 Provisional Application for 81

Patent, Bates HERMANITC
00010 - 00011

Exhibit 8 Merged Cascode Transistor 84

(III-Nitride, Compound, or
Silicon Semiconductor) for Low
Gate Charge, Fast Switching, and
High Voltage, Bates

HERMANITC 00012 - 00015

Exhibit 9 3/30/06 Email from Mr. Weiner to 87

Mr. Wenskay, Bates

HERMANITC 00009

Exhibit 10 U.S. Patent No. 8,264,003, Bates 93

INFINEON-1414-0000195 - 0000204

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Exhibit 11 Annotated, U.S. Patent No. 8,264,003, Bates INFINEON-1414-0000195 - 0000204	106
Exhibit 12 U.S. Patent Application Publication No. 2006/0175633, Bates Innoscience 1515-00006249 - 00006256	109
Exhibit 13 Annotated U.S. Patent Application Publication No. 2006/0175633, Bates Innoscience 1515-00006249 - 00006256	128
Exhibit 14 4/19/07 Letter from Mr. Weiner to Mr. Herman, Bates HERMANITC 00046	136
Exhibit 15 File History, Patent No. 8,264,003, Bates Innoscience 1414-00010362 - 00011144	138

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1	P R O C E E D I N G S	12:01:03
2	VIDEO SPECIALIST: Here begins Media	12:01:03
3	Number 1 in the videotaped deposition of Thomas	12:01:08
4	Herman in the matter of Certain Semiconductor	12:01:11
5	Devices and Products Containing the Same, in the	12:01:14
6	United States International Trade Commission,	12:01:17
7	Washington, DC; Case Number 337-TA-1414.	12:01:20
8	Today's date is January 17, 2025, and the	12:01:29
9	time on the video monitor is 12:01 p.m. eastern	12:01:33
10	time. The remote videographer today is Peyton	12:01:36
11	Abrams, representing Planet Depos, and all parties	12:01:41
12	of this video deposition are attending remotely.	12:01:43
13	Would counsel please voice-identify	12:01:47
14	themselves and state whom they represent, starting	12:01:49
15	with the taking attorney.	12:01:50
16	MR. MacDONALD: Good morning. This is	12:01:51
17	Luke MacDonald from the law firm of Finnegan	12:01:53
18	Henderson on behalf of Respondents Innoscience	12:01:55
19	Suzhou Technology Company Limited and Innoscience	12:02:01
20	America, Inc.	12:02:03
21	Also with me in the room from Finnegan is	12:02:04
22	Deanna Smiley and Lionel Lavenue.	12:02:08

[REDACTED]

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1	MR. BALICH: Jason Balich; Wolf,	12:02:13
2	Greenfield, for Complainants Infineon.	12:02:17
3	MR. GRAY: Joseph Gray of Slayden,	12:02:20
4	Grubert Beard, on behalf of the witness	12:02:23
5	Mr. Herman.	12:02:24
6	VIDEO SPECIALIST: Thank you, Counsels.	12:02:25
7	The court reporter today is Debbie	12:02:28
8	Whitehead, representing Planet Depos.	12:02:28
9	The witness will now be sworn.	12:02:37
10	THOMAS HERMAN,	12:02:37
11	having been duly sworn, testified as follows:	12:02:44
12	EXAMINATION BY COUNSEL FOR THE INNOSCIENCE	12:02:44
13	RESPONDENTS	12:02:44
14	BY MR. MacDONALD:	12:02:44
15	Q Good morning, Mr. Herman.	12:02:46
16	A Good morning.	12:02:48
17	Q I'd like to just start with a few ground	12:02:49
18	rules and preliminary questions for you about the	12:02:53
19	deposition. Have you ever had your deposition	12:02:55
20	taken before?	12:02:57
21	A I have.	12:02:59
22	Q When was that?	12:03:00

[REDACTED]

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1	A	Yes.	12:33:51
2	Q	Did you design semiconductors with	12:33:53
3		enhancement mode gates while you were at IR?	12:33:55
4	A	The bulk of the effort at IR was	12:34:01
5		depletion mode gates and transistors in GaN.	12:34:08
6	Q	Did you also design enhancement mode	12:34:14
7		transistors in GaN?	12:34:21
8	A	There was interest in designing	12:34:27
9		enhancement mode GaN transistors, but that was	12:34:33
10		deemed a more difficult task. And I don't believe	12:34:38
11		there was any active effort to specifically	12:34:47
12		fabricate enhancement mode GaN transistors, other	12:34:51
13		than, you know, feasibility and discussions. But	12:34:56
14		I don't believe there was any effort to physically	12:35:02
15		fabricate an enhancement mode GaN device.	12:35:05
16	Q	Thank you.	12:35:10
17		Did you engage in discussions regarding	12:35:12
18		feasibility and enhancement mode GaN transistors	12:35:15
19		while you were at IR from 2004 to 2006?	12:35:18
20		MR. GRAY: Objection. Form.	12:35:24
21	A	I don't recall if I did or not.	12:35:26
22	Q	At IR did you work on the design of	12:35:32