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September 29, 2025

VIA EMAIL

ATTN: Production Manager
O'Brien & Levine Court Reporting Solutions
68 Commercial Wharf
Boston, MA 02110

Re: Errata Submission of Dr. Joseph Bernstein for Deposition, dated August 27, 2025
Innoscence America, Inc. v. Infineon Technologies America Corp., IPR2025-00094

On behalf of Luke MacDonald, attached please find the executed errata sheet and signature by Dr. Joseph Bernstein in the above-referenced proceeding for your record.

I have also copied Patent Owner's counsel to ensure that they have a copy of this for their record.

Please let us know if you have any questions.

Sincerely,

/s/ Daniel E. Doku

Daniel E. Doku
Senior Litigation Paralegal

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E R R A T A
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Page:line	Error	Correction	Reason
95:14	capacitor	capacity	Mistranscription
96:20	onset	ON-state	Mistranscription
100:12	first (unintelligible)	first region	Mistranscription
108:6	completion mode	depletion mode	Mistranscription
114:19	This are words.	These are words.	Mistranscription
119:18	integrating into transistor	insulated gate transistor	Mistranscription
121:3	POSITA would know how to	POSITA would know: how to	Punctuation
132:3	capacitings	capacitance	Mistranscription
132:7	farming	harming	Mistranscription
139:10	another	any	Mistranscription
145:15	configurative	configured	Mistranscription
166:14	couldn't	could have	Mistranscription
166:20	losing	using	Mistranscription

SIGNATURE



DATE 27.Sept.2025