

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

INNOSCIENCE AMERICA, INC.,
Petitioner,

v.

INFINEON TECHNOLOGIES AMERICAS CORP.,
Patent Owner.

Case No. IPR2025-00094
Patent No. 8,264,003

NOTICE OF FILING PATENT OWNER'S DEMONSTRATIVE EXHIBITS

LISTING OF EXHIBITS

Exhibit	Description
2001	Stewart, C.M., “Interim Processes for PTAB Workload Management,” Memorandum dated March 26, 2025
2002	Horowitz, P., & Hill, W. (1989). The Art of Electronics (2nd ed.). Cambridge University Press.
2003	Transcript Excerpt from Deposition of Thomas Herman dated January 17, 2025. <i>In the matter of Certain Semiconductor Devices and Products Containing the Same</i> ; ITC Inv. No. 337-TA-1414.
2004	Declaration of David K. Choi Ph.D.
2005	CV of David K. Choi Ph.D.
2006	Mil’Shtein, S., et al., Heterostructure transistor with tunable gate bias, Microelectronics Journal 36 (2005) 301-303.
2007	Preliminary Cree CGH40025F Datasheet, May 2007.
2008	Jorgenson, Dale W., et al. eds., Measuring and Sustaining the New Economy: Report of a Workshop (National Academy Press, 2002).
2009	Deposition of Joseph B. Bernstein (August 27, 2025).
2010	Errata to Deposition of Joseph B. Bernstein (September 27, 2025).
2011	Patent Owner’s Demonstrative Exhibits

Pursuant to 37 CFR § 42.6(e)(4)(ii), and the Board's Order Setting Oral Argument (Paper 19), Patent Owner herewith submits the following Exhibit:

<u>Exhibit</u>	<u>Description</u>
2011	Patent Owner's Demonstrative Exhibits

Respectfully submitted,
Infineon Technologies Americas Corp.

Date: April 3, 2026

/Jason W. Balich/

Scott A. McKeown, Reg. No. 42,866
Jason W. Balich, Reg. No. 67,110
WOLF, GREENFIELD & SACKS, P.C.

CERTIFICATE OF SERVICE UNDER 37 C.F.R. § 42.6 (E)(4)

I certify that on April 3, 2026, a copy of the foregoing document, including any exhibits filed therewith, is being served via electronic mail, as previously consented to by Petitioner, upon the following:

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