

**EXHIBIT J-1 (2024-10-02) – YANGTZE MEMORY TECHNOLOGIES COMPANY, LTD.
HIGHLY CONFIDENTIAL – ATTORNEYS’ EYES ONLY**

Claim Language	Micron’s DRAM Accused Products¹
	As seen in the annotated image, the DRAM Accused Products comprise a plurality of pad groups (green), adjacent and connected to the plurality of ESD buses (light blue).
[1.d] a common ESD bus, adjacent to the plurality of pad groups; and	Micron’s DRAM Accused Products include a common ESD bus, adjacent to the plurality of pad groups. <i>See, e.g.:</i> REDACTED As seen in the annotated image, the DRAM Accused Products comprise a common ESD bus (dark red), adjacent to the plurality of pad groups (green),
[1.e] a plurality of bonding wires configured to connect the plurality of pad groups to the common ESD bus;	Micron’s DRAM Accused Products include a plurality of bonding wires configured to connect the plurality of pad groups to the common ESD bus. <i>See, e.g.:</i> REDACTED

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Claim Language	Micron’s DRAM Accused Products ¹
	REDACTED

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Claim Language	Micron’s DRAM Accused Products¹
	As seen in these annotated images, the DRAM Accused Products comprise a plurality of bonding wires (blue) configured to connect the plurality of pad groups (green) to the common ESD bus (dark red).
[1.f] wherein the circuit area comprises at least one saved area, each of at least one saved area is adjacent to two respective ESD buses of the plurality of ESD buses and respective pads of two respective pad groups of the plurality of pad groups, and	In Micron’s DRAM Accused Products the circuit area comprises at least one saved area, each of which is adjacent to two respective ESD buses of the plurality of ESD buses and respective pads of two respective pad groups of the plurality of pad groups. <i>See, e.g.:</i> REDACTED As seen in these annotated images, in the DRAM Accused Products, the circuit area comprises at least one saved area (pink), each of which is adjacent to two respective ESD buses (light blue) of the plurality of ESD buses and respective pads of two respective pad groups (green) of the plurality of pad groups
[1.g] there is no ESD bus and no pad in the at least one saved area.	In Micron’s DRAM Accused Products there is no ESD bus and no pad in the at least one saved area. <i>See, e.g.:</i>

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Claim Language	Micron’s DRAM Accused Products ¹
	As seen in these annotated images cited in Claim [1.b], the DRAM Accused Products comprise a plurality of ESD buses (shown in a light blue arrow) which are connected into ground pads (shown in green arrows).
[6.c] a plurality of pad groups, adjacent and connected to the plurality of ESD buses; and	Micron’s DRAM Accused Products include a plurality of ESD buses. <i>See, e.g.:</i> <i>See Claim [1.c].</i> As seen in these annotated images cited in Claim [1.b], the DRAM Accused Products comprise a plurality of pad groups (green), adjacent and connected to the plurality of ESD buses (light blue).
[6.d] a plurality of bonding wires configured to connect one to another of the plurality of pad groups;	Micron’s DRAM Accused Products include a plurality of bonding wires configured to connect the plurality of pad groups. <i>See, e.g.:</i> <i>See Claim [1.e].</i> As seen in these annotated images cited in [1.e], the DRAM Accused Products comprise a plurality of bonding wires (blue) configured to connect the plurality of pad groups (green).
[6.e] wherein the circuit area comprises at least one saved area, each of at least one saved area is adjacent to two respective ESD buses of the plurality of ESD buses and respective pads of two respective pad groups of the	In Micron’s DRAM Accused Products the circuit area comprises at least one saved area, each of which is adjacent to two respective ESD buses of the plurality of ESD buses and respective pads of two respective pad groups of the plurality of pad groups. <i>See, e.g.:</i> <i>See Claim [1.f].</i> As seen in these annotated images cited in Claim [1.f], in the DRAM Accused Products, the circuit area comprises at least one saved area (pink), each of which is adjacent to two respective ESD buses of the plurality of ESD buses and respective pads of two respective pad groups of the plurality of pad groups.