

Exhibit C: Claim Chart for U.S. Patent 10,873,753

U.S. Patent No. 10,873,753

This claim chart is based on publicly available literature, the source code¹ Defendants have made available, and the documents currently produced by Defendants (AMD-CV-0000001–AMD-CV-00005452). Plaintiffs Concurrent Ventures, LLC and XstreamEdge, Inc. (“Plaintiffs” or “CV”) reserve the right to supplement and/or amend their positions based on discovery from Defendants Advanced Micro Devices, Inc. and Pensando Systems, Inc. (“AMD Pensando” or “Defendants”), including technical documentation such as schematics and datasheets, and source code. Plaintiffs reserve the right to amend these contentions for good cause with leave of the Court.

Defendants infringed and continue to infringe U.S. Patent No. 10,873,753 (the “’753 patent”) by having made, used, tested, sold or offered for sale, and/or imported into the United States their Distributed Services Cards (“DSCs”) and Data Processing Units (“DPUs”) including at least Elba, Capri, Giglio, and Salina (including AMD DSC2-25G, AMD DSC2-100G, and AMD DSC2-200), as well as their DPU and DSC technology when implemented in other products, services, and offerings, for example the SmartSwitch or AMD Pensando Pollara 400² (collectively, the “Accused Products,” “Accused DPUs,” or “Accused DSCs”). For an example, Defendants directly infringe when they themselves implement, use, or test the Accused Products in the claimed manner discussed herein.

Additionally, Defendants also indirectly infringe the ’753 patent by having induced and/or contributed to third parties’ infringement, including the infringement of Defendants’ customers. In particular, Defendants, through their advertising, marketing, communications with customers and clients, troubleshooting, guidance, testing, setting of default settings, and helping, actively and knowingly aids and abets their customers’ and clients’ infringement, including their use of the Accused Products. Defendants also sold or offered to sell into the United States, or imported into the United States, their Accused Products, which are material components of the infringing implementation and not staple articles of commerce capable of substantial noninfringing use, as shown, for example, at least by Defendants’ own descriptions, guidance, and instructions to implement the Accused Products. Defendants have known of the patents, their infringement, and that the Accused Products are especially made for use in a manner that would infringe the patent when they sold, offered to sell, and/or imported the Accused Products. Further details are provided herein.

¹ Due to the parties’ ongoing negotiations regarding the protective order and the time constraints inherent in the draft procedures for printing source code in the protective order, these contentions incorporate Plaintiffs’ review of the source code but do not explicitly refer to the source code.

² <https://www.tomshardware.com/networking/amd-unveils-industrys-first-ultra-ethernet-ready-network-card-for-ai-and-hpc>, CV-AMD0016365; CV-AMD0017532.

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Theories exemplifying how the Accused Products infringe the asserted claims of the '753 patent are detailed herein. The products included are representative; on information and belief, all Accused Products function in substantially the same manner as one or more of the examples provided, and include generally the same components. For example, for purposes of infringement of the '753 patent, Defendants' Elba, Capri, Giglio, and Salina versions operate, in relevant part, in the same manner, and thus infringe in substantially the same way. *See* AMD-CV-0002195–AMD-CV-0002196; AMD-CV-0004986–AMD-CV-0005004 at AMD-CV-0004988; AMD-CV-0003945–AMD-CV-0004184 at AMD-CV-0003962; AMD-CV-0002195; AMD-CV-0004986; Defendants' Response to Interrogatory Nos. 1, 2. The following documents provide additional support for Plaintiffs' infringement theories documented herein: AMD-CV-0001962; AMD-CV-0000001; AMD-CV-0000608; AMD-CV-0000643; AMD-CV-0000935; AMD-CV-0000988; AMD-CV-0000994; AMD-CV-0001126; AMD-CV-0001155; AMD-CV-0002797; AMD-CV-0002200; AMD-CV-0004235; AMD-CV-0003945–AMD-CV-0004184; Defendants' Response to Interrogatory Nos. 1, 2. This is further shown by the evidence for the various versions cited herein.

Claim Language	Infringement Contentions and Exemplary Evidence of Infringement
[1pre] A platform for data flow processing, comprising:	To the extent the preamble is limiting, Defendants make, use, test, sell, and offer to sell the claimed platform for data flow processing, including by making, using, testing, selling, and offering to sell the Accused Products; implementing and/or testing those Accused Products in their own platforms; and providing them to customers. <i>See, e.g.</i>, AMD-CV-0003945–AMD-CV-0004184. For example, the AMD Pensando Distributed Services Cards (DSCs) or AMD Pensando Distributed Modules (DSMs) are Data Processing Unit (DPU) enabled systems deployed in Server, Smart Switch, and Appliance platforms for data flow processing and for providing various infrastructure services.

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	https://www.servethehome.com/amd-acquires-pensando-for-its-dpu-future-intel-ipu-nvidia-bluefield-marvell-octeon-fungible/, CV-AMD0016148.
[1e] (continued) and supporting messaging-based communication using packets each	The Accused Products comprise a network coupled to the one or more swappable pods or cards and supporting messaging-based communication using packets each having a header with a chassis identifier, a board identifier, a module identifier, an instance identifier, and a type identifier, so that each type of module,

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having a header with a chassis identifier, a board identifier, a module identifier, an instance identifier, and a type identifier, so that each type of module, each instance of a type of module, and each module on each board in each chassis can be addressed through the header.	each instance of a type of module, and each module on each board in each chassis can be addressed through the header. For example, based upon information and belief, the network is coupled to the AMD Pensando DSCs and supports a messaging-based communication using packets each having a header with a chassis identifier, a board identifier, a module identifier, an instance identifier, and a type identifier, so that each type of module, each instance of a type of module, and each module on each board in each chassis can be addressed through the header. Solutions Brief, Guardicore Integration with Pensando Distributed Services Cards (2021), CV-AMD0016400.

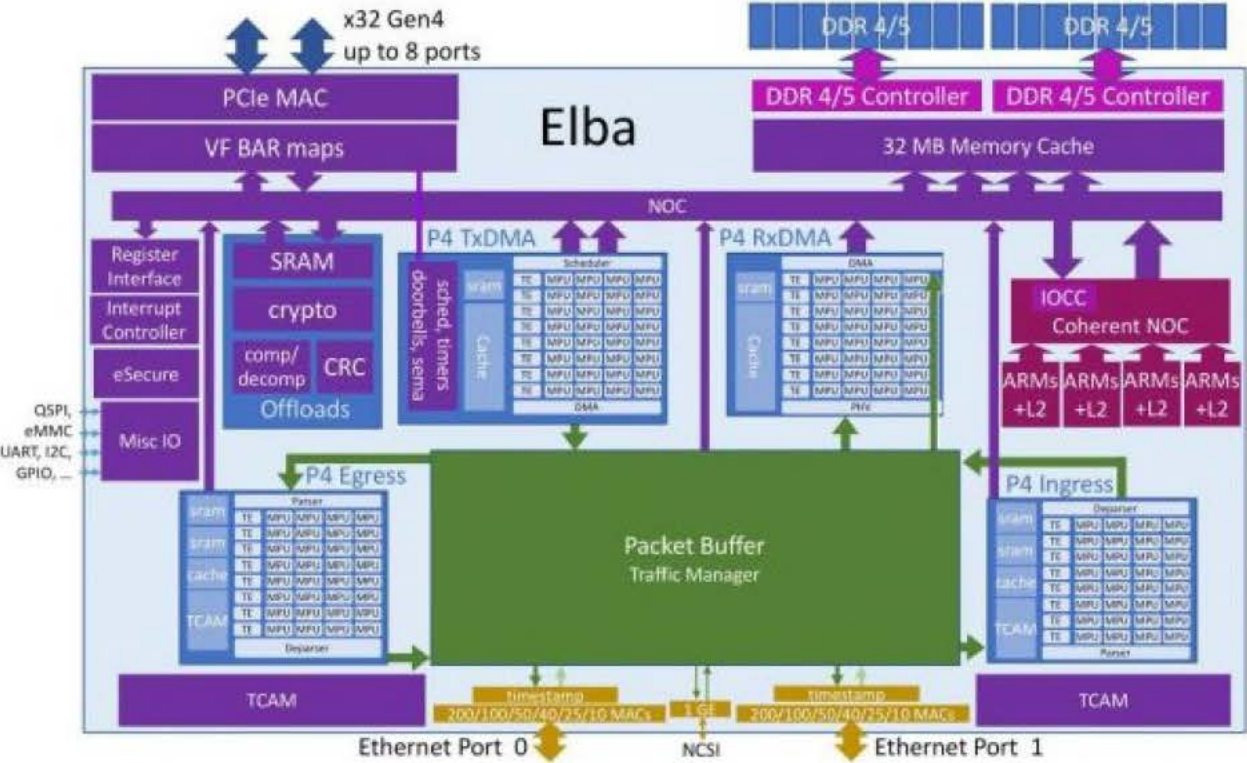
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	Table: Packet 1 - Redirect Action - Test Packet Header Information <table><tr><th>Receiving Port</th><th>Destination MAC</th><th>Destination IP</th><th>Destination Port</th><th>Source IP</th><th>Source Port</th><th>Protocol</th></tr><tr><td>Eth 1/2</td><td>00:00:00:00:10:02</td><td>1.2.3.4</td><td>456</td><td>2.3.4.5</td><td>345</td><td>UDP</td></tr></table> https://docs.amd.com/r/en-US/ug1669-amd-pensando-ssdk-hello-world-user-guide/Redirect-P4-Action, CV-AMD0016403. Additionally, upon information and belief, AMD Pensando DSCs utilize Link Layer Discovery Protocol (LLDP) to discover and maintain information about devices connected to the network, and provide and support the claimed messaging-based communication. Using LLDP, device information such as chassis identification, port ID, port description, system name and description, device capability (as router, switch, hub...), IP/MAC address, etc., are transmitted to the neighboring devices. This information is also stored in local Management Information Databases (MIBs), and can be queried with the Simple Network Management Protocol (SNMP). The LLDP-enabled devices have an LLDP agent installed in them, which sends out advertisements from all physical interfaces either periodically or as changes occur. https://www.whatsupgold.com/network-discovery/link-layer-discovery-protocol-lldp, CV-AMD0016369.	Receiving Port	Destination MAC	Destination IP	Destination Port	Source IP	Source Port	Protocol	Eth 1/2	00:00:00:00:10:02	1.2.3.4	456	2.3.4.5	345	UDP
Receiving Port	Destination MAC	Destination IP	Destination Port	Source IP	Source Port	Protocol									
Eth 1/2	00:00:00:00:10:02	1.2.3.4	456	2.3.4.5	345	UDP									

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[4] The platform of claim 1, further comprising: at least one of the one or more swappable pods or cards having a plurality of types of memory and memory superposition with selectable arrangement of the plurality of types of memory as multilevel cache, interleaved memory, swapped memory over a specified address range, and memory allocated to specified address ranges according to	The Accused Products include at least one of the one or more swappable pods or cards having a plurality of types of memory and memory superposition with selectable arrangement of the plurality of types of memory as multilevel cache, interleaved memory, swapped memory over a specified address range, and memory allocated to specified address ranges according to parameterized task needs. The Accused Products support/handle various types of memory to optimize performance (<i>e.g.</i>, various memory types like SRAM, TCAM, DRAM memory cache, DDR4/5). Accordingly, based upon information and belief, the Accused Products include at least one of the one or more swappable pods or cards having a plurality of types of memory and memory superposition with selectable arrangement of the plurality of types of memory as multilevel cache, interleaved memory, swapped memory over a specified address range, and memory allocated to specified address ranges according to parameterized task needs.

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parameterized task needs.	 https://www.servethehome.com/amd-acquires-pensando-for-its-dpu-future-intel-ipu-nvidia-bluefield-marvell-octeon-fungible/, CV-ADM0016148.